SPEC® CINT2006 Result
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Dell Inc.

PowerEdge 1950 III (Intel Xeon X5460, 3.16 GHz)

SPECint®2006 = 29.2

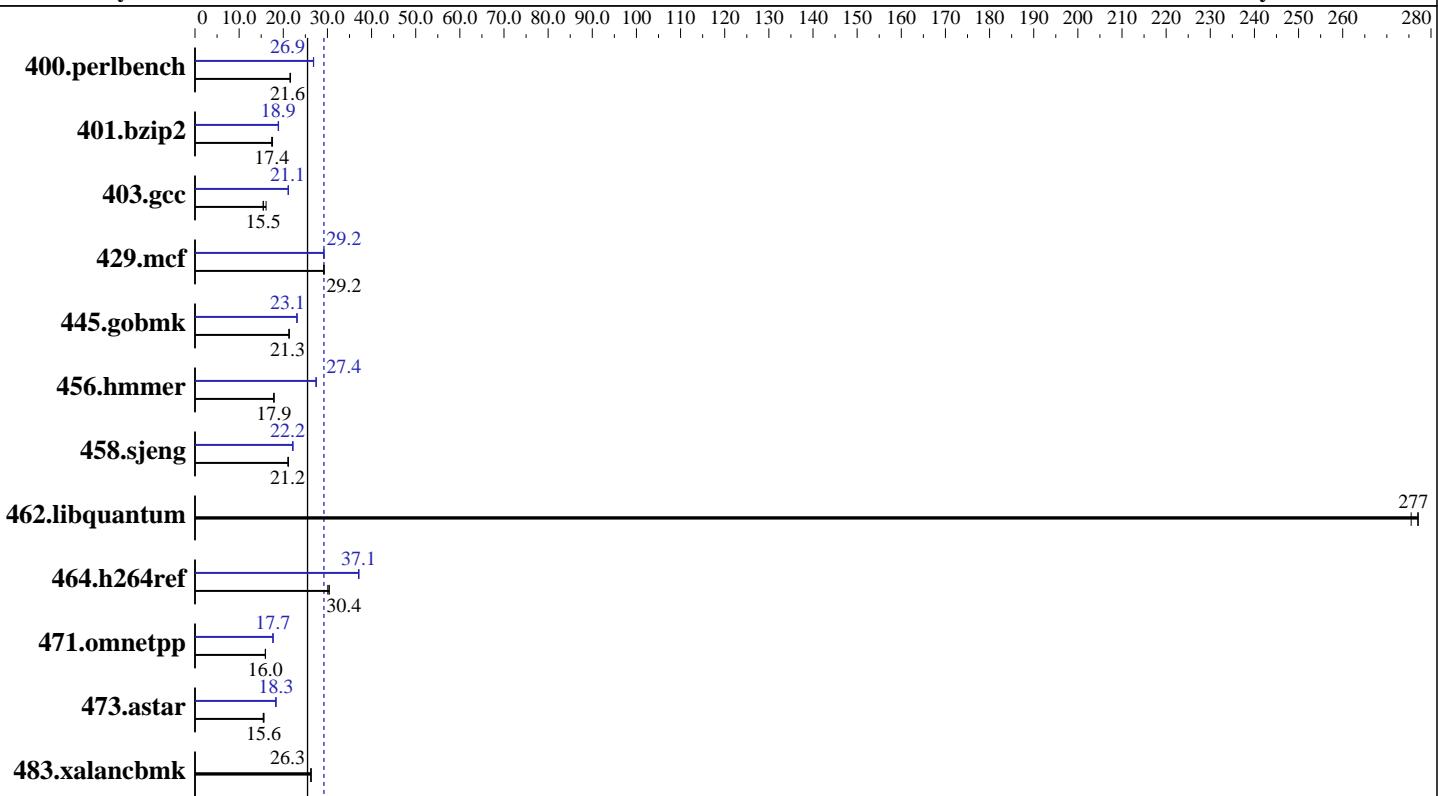SPECint_base2006 = 25.5

CPU2006 license: 55

Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Oct-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008

400.perlbench	26.9 / 21.6
401.bzip2	18.9 / 17.4
403.gcc	21.1 / 15.5
429.mcf	29.2 / 29.2
445.gobmk	23.1 / 21.3
456.hmmer	27.4 / 17.9
458.sjeng	22.2 / 21.2
462.libquantum	277
464.h264ref	37.1 / 30.4
471.omnetpp	17.7 / 16.0
473.astar	18.3 / 15.6
483.xalancbmk	26.3

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Hardware

CPU Name:	Intel Xeon X5460
CPU Characteristics:	
CPU MHz:	3166
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB DDR2-667 FB-DIMM)
Disk Subsystem:	1 x 73 GB 15000 RPM SAS
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Binutils 2.18.50.0.7.20080502 Microquill SmartHeap V8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	451	21.6	454	21.5	**453**	**21.6**	364	26.9	364	26.8	**364**	**26.9**
401.bzip2	549	17.6	554	17.4	**554**	**17.4**	510	18.9	512	18.9	**511**	**18.9**
403.gcc	500	16.1	**520**	**15.5**	521	15.5	381	21.1	381	21.1	**381**	**21.1**
429.mcf	312	29.2	**312**	**29.2**	312	29.2	312	29.2	312	29.2	**312**	**29.2**
445.gobmk	492	21.3	**492**	**21.3**	492	21.3	**454**	**23.1**	454	23.1	454	23.1
456.hmmer	522	17.9	**522**	**17.9**	522	17.9	340	27.5	**340**	**27.4**	340	27.4
458.sjeng	572	21.2	**572**	**21.2**	575	21.0	**546**	**22.2**	545	22.2	547	22.1
462.libquantum	75.2	276	74.8	277	**74.8**	**277**	75.2	276	74.8	277	**74.8**	**277**
464.h264ref	736	30.1	**729**	**30.4**	728	30.4	596	37.1	**596**	**37.1**	597	37.0
471.omnetpp	393	15.9	391	16.0	**391**	**16.0**	**354**	**17.7**	354	17.7	354	17.7
473.astar	454	15.5	**451**	**15.6**	450	15.6	**383**	**18.3**	384	18.3	382	18.4
483.xalancbmk	262	26.3	**262**	**26.3**	263	26.2	262	26.3	**262**	**26.3**	263	26.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"

Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

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Base Optimization Flags

C benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel
-par-runtime-control -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

```
401.bzip2: /opt/intel/Compiler/11.0/042/bin/intel64/icc
           -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
           -I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

```
456.hmmer: /opt/intel/Compiler/11.0/042/bin/intel64/icc
           -L/opt/intel/Compiler/11.0/042/ipp/em64t/lib
           -I/opt/intel/Compiler/11.0/042/ipp/em64t/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
   401.bzip2: -DSPEC_CPU_LP64
   456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

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Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -ansi-alias -opt-prefetch`

401.bzip2: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32 -opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc -opt-malloc-options=3`

429.mcf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

445.gobmk: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -auto-ilp32`

458.sjeng: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4`

462.libquantum: `basepeak = yes`

464.h264ref: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.html
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.html

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You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090713.02.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.02.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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