



# SPEC® CINT2006 Result

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## ACTION S.A.

## SPECint®\_rate2006 = 113

### ACTINA SOLAR 212 X2 (Intel Xeon E5410, 2.33 GHz)

## SPECint\_rate\_base2006 = 105

CPU2006 license: 9008

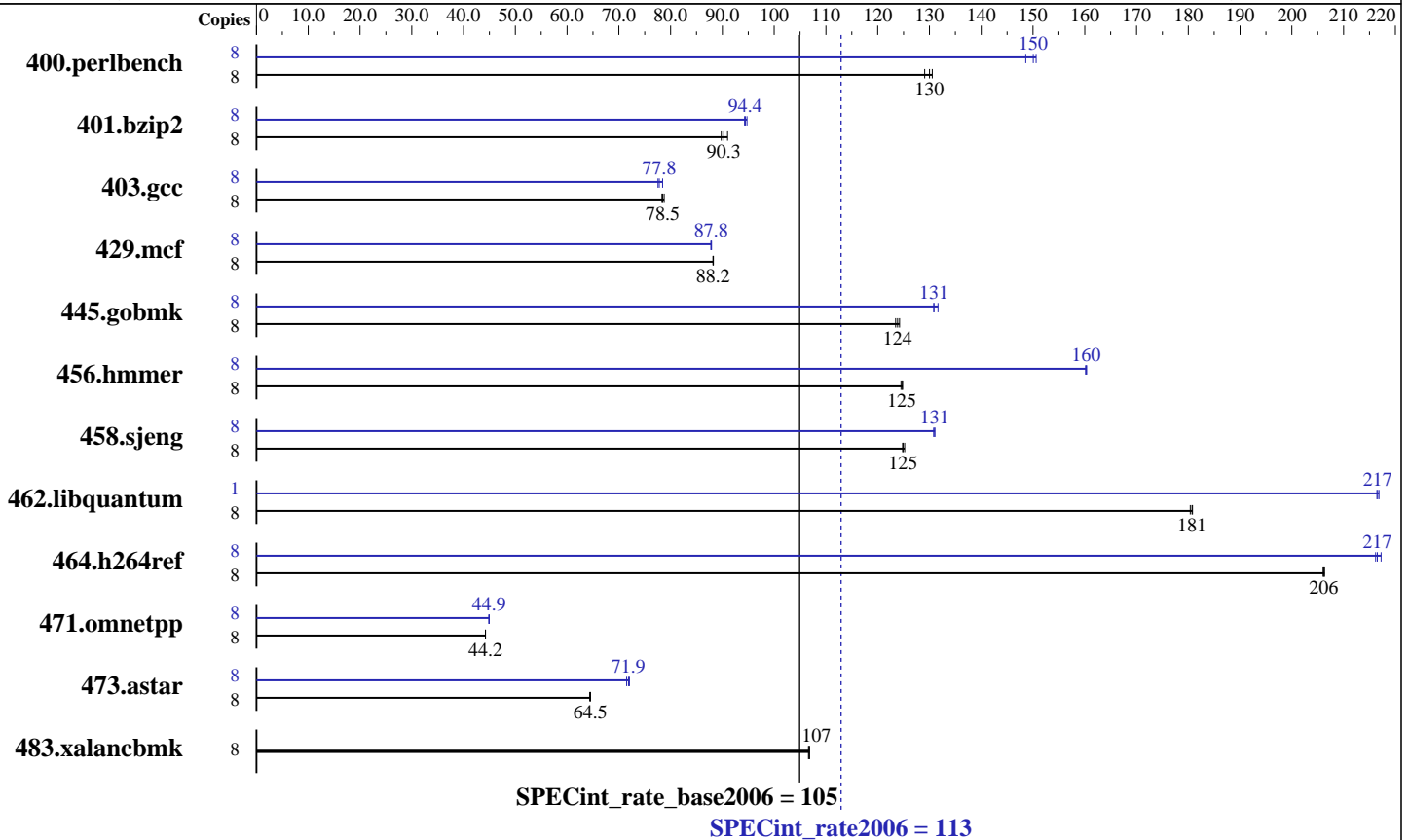
Test sponsor: ACTION S.A.

Tested by: ACTION S.A.

Test date: Dec-2008

Hardware Availability: Sep-2008

Software Availability: Nov-2008



### Hardware

CPU Name: Intel Xeon E5410  
 CPU Characteristics: 1333 MHz System Bus  
 CPU MHz: 2330  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8x 2 GB, PC2-5300, CL 5-5-5, FB ECC)  
 Disk Subsystem: 500 GB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 10 (x86\_64)  
 SP2, kernel 2.6.16.60-0.21-smp  
 Compiler: Intel C++ Compiler 11.0 for Linux  
 Build 20080930 Package ID: l\_cproc\_p\_11.0.066  
 Auto Parallel: Yes  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1  
 Binutils 2.18.50.0.7.20080502



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## Results Table

Benchmark	Base						Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	599	131	<b>601</b>	<b>130</b>	606	129	8	526	149	519	151	<b>521</b>	<b>150</b>
401.bzip2	8	848	91.0	860	89.8	<b>855</b>	<b>90.3</b>	8	815	94.8	<b>817</b>	<b>94.4</b>	819	94.3
403.gcc	8	<b>821</b>	<b>78.5</b>	822	78.3	818	78.8	8	821	78.5	830	77.6	<b>828</b>	<b>77.8</b>
429.mcf	8	827	88.2	827	88.3	<b>827</b>	<b>88.2</b>	8	<b>831</b>	<b>87.8</b>	831	87.8	830	87.9
445.gobmk	8	676	124	<b>678</b>	<b>124</b>	680	123	8	641	131	637	132	<b>641</b>	<b>131</b>
456.hammer	8	<b>599</b>	<b>125</b>	600	125	598	125	8	466	160	<b>466</b>	<b>160</b>	465	160
458.sjeng	8	773	125	<b>775</b>	<b>125</b>	776	125	8	739	131	740	131	<b>739</b>	<b>131</b>
462.libquantum	8	919	180	<b>917</b>	<b>181</b>	917	181	1	95.7	216	<b>95.7</b>	<b>217</b>	95.6	217
464.h264ref	8	<b>859</b>	<b>206</b>	859	206	858	206	8	<b>818</b>	<b>217</b>	815	217	819	216
471.omnetpp	8	1130	44.2	<b>1130</b>	<b>44.2</b>	1131	44.2	8	1112	45.0	<b>1113</b>	<b>44.9</b>	1113	44.9
473.astar	8	871	64.5	<b>871</b>	<b>64.5</b>	872	64.4	8	785	71.5	<b>781</b>	<b>71.9</b>	779	72.0
483.xalancbmk	8	517	107	518	107	<b>517</b>	<b>107</b>	8	517	107	518	107	<b>517</b>	<b>107</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Platform Notes

This result is measured on ACTINA SOLAR 222 X2.  
Note that the ACTINA SOLAR 222 X2 and ACTINA SOLAR 212 X2 are electrically equivalent.

## General Notes

'taskset' was used to bind processes to cores except for 462.libquantum peak  
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
OMP\_NUM\_THREADS set to number of processors  
KMP\_AFFINITY set to "physical,0"  
KMP\_STACKSIZE set to 64M

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch

C++ benchmarks:

-xSSE4.1 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/Compiler/11.0/066/bin/intel64/icc

456.hmmer: /opt/intel/Compiler/11.0/066/bin/intel64/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: -xSSE4.1 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3

429.mcf: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -opt-prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -O2 -ipo  
-no-prec-div -ansi-alias

456.hmmer: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll4

462.libquantum: -xSSE4.1 -ipo -O3 -no-prec-div -static  
-opt-malloc-options=3 -parallel -par-runtime-control  
-opt-prefetch

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=block  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3  
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine  
-Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revA.20090710.01.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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