



SPEC® CFP2006 Result

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Fujitsu

SPECfp®2006 = 38.0

PRIMERGY TX300 S5, Intel Xeon X5550, 2.67 GHz

SPECfp_base2006 = 35.8

CPU2006 license: 19

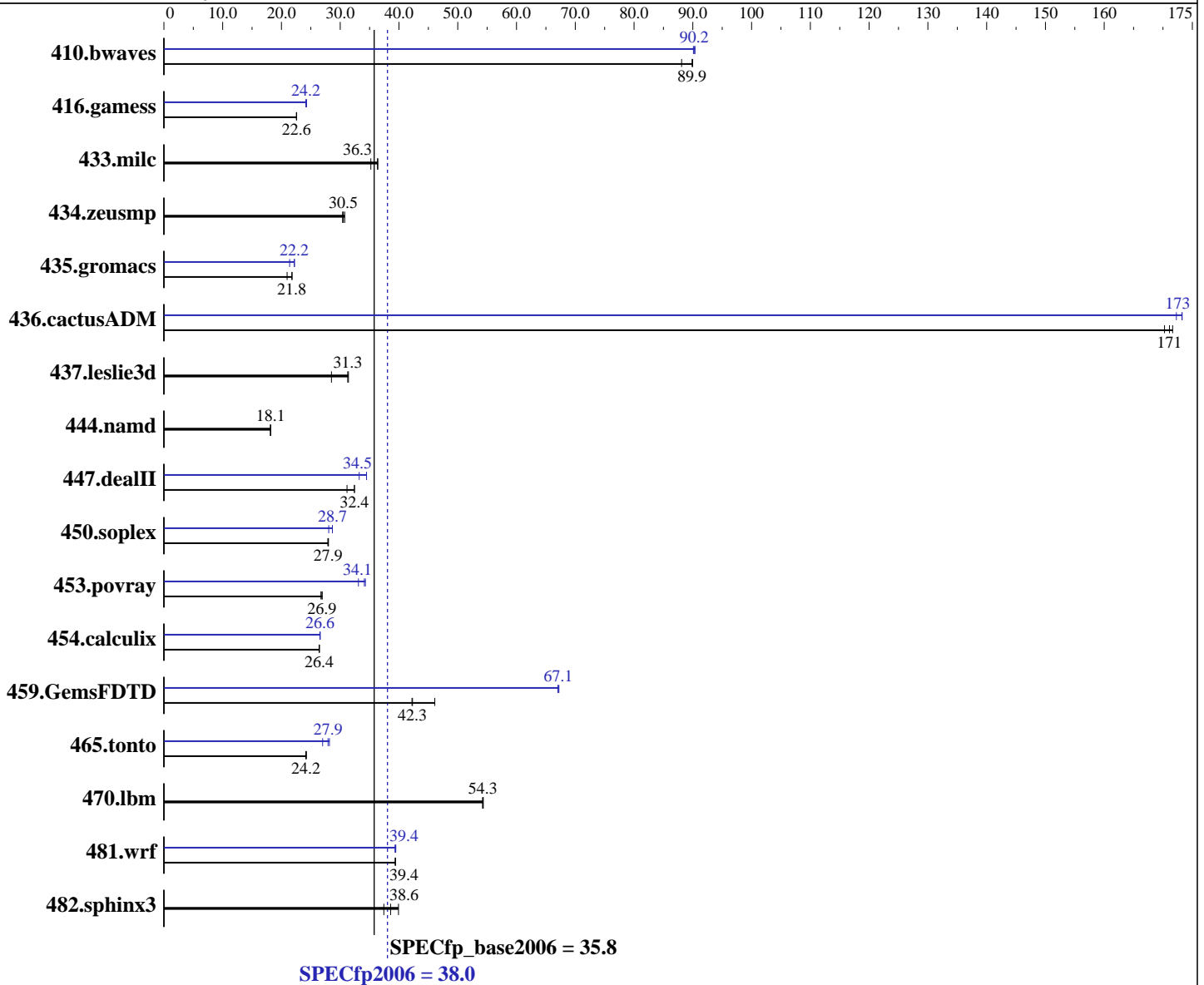
Test date: Jun-2009

Test sponsor: Fujitsu

Hardware Availability: Apr-2009

Tested by: Fujitsu

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5550
 CPU Characteristics: Intel Turbo Boost Technology up to 3.07 GHz
 CPU MHz: 2667
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smpp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	154	88.1	151	90.0	<u>151</u>	<u>89.9</u>	151	90.1	<u>151</u>	<u>90.2</u>	150	90.4
416.gamess	867	22.6	<u>868</u>	<u>22.6</u>	868	22.6	810	24.2	807	24.3	<u>810</u>	<u>24.2</u>
433.milc	252	36.4	261	35.2	<u>253</u>	<u>36.3</u>	252	36.4	261	35.2	<u>253</u>	<u>36.3</u>
434.zeusmp	<u>298</u>	<u>30.5</u>	299	30.4	296	30.8	<u>298</u>	<u>30.5</u>	299	30.4	296	30.8
435.gromacs	341	21.0	<u>328</u>	<u>21.8</u>	327	21.8	321	22.2	<u>322</u>	<u>22.2</u>	334	21.4
436.cactusADM	70.2	170	69.6	172	<u>69.8</u>	<u>171</u>	69.0	173	69.4	172	<u>69.0</u>	<u>173</u>
437.leslie3d	<u>301</u>	<u>31.3</u>	330	28.5	299	31.4	<u>301</u>	<u>31.3</u>	330	28.5	299	31.4
444.namd	442	18.2	<u>442</u>	<u>18.1</u>	442	18.1	442	18.2	<u>442</u>	<u>18.1</u>	442	18.1
447.dealII	353	32.4	<u>353</u>	<u>32.4</u>	367	31.2	344	33.2	<u>332</u>	<u>34.5</u>	332	34.5
450.soplex	<u>299</u>	<u>27.9</u>	299	27.9	298	28.0	<u>291</u>	<u>28.7</u>	297	28.0	291	28.7
453.povray	198	26.9	<u>198</u>	<u>26.9</u>	199	26.7	161	33.1	155	34.3	<u>156</u>	<u>34.1</u>
454.calculix	312	26.5	<u>312</u>	<u>26.4</u>	312	26.4	310	26.6	311	26.5	<u>310</u>	<u>26.6</u>
459.GemsFDTD	251	42.2	230	46.1	<u>251</u>	<u>42.3</u>	158	67.2	158	67.0	<u>158</u>	<u>67.1</u>
465.tonto	406	24.2	<u>407</u>	<u>24.2</u>	408	24.1	<u>352</u>	<u>27.9</u>	364	27.0	349	28.2
470.lbm	<u>253</u>	<u>54.3</u>	253	54.3	253	54.2	<u>253</u>	<u>54.3</u>	253	54.3	253	54.2
481.wrf	<u>284</u>	<u>39.4</u>	284	39.4	284	39.4	284	39.3	<u>283</u>	<u>39.4</u>	283	39.5
482.sphinx3	<u>505</u>	<u>38.6</u>	521	37.4	488	39.9	<u>505</u>	<u>38.6</u>	521	37.4	488	39.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

This result was measured on the PRIMERGY TX300 S5. The PRIMERGY TX300 S5 and the PRIMERGY RX300 S5 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch



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Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel -auto-ilp32



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20091013.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20091013.xml>

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Tested with SPEC CPU2006 v1.1.
Report generated on Wed Jul 23 04:47:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 October 2009.