



# SPEC® CINT2006 Result

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## Fujitsu

### SPECint®\_rate2006 = 121

PRIMERGY TX150 S7, Intel Xeon X3460, 2.80 GHz

### SPECint\_rate\_base2006 = 114

CPU2006 license: 19

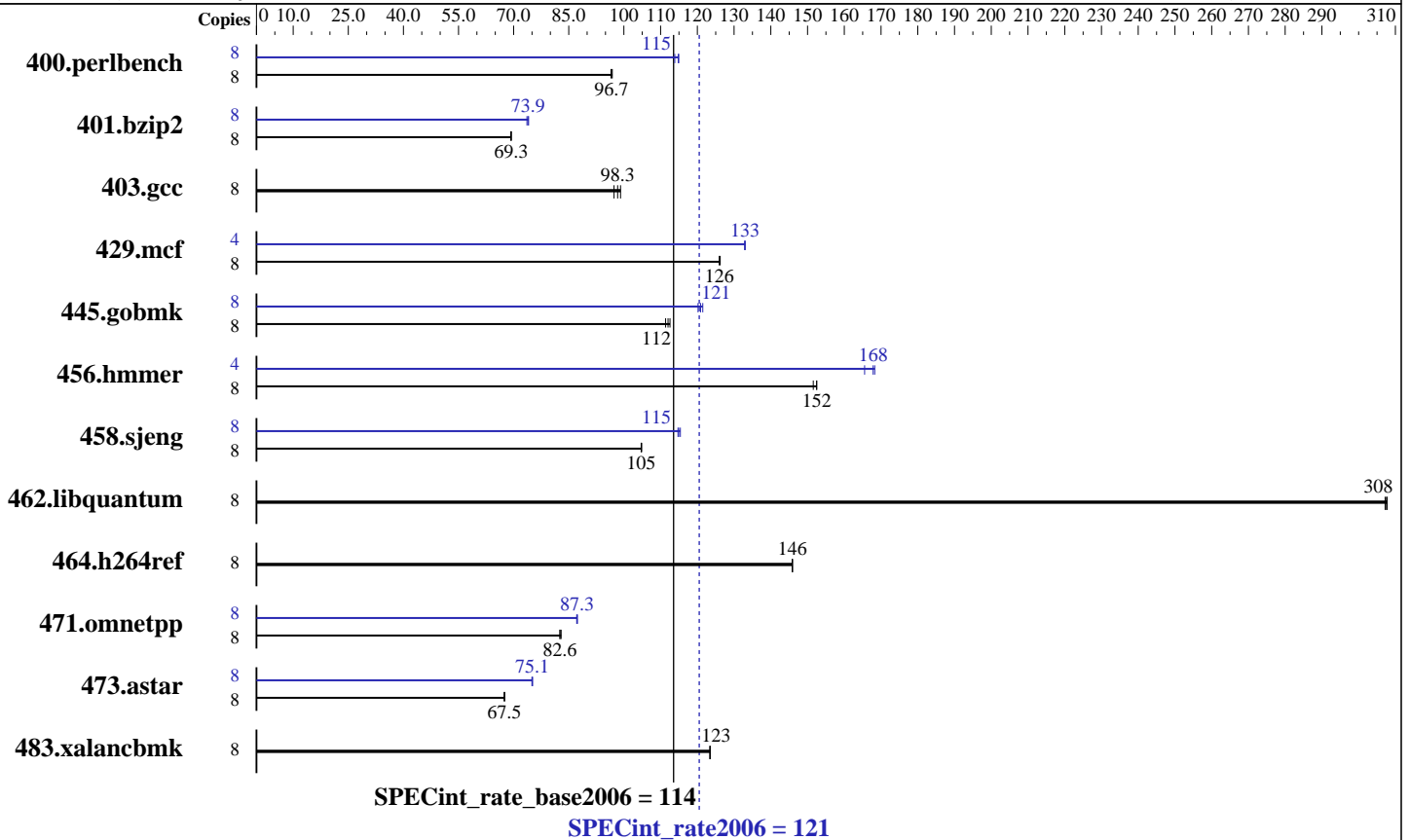
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2009

Hardware Availability: Jan-2010

Software Availability: Nov-2009



### Hardware

CPU Name: Intel Xeon X3460  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz  
 CPU MHz: 2800  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 16 GB (4x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)  
 Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-smp  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l\_cproc\_p\_11.1.059  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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## Results Table

| Benchmark      | Base   |            |             |             |             |             |             | Peak   |            |             |             |             |             |             |
|----------------|--------|------------|-------------|-------------|-------------|-------------|-------------|--------|------------|-------------|-------------|-------------|-------------|-------------|
|                | Copies | Seconds    | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       | Copies | Seconds    | Ratio       | Seconds     | Ratio       | Seconds     | Ratio       |
| 400.perlbench  | 8      | 807        | 96.9        | 810         | 96.5        | <u>809</u>  | <u>96.7</u> | 8      | 686        | 114         | <u>681</u>  | <u>115</u>  | 680         | 115         |
| 401.bzip2      | 8      | 1113       | 69.4        | 1115        | 69.2        | <u>1113</u> | <u>69.3</u> | 8      | 1049       | 73.6        | 1042        | 74.1        | <u>1045</u> | <u>73.9</u> |
| 403.gcc        | 8      | <u>655</u> | <u>98.3</u> | 662         | 97.3        | 650         | 99.1        | 8      | <u>655</u> | <u>98.3</u> | 662         | 97.3        | 650         | 99.1        |
| 429.mcf        | 8      | 578        | 126         | 579         | 126         | <u>579</u>  | <u>126</u>  | 4      | 274        | 133         | 275         | 133         | <u>274</u>  | <u>133</u>  |
| 445.gobmk      | 8      | <u>749</u> | <u>112</u>  | 754         | 111         | 746         | 113         | 8      | <u>694</u> | <u>121</u>  | 698         | 120         | 691         | 121         |
| 456.hammer     | 8      | <u>490</u> | <u>152</u>  | 492         | 152         | 489         | 152         | 4      | <u>222</u> | <u>168</u>  | 222         | 168         | 225         | 166         |
| 458.sjeng      | 8      | 924        | 105         | 923         | 105         | <u>924</u>  | <u>105</u>  | 8      | 844        | 115         | 839         | 115         | <u>842</u>  | <u>115</u>  |
| 462.libquantum | 8      | 539        | 308         | <u>539</u>  | <u>308</u>  | 540         | 307         | 8      | 539        | 308         | <u>539</u>  | <u>308</u>  | 540         | 307         |
| 464.h264ref    | 8      | 1214       | 146         | <u>1214</u> | <u>146</u>  | 1213        | 146         | 8      | 1214       | 146         | <u>1214</u> | <u>146</u>  | 1213        | 146         |
| 471.omnetpp    | 8      | 603        | 82.9        | 605         | 82.6        | <u>605</u>  | <u>82.6</u> | 8      | 574        | 87.2        | <u>573</u>  | <u>87.3</u> | 572         | 87.3        |
| 473.astar      | 8      | 833        | 67.5        | <u>832</u>  | <u>67.5</u> | 831         | 67.5        | 8      | 749        | 75.0        | <u>748</u>  | <u>75.1</u> | 748         | 75.1        |
| 483.xalancbmk  | 8      | 447        | 123         | <u>447</u>  | <u>123</u>  | 447         | 123         | 8      | 447        | 123         | <u>447</u>  | <u>123</u>  | 447         | 123         |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m32  
  
C++ benchmarks:  
icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

Continued on next page



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## Base Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX

Continued on next page



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## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
-ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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