SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX150 S7, Intel Xeon L3426, 1.86 GHz

SPECint®_rate2006 =   90.5

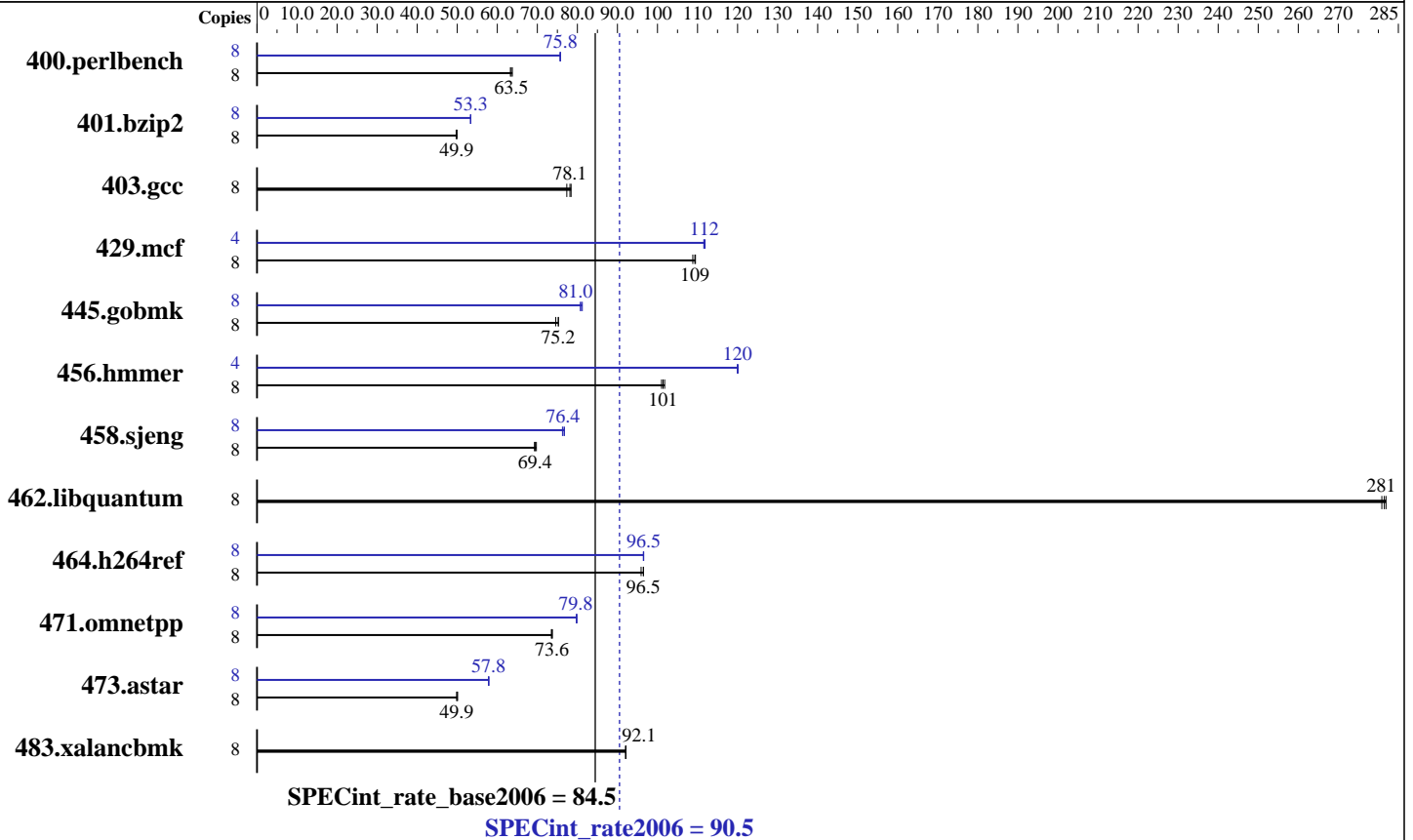SPECint_rate_base2006 = 84.5

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Dec-2009
Hardware Availability: Jan-2010
Software Availability: Nov-2009

| Copies | 0 10.0 20.0 30.0 40.0 50.0 60.0 70.0 80.0 90.0 100 110 120 130 140 150 160 170 180 190 200 210 220 230 240 250 260 270 285 |

400.perlbench — 8: 75.8 / 8: 63.5
401.bzip2 — 8: 53.3 / 8: 49.9
403.gcc — 8: 78.1
429.mcf — 4: 112 / 8: 109
445.gobmk — 8: 81.0 / 8: 75.2
456.hmmer — 4: 120 / 8: 101
458.sjeng — 8: 76.4 / 8: 69.4
462.libquantum — 8: 281
464.h264ref — 8: 96.5 / 8: 96.5
471.omnetpp — 8: 79.8 / 8: 73.6
473.astar — 8: 57.8 / 8: 49.9
483.xalancbmk — 8: 92.1

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Hardware

CPU Name:	Intel Xeon L3426
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	8	1235	63.3	**1231**	**63.5**	1226	63.8	8	1033	75.6	1031	75.8	**1031**	**75.8**
401.bzip2	8	**1547**	**49.9**	1547	49.9	1552	49.7	8	1448	53.3	**1447**	**53.3**	1446	53.4
403.gcc	8	833	77.4	821	78.4	**824**	**78.1**	8	833	77.4	821	78.4	**824**	**78.1**
429.mcf	8	670	109	**667**	**109**	666	109	4	326	112	**327**	**112**	327	112
445.gobmk	8	1115	75.3	**1115**	**75.2**	1125	74.6	8	**1036**	**81.0**	1040	80.7	1033	81.2
456.hmmer	8	733	102	739	101	**736**	**101**	4	311	120	**311**	**120**	311	120
458.sjeng	8	1397	69.3	**1394**	**69.4**	1388	69.8	8	1261	76.8	1269	76.3	**1266**	**76.4**
462.libquantum	8	590	281	588	282	**589**	**281**	8	590	281	588	282	**589**	**281**
464.h264ref	8	**1835**	**96.5**	1835	96.5	1846	95.9	8	1833	96.6	1835	96.5	**1834**	**96.5**
471.omnetpp	8	678	73.7	680	73.5	**679**	**73.6**	8	**626**	**79.8**	627	79.7	626	79.9
473.astar	8	**1126**	**49.9**	1121	50.1	1126	49.9	8	**971**	**57.8**	970	57.9	971	57.8
483.xalancbmk	8	599	92.1	**599**	**92.1**	600	92.0	8	599	92.1	**599**	**92.1**	600	92.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32

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Base Portability Flags (Continued)

        462.libquantum: -DSPEC_CPU_LINUX
        483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
        -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
        -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

Base Other Flags

C benchmarks:

            403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
        icc -m32

        401.bzip2: icc -m64

        456.hmmer: icc -m64

        458.sjeng: icc -m64

C++ benchmarks (except as noted below):
        icpc -m32

            473.astar: icpc -m64

Peak Portability Flags

        400.perlbench: -DSPEC_CPU_LINUX_IA32
        401.bzip2: -DSPEC_CPU_LP64
        456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
        462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

    473.astar: `-DSPEC_CPU_LP64`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
               `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
               `-prof-use(pass 2) -ansi-alias`

      401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
               `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
               `-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32`

        403.gcc: `basepeak = yes`

        429.mcf: `-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch`

     445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2`
               `-ipo -no-prec-div -ansi-alias`

     456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2`
               `-ansi-alias -auto-ilp32`

      458.sjeng: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
               `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
               `-prof-use(pass 2) -unroll4 -auto-ilp32`

  462.libquantum: `basepeak = yes`

    464.h264ref: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
               `-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)`
               `-prof-use(pass 2) -unroll2 -ansi-alias`

C++ benchmarks:

    471.omnetpp: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
               `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
               `-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs`
               `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap`

      473.astar: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)`
               `-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)`
               `-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs`
               `-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64`

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
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