



# SPEC® CINT2006 Result

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## Cisco Systems

## SPECint®\_rate2006 = 258

## UCS C200 M1 (Intel Xeon X5550)

## SPECint\_rate\_base2006 = 241

CPU2006 license: 9019

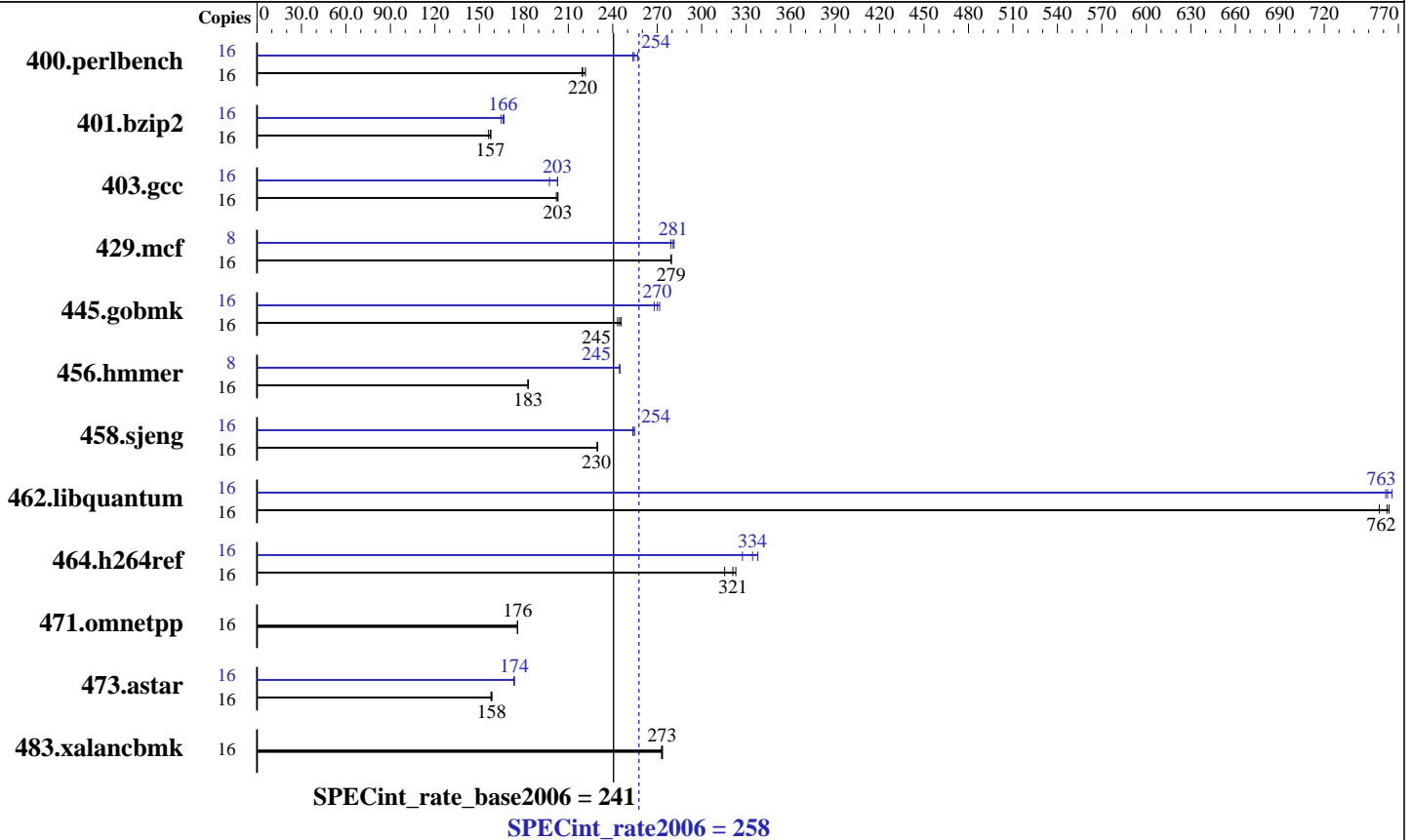
Test date: Jan-2010

Test sponsor: Cisco Systems

Hardware Availability: Oct-2009

Tested by: Cisco Systems

Software Availability: Mar-2009



### Hardware

CPU Name: Intel Xeon X5550  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 Chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 24 GB (6 \* 4GB DDR3-1333 MHz)  
 Disk Subsystem: 73 GB SATA, 15kRPM  
 Other Hardware: None

### Software

Operating System: SuSe Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27-15-2-default, RC4  
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080, l\_cprof\_p\_11.0.080  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Binutils 2.18.50.0.7.20080502 and SmartHeap



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	<u>712</u>	<u>220</u>	706	222	713	219	16	617	253	609	257	<u>614</u>	<u>254</u>
401.bzip2	16	988	156	<u>981</u>	<u>157</u>	979	158	16	938	165	<u>930</u>	<u>166</u>	927	167
403.gcc	16	634	203	638	202	<u>635</u>	<u>203</u>	16	<u>636</u>	<u>203</u>	635	203	653	197
429.mcf	16	<u>522</u>	<u>279</u>	522	279	522	280	8	261	279	<u>260</u>	<u>281</u>	259	281
445.gobmk	16	683	246	690	243	<u>686</u>	<u>245</u>	16	618	272	626	268	<u>621</u>	<u>270</u>
456.hammer	16	817	183	<u>816</u>	<u>183</u>	815	183	8	305	245	<u>305</u>	<u>245</u>	305	245
458.sjeng	16	843	230	<u>843</u>	<u>230</u>	844	229	16	760	255	<u>763</u>	<u>254</u>	763	254
462.libquantum	16	438	757	434	764	<u>435</u>	<u>762</u>	16	435	761	<u>435</u>	<u>763</u>	433	766
464.h264ref	16	1096	323	<u>1103</u>	<u>321</u>	1122	316	16	1048	338	<u>1059</u>	<u>334</u>	1081	327
471.omnetpp	16	<u>569</u>	<u>176</u>	569	176	569	176	16	<u>569</u>	<u>176</u>	569	176	569	176
473.astar	16	712	158	<u>710</u>	<u>158</u>	709	158	16	648	173	<u>647</u>	<u>174</u>	647	174
483.xalancbmk	16	404	273	404	274	<u>404</u>	<u>273</u>	16	404	273	404	274	<u>404</u>	<u>273</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

ulimit -s unlimited was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:

```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/spec/cpu2006.1.1/lib -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
456.hmmer: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

```
458.sjeng: /opt/intel/Compiler/11.0/080/bin/intel64/icc
```

C++ benchmarks (except as noted below):

icpc

```
473.astar: /opt/intel/Compiler/11.0/080/bin/intel64/icpc
```

## Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -DSPEC_CPU_LINUX
```

```
473.astar: -DSPEC_CPU_LP64
```

```
483.xalancbmk: -DSPEC_CPU_LINUX
```



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## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -ansi-alias -opt-prefetch

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc  
 -opt-malloc-options=3

429.mcf: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -opt-prefetch

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
 -ipo -no-prec-div -ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2  
 -ansi-alias -auto-ilp32

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static  
 -opt-malloc-options=3 -opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
 -prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: basepeak = yes

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
 -ansi-alias -opt-ra-region-strategy=routine -auto-ilp32  
 -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmarheap64

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-int-linux64-revH.20100317.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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