SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX150 S7, Intel Celeron G1101, 2.26 GHz

SPECint®_rate2006 =   35.0

SPECint_rate_base2006 = 32.2
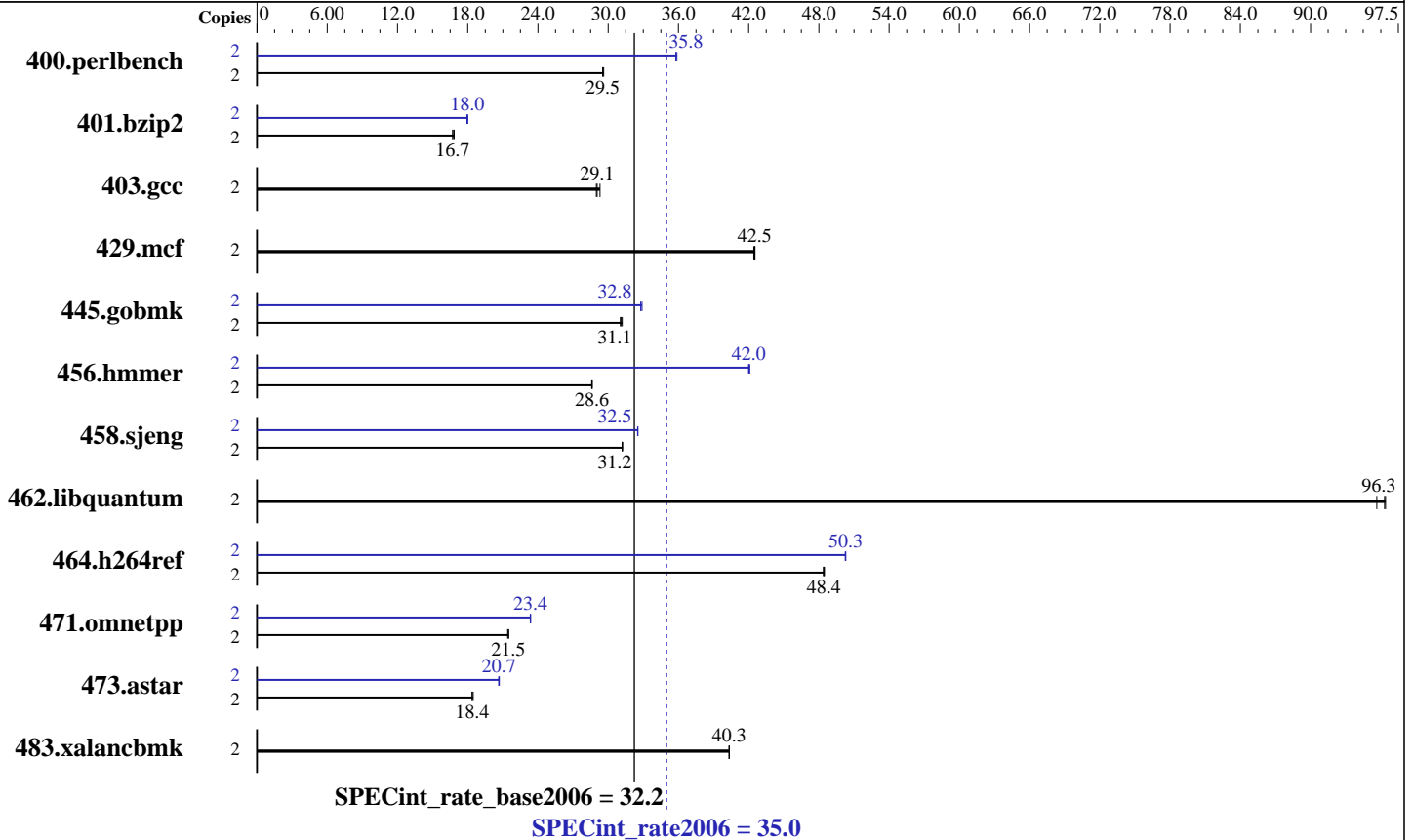
CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: Jan-2010
Hardware Availability: Jan-2010
Software Availability: Nov-2009

Copies

Benchmark		
400.perlbench	2	35.8
	2	29.5
401.bzip2	2	18.0
	2	16.7
403.gcc	2	29.1
429.mcf	2	42.5
445.gobmk	2	32.8
	2	31.1
456.hmmer	2	42.0
	2	28.6
458.sjeng	2	32.5
	2	31.2
462.libquantum	2	96.3
464.h264ref	2	50.3
	2	48.4
471.omnetpp	2	23.4
	2	21.5
473.astar	2	20.7
	2	18.4
483.xalancbmk	2	40.3

SPECint_rate_base2006 = 32.2

SPECint_rate2006 = 35.0

Hardware

CPU Name:	Intel Celeron G1101
CPU Characteristics:	
CPU MHz:	2267
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	8 GB (2x4 GB PC3-10600E, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem:	1 x SATA, 250 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-smp
Compiler:	Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091012 Package ID: l_cproc_p_11.1.059
Auto Parallel:	No
File System:	ext3
System State:	Multi-User Run Level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	2	**661**	**29.5**	662	29.5	660	29.6	2	545	35.9	546	35.8	**546**	**35.8**
401.bzip2	2	**1154**	**16.7**	1146	16.8	1154	16.7	2	1076	17.9	**1074**	**18.0**	1072	18.0
403.gcc	2	549	29.3	555	29.0	**554**	**29.1**	2	549	29.3	555	29.0	**554**	**29.1**
429.mcf	2	**429**	**42.5**	429	42.5	430	42.4	2	**429**	**42.5**	429	42.5	430	42.4
445.gobmk	2	676	31.0	672	31.2	**675**	**31.1**	2	638	32.9	**640**	**32.8**	640	32.8
456.hmmer	2	651	28.6	653	28.6	**652**	**28.6**	2	443	42.1	**444**	**42.0**	444	42.0
458.sjeng	2	775	31.2	776	31.2	**776**	**31.2**	2	**744**	**32.5**	744	32.5	744	32.5
462.libquantum	2	430	96.4	433	95.7	**430**	**96.3**	2	430	96.4	433	95.7	**430**	**96.3**
464.h264ref	2	913	48.5	**914**	**48.4**	915	48.4	2	880	50.3	**880**	**50.3**	881	50.2
471.omnetpp	2	581	21.5	**582**	**21.5**	584	21.4	2	536	23.3	535	23.4	**535**	**23.4**
473.astar	2	**764**	**18.4**	760	18.5	764	18.4	2	**679**	**20.7**	678	20.7	680	20.6
483.xalancbmk	2	**342**	**40.3**	342	40.3	342	40.4	2	**342**	**40.3**	342	40.3	342	40.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

    The config file option 'submit' was used.
     taskset has been used to bind processes to cores

Operating System Notes

    'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

    The system automatically configures the memory to run at 1066 MHz.

General Notes

    For information about Fujitsu please visit: http://www.fujitsu.com

Base Compiler Invocation

C benchmarks:
     icc -m32

C++ benchmarks:
     icpc -m32

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Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -static -inline-calloc
-opt-malloc-options=3 -opt-prefetch
```

C++ benchmarks:
```
-xSSSE3 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m32
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
```

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -ansi-alias -opt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -opt-prefetch -ansi-alias

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -xSSSE3 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll4

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xSSSE3 -ipo -O3
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -ansi-alias
-opt-ra-region-strategy=routine -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

483.xalancbmk: basepeak = yes

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Peak Other Flags

C benchmarks:

   403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-int-linux64-revE.20100202.xml

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