



# SPEC® CFP2006 Result

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Bull SAS

BL265  
(Intel Xeon X5570, 2.93 GHz)

**SPECfp®\_rate2006 = 197**

**SPECfp\_rate\_base2006 = 191**

CPU2006 license: 20

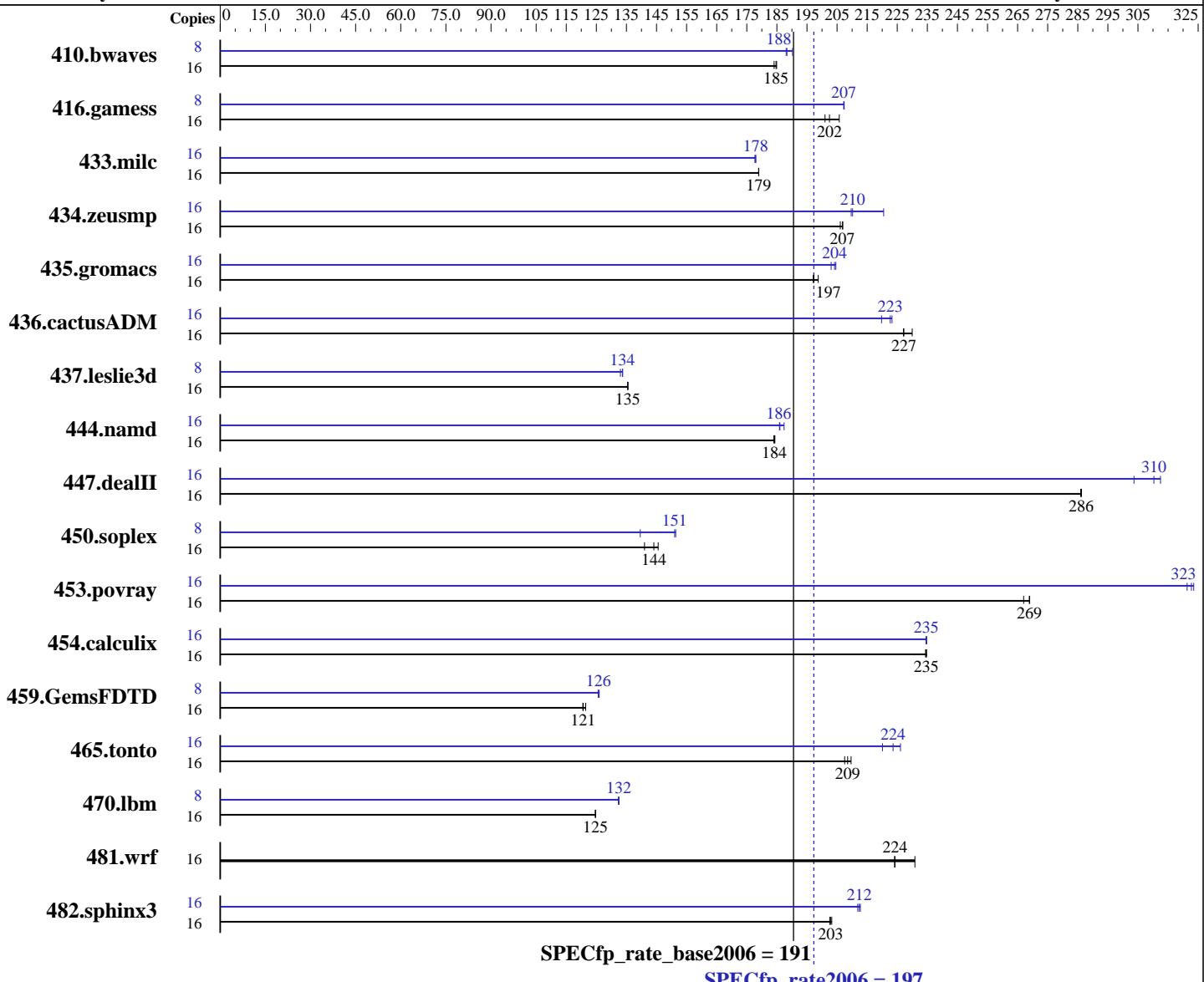
Test sponsor: Bull SAS

Tested by: Bull SAS

**Test date:** Jan-2010

**Hardware Availability:** May-2009

**Software Availability:** Feb-2009



## Hardware

CPU Name: Intel Xeon X5570  
CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz  
CPU MHz: 2933  
FPU: Integrated  
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
CPU(s) orderable: 1,2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: Red Hat Enterprise Linux 5 (x86\_64) Update 3, Kernel 2.6.18-128.el5  
Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l\_cproc\_p\_11.0.080, l\_cprof\_p\_11.0.080  
Auto Parallel: No  
File System: ReiserFS  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
Other Cache: None  
Memory: 24 GB (6 x 4 GB PC3-10600R, 2 Rank, CL9-9-9, ECC)  
Disk Subsystem: 1 x 73 GB SAS, 10000 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: Binutils 2.18.50.0.7.20080502

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	1181	184	1176	185	<u>1177</u>	<u>185</u>	8	578	188	572	190	<u>577</u>	<u>188</u>
416.gamess	16	<u>1547</u>	<u>202</u>	1559	201	1523	206	8	<u>756</u>	<u>207</u>	756	207	<u>755</u>	<u>207</u>
433.milc	16	821	179	<u>821</u>	<u>179</u>	821	179	16	825	178	827	178	<u>825</u>	<u>178</u>
434.zeusmp	16	707	206	<u>704</u>	<u>207</u>	704	207	16	660	220	<u>693</u>	<u>210</u>	694	210
435.gromacs	16	<u>579</u>	<u>197</u>	580	197	575	199	16	558	205	563	203	<u>560</u>	<u>204</u>
436.cactusADM	16	<u>842</u>	<u>227</u>	831	230	842	227	16	870	220	<u>859</u>	<u>223</u>	856	223
437.leslie3d	16	<u>1110</u>	<u>135</u>	1110	135	1111	135	8	562	134	<u>563</u>	<u>134</u>	565	133
444.namd	16	697	184	696	184	<u>697</u>	<u>184</u>	16	685	187	690	186	<u>690</u>	<u>186</u>
447.dealII	16	640	286	639	286	<u>640</u>	<u>286</u>	16	<u>590</u>	<u>310</u>	603	304	<u>586</u>	<u>312</u>
450.soplex	16	<u>926</u>	<u>144</u>	946	141	917	146	8	478	140	<u>442</u>	<u>151</u>	441	151
453.povray	16	319	267	<u>317</u>	<u>269</u>	316	269	16	263	324	265	321	<u>264</u>	<u>323</u>
454.calculix	16	<u>563</u>	<u>235</u>	563	234	562	235	16	562	235	563	235	<u>563</u>	<u>235</u>
459.GemsFDTD	16	1398	121	<u>1407</u>	<u>121</u>	1409	121	8	<u>675</u>	<u>126</u>	674	126	<u>676</u>	<u>126</u>
465.tonto	16	<u>755</u>	<u>209</u>	751	210	759	208	16	<u>704</u>	<u>224</u>	696	226	<u>715</u>	<u>220</u>
470.lbm	16	1762	125	1763	125	<u>1763</u>	<u>125</u>	8	831	132	<u>830</u>	<u>132</u>	830	132
481.wrf	16	774	231	797	224	<u>797</u>	<u>224</u>	16	774	231	797	224	<u>797</u>	<u>224</u>
482.sphinx3	16	1539	203	<u>1537</u>	<u>203</u>	1534	203	16	1472	212	<u>1468</u>	<u>212</u>	1466	213

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Base Compiler Invocation

C benchmarks:  
icc

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## Base Compiler Invocation (Continued)

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.games: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks (except as noted below):

ifort

437.leslie3d: ifort -m32

Benchmarks using both Fortran and C:

icc ifort

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

```

## Peak Optimization Flags

C benchmarks:

```

433.milc: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
          -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
          -fno-alias

```

```

470.lbm: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
          -auto-ilp32

```

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## Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll12

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll12 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll12 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32

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## Peak Optimization Flags (Continued)

436.cactusADM: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)  
-unroll2 -opt-prefetch -auto-ilp32

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20100202.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20100202.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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