



SPEC® CINT2006 Result

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Intel Corporation

SPECint®_rate2006 = 62.6

Intel DH57JG motherboard (Intel Core i5-661)

SPECint_rate_base2006 = 60.1

CPU2006 license: 13

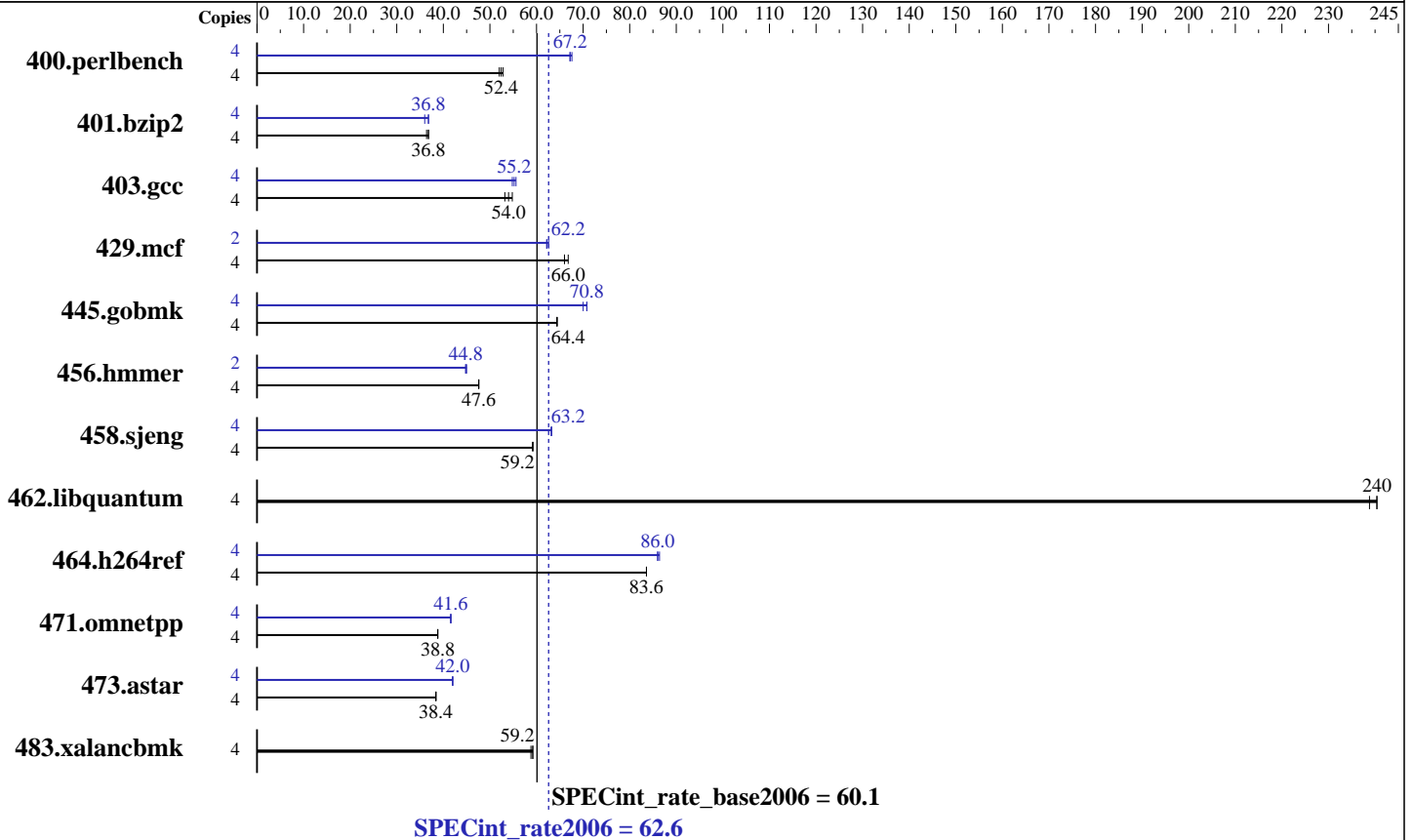
Test date: Jan-2009

Test sponsor: Intel Corporation

Hardware Availability: Mar-2010

Tested by: Intel Corporation

Software Availability: Oct-2009



Hardware

CPU Name: Intel Core i5-661
 CPU Characteristics: Intel Turbo Boost Technology up to 3.6 GHz
 CPU MHz: 3333
 FPU: Integrated
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 4 MB I+D on chip per chip
 Other Cache: None
 Memory: 4 GB (2x2GB Micron MT16JTF25664AZ-1G4 DDR3-1333 CL9)
 Disk Subsystem: Intel X25-M 80GB SSD
 Other Hardware: None

Software

Operating System: Windows Vista Ultimate w/ SP1 (64-bit)
 Compiler: Intel C++ Compiler Professional 11.1 for IA32
 Build 20090903 Package ID: w_cproc_p_11.1.045
 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: None
 SmartHeap Library Version 8.1 from <http://www.microquill.com/>



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	747	52.4	751	52.0	740	52.8	4	577	67.6	581	67.2	582	67.2
401.bzip2	4	1057	36.4	1044	36.8	1047	36.8	4	1052	36.8	1050	36.8	1073	36.0
403.gcc	4	605	53.2	587	54.8	595	54.0	4	583	55.2	588	54.8	577	55.6
429.mcf	4	552	66.0	553	66.0	548	66.8	2	292	62.6	293	62.2	293	62.2
445.gobmk	4	651	64.4	650	64.4	650	64.4	4	591	70.8	598	70.0	591	70.8
456.hammer	4	784	47.6	784	47.6	784	47.6	2	415	45.0	416	44.8	416	44.8
458.sjeng	4	815	59.2	815	59.2	815	59.2	4	768	63.2	768	63.2	768	63.2
462.libquantum	4	345	240	345	240	347	239	4	345	240	345	240	347	239
464.h264ref	4	1058	83.6	1057	83.6	1057	83.6	4	1028	86.0	1028	86.0	1026	86.4
471.omnetpp	4	647	38.8	646	38.8	647	38.8	4	602	41.6	601	41.6	602	41.6
473.astar	4	731	38.4	731	38.4	732	38.4	4	672	42.0	671	42.0	671	42.0
483.xalanbmk	4	467	59.2	468	59.2	468	58.8	4	467	59.2	468	59.2	468	58.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
Windows start command was used to bind copies to processors

General Notes

Tested systems can be used with Shin-G ATX case,
PC Power and Cooling 1200W power supply
System was configured with nVidia GTX 280 discrete graphics card

Base Compiler Invocation

C benchmarks:
icl -Qvc9 -Qstd=c99

C++ benchmarks:
icl -Qvc9

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
483.xalanbmk: -Qoption, cpp, --no_wchar_t_keyword



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Base Optimization Flags

C benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

C++ benchmarks:

-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:

icl -Qvc9 -Qstd=c99

C++ benchmarks:

icl -Qvc9

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DWIN32 -DSPEC_CPU_NO_INTTYPES
483.xalancbmk: -Qoption,cpp,--no_wchar_t_keyword

Peak Optimization Flags

C benchmarks:

400.perlbench: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch
/F512000000 shlw32M.lib -link /FORCE:MULTIPLE

401.bzip2: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch -Qansi-alias
/F512000000

403.gcc: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2)
-Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

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Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmcr: -QxSSE4.2 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlW32M.lib -link /FORCE:MULTIPLE

473.astar: -QxSSE4.2(pass 2) -Qprof_gen(pass 1) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlW32M.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.00.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.00.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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