SPEC® CINT2006 Result
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Acer Incorporated

Gateway GT310 F1 (Intel Xeon X3430)

SPECint®2006 =   29.1

SPECint_base2006 =   27.3

Benchmark	Base	Peak
400.perlbench	20.8	24.0
401.bzip2	14.6	14.6
403.gcc	21.0	22.8
429.mcf	36.0	40.5
445.gobmk	19.7	20.4
456.hmmer	38.5	40.1
458.sjeng	21.5	22.2
462.libquantum	172	171
464.h264ref	29.5	32.1
471.omnetpp	19.3	23.6
473.astar	15.3	15.9
483.xalancbmk	30.3	30.9

SPECint_base2006 = 27.3
SPECint2006 = 29.1

Hardware

CPU Name: Intel Xeon X3430
CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core
L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 8 GB (4 x 2048 MB Registered DDR3-1333)
Disk Subsystem: 1 x 1000 GB 7200RPM SATA HDD
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Sserver 11 (x86_64) Kernel 2.6.27.19-5
Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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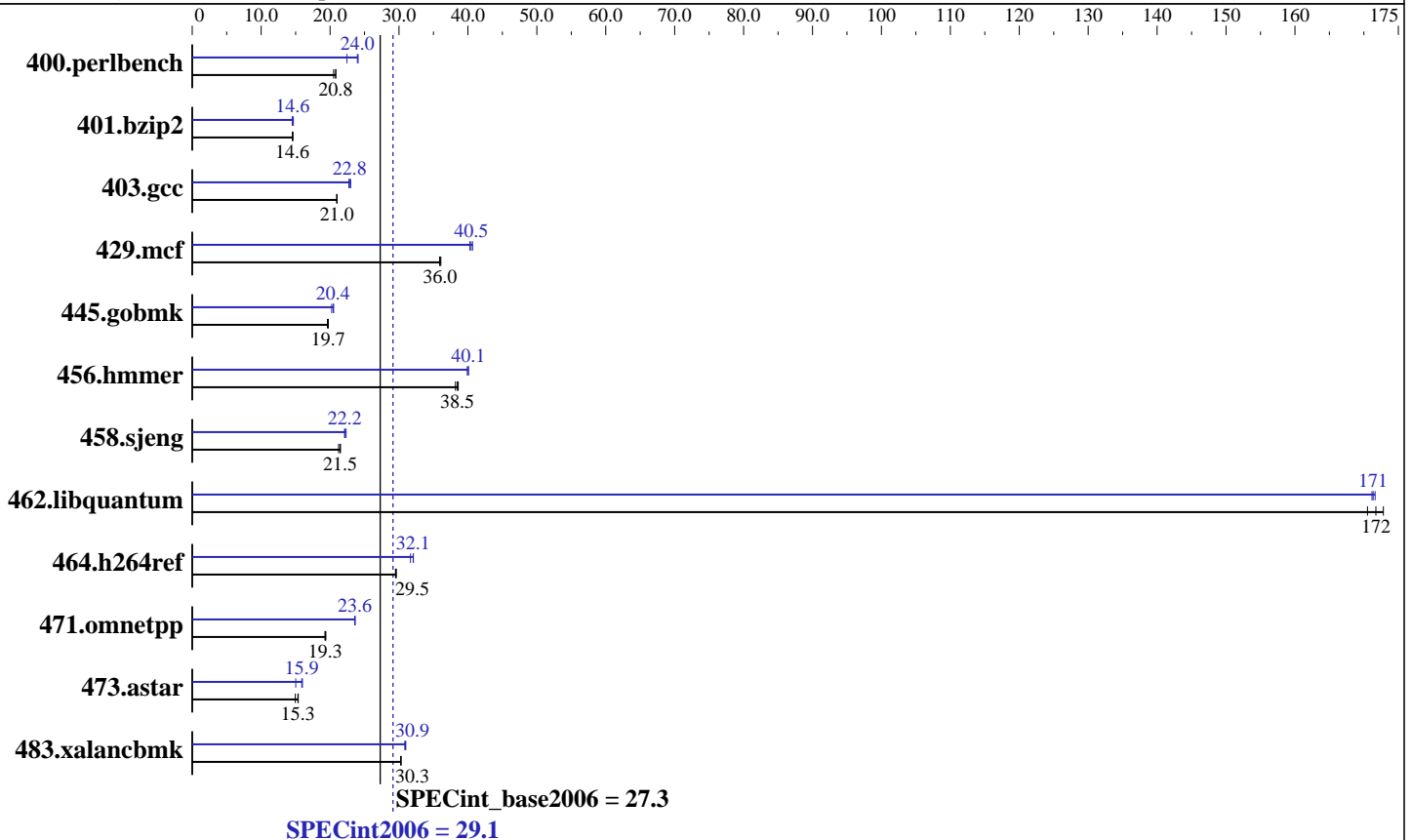| Acer Incorporated	SPECint2006 =	29.1
Gateway GT310 F1 (Intel Xeon X3430)	SPECint_base2006 =	27.3

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: Mar-2010
Hardware Availability: Mar-2010
Software Availability: Jan-2010

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**469**	**20.8**	469	20.8	476	20.5	**408**	**24.0**	435	22.5	406	24.1
401.bzip2	661	14.6	663	14.5	**663**	**14.6**	**662**	**14.6**	665	14.5	657	14.7
403.gcc	383	21.0	**383**	**21.0**	384	21.0	350	23.0	354	22.7	**352**	**22.8**
429.mcf	253	36.1	254	35.9	**253**	**36.0**	226	40.3	**225**	**40.5**	224	40.7
445.gobmk	**532**	**19.7**	532	19.7	535	19.6	**513**	**20.4**	519	20.2	511	20.5
456.hmmer	242	38.6	**243**	**38.5**	244	38.2	**233**	**40.1**	233	40.1	234	39.9
458.sjeng	**564**	**21.5**	563	21.5	570	21.2	542	22.3	**546**	**22.2**	547	22.1
462.libquantum	122	171	120	173	**121**	**172**	121	171	121	172	**121**	**171**
464.h264ref	747	29.6	750	29.5	**749**	**29.5**	699	31.7	690	32.1	**690**	**32.1**
471.omnetpp	322	19.4	**324**	**19.3**	324	19.3	265	23.6	265	23.6	**265**	**23.6**
473.astar	456	15.4	470	14.9	**458**	**15.3**	467	15.0	**442**	**15.9**	439	16.0
483.xalancbmk	228	30.3	**228**	**30.3**	228	30.3	223	31.0	224	30.8	**223**	**30.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was set for stacksize unlimited
```

General Notes

```
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
This result was measured on the Gateway GT310 F1.
These 4 systems are electronically equivalent:
Acer AR320 F1, Acer AT310 F1, Gateway GR320 F1, and Gateway GT310 F1.
```

Base Compiler Invocation

C benchmarks:
```
icc -m64
```

C++ benchmarks:
```
icpc -m64
```

Base Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
  401.bzip2: -DSPEC_CPU_LP64
```

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Base Portability Flags (Continued)

```
   403.gcc: -DSPEC_CPU_LP64
   429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
  458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
  473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
```

C++ benchmarks:
```
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc  -m64
```

```
400.perlbench: icc -m32
```

```
429.mcf: icc -m32
```

```
445.gobmk: icc -m32
```

```
464.h264ref: icc -m32
```

C++ benchmarks (except as noted below):
```
icpc -m32
```

```
473.astar: icpc -m64
```

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Peak Portability Flags

```
      400.perlbench: -DSPEC_CPU_LINUX_IA32
          401.bzip2: -DSPEC_CPU_LP64
            403.gcc: -DSPEC_CPU_LP64
         456.hmmer: -DSPEC_CPU_LP64
          458.sjeng: -DSPEC_CPU_LP64
     462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
          473.astar: -DSPEC_CPU_LP64
      483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
     400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                    -prof-use(pass 2) -ansi-alias -opt-prefetch

         401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div -static(pass 2) -prof-use(pass 2)
                    -auto-ilp32 -opt-prefetch -ansi-alias

           403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -static -inline-calloc
                    -opt-malloc-options=3 -auto-ilp32

           429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

         445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
                    -ipo -no-prec-div -ansi-alias

         456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
                    -ansi-alias -auto-ilp32

         458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                    -prof-use(pass 2) -unroll4

    462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel
                    -opt-prefetch -par-schedule-static=32768 -ansi-alias

       464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
                    -prof-use(pass 2) -unroll2 -ansi-alias
```

C++ benchmarks:

```
       471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                    -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                    -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                    -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

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Peak Optimization Flags (Continued)

```
      473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
               -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

   483.xalancbmk: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch
               -Wl,-z,muldefs
               -L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
```

Peak Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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