



SPEC® CINT2006 Result

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Fujitsu

SPECint®_rate2006 = 287

PRIMERGY RX300 S6, Intel Xeon X5667, 3.06 GHz

SPECint_rate_base2006 = 271

CPU2006 license: 19

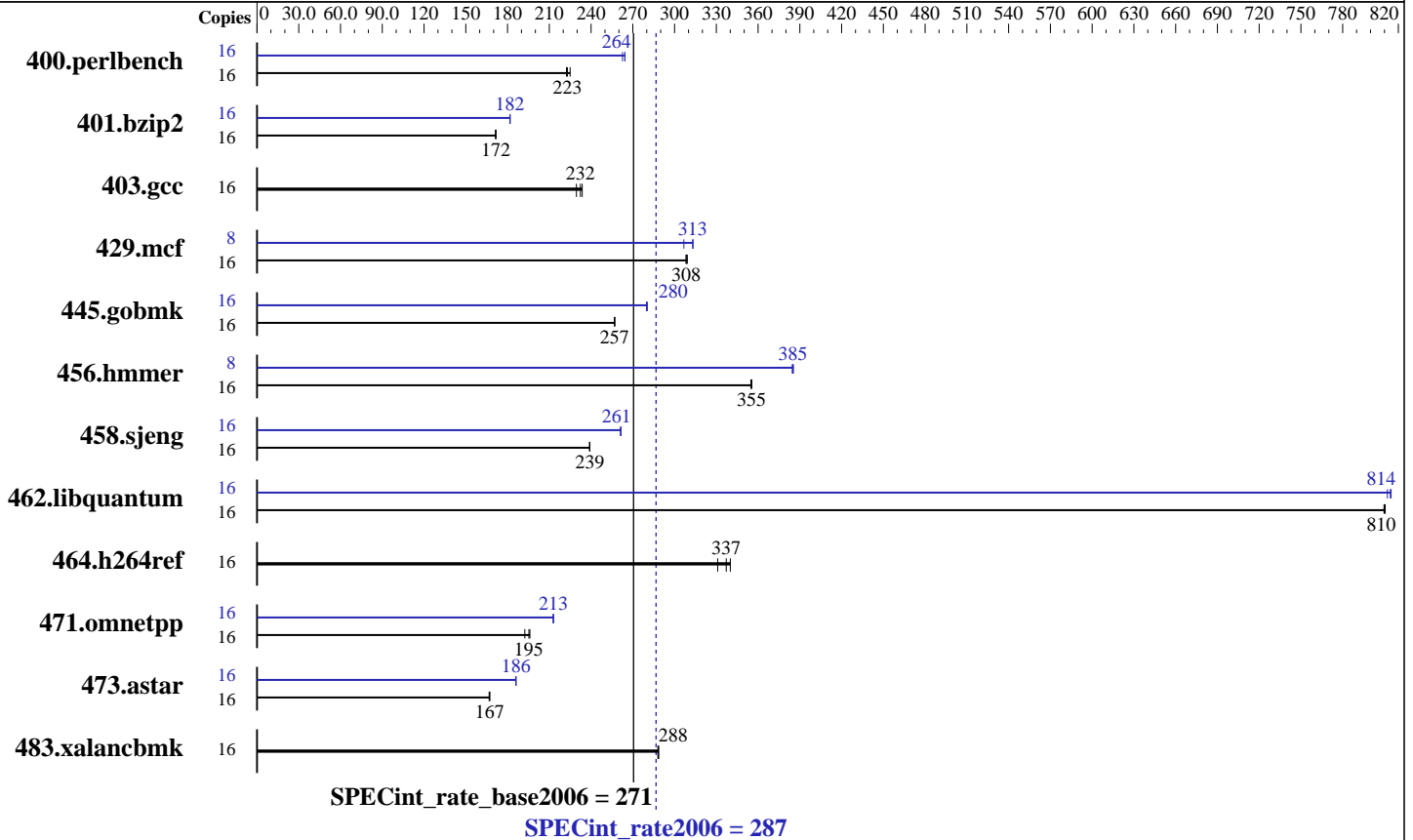
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Feb-2010

Hardware Availability: Apr-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon X5667
 CPU Characteristics: Intel Turbo Boost Technology up to 3.46 GHz
 CPU MHz: 3067
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
 Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502



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Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|----------------|--------|------------|------------|------------|------------|-------------|------------|--------|------------|------------|------------|------------|-------------|------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | 695 | 225 | 703 | 222 | <u>701</u> | <u>223</u> | 16 | 595 | 263 | <u>591</u> | <u>264</u> | 591 | 264 |
| 401.bzip2 | 16 | 900 | 172 | 901 | 171 | <u>900</u> | <u>172</u> | 16 | <u>850</u> | <u>182</u> | 850 | 182 | 849 | 182 |
| 403.gcc | 16 | 561 | 229 | 551 | 234 | <u>554</u> | <u>232</u> | 16 | 561 | 229 | 551 | 234 | <u>554</u> | <u>232</u> |
| 429.mcf | 16 | 473 | 308 | 472 | 309 | <u>473</u> | <u>308</u> | 8 | 238 | 307 | <u>233</u> | <u>313</u> | 233 | 313 |
| 445.gobmk | 16 | <u>654</u> | <u>257</u> | 654 | 257 | 653 | 257 | 16 | <u>600</u> | <u>280</u> | 599 | 280 | 600 | 280 |
| 456.hammer | 16 | 420 | 355 | 421 | 355 | <u>420</u> | <u>355</u> | 8 | <u>194</u> | <u>385</u> | 194 | 385 | 194 | 385 |
| 458.sjeng | 16 | <u>810</u> | <u>239</u> | 811 | 239 | 810 | 239 | 16 | 740 | 261 | 741 | 261 | <u>741</u> | <u>261</u> |
| 462.libquantum | 16 | <u>409</u> | <u>810</u> | 409 | 811 | 409 | 810 | 16 | 408 | 812 | <u>407</u> | <u>814</u> | 407 | 815 |
| 464.h264ref | 16 | 1070 | 331 | 1041 | 340 | <u>1050</u> | <u>337</u> | 16 | 1070 | 331 | 1041 | 340 | <u>1050</u> | <u>337</u> |
| 471.omnetpp | 16 | 520 | 192 | <u>512</u> | <u>195</u> | 510 | 196 | 16 | 470 | 213 | 469 | 213 | <u>470</u> | <u>213</u> |
| 473.astar | 16 | 674 | 167 | <u>672</u> | <u>167</u> | 672 | 167 | 16 | 604 | 186 | <u>604</u> | <u>186</u> | 603 | 186 |
| 483.xalancbmk | 16 | <u>383</u> | <u>288</u> | 383 | 289 | 383 | 288 | 16 | <u>383</u> | <u>288</u> | 383 | 289 | 383 | 288 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

BIOS configuration:
Data Reuse Optimization = Disabled

General Notes

This result was measured on the PRIMERGY RX300 S6. The PRIMERGY RX300 S6 and the PRIMERGY TX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m32

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl1.1/libicl1.1-32bit -lsmarheap

Base Other Flags

C benchmarks:
403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc -m32

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):
icpc -m32

473.astar: icpc -m64



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Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -ansi-alias
401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32
403.gcc: basepeak = yes
429.mcf: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2
-ipo -no-prec-div -ansi-alias
456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2
-ansi-alias -auto-ilp32
458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)
-prof-use(pass 2) -unroll4 -auto-ilp32
462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-prefetch
464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap
473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs

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Peak Optimization Flags (Continued)

473.astar (continued):

`-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmarthearp64`

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100330.01.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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