



SPEC® CFP2006 Result

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Fujitsu

SPECfp®2006 = 29.3

PRIMERGY TX300 S6, Intel Xeon E5507, 2.26 GHz

SPECfp_base2006 = 27.4

CPU2006 license: 19

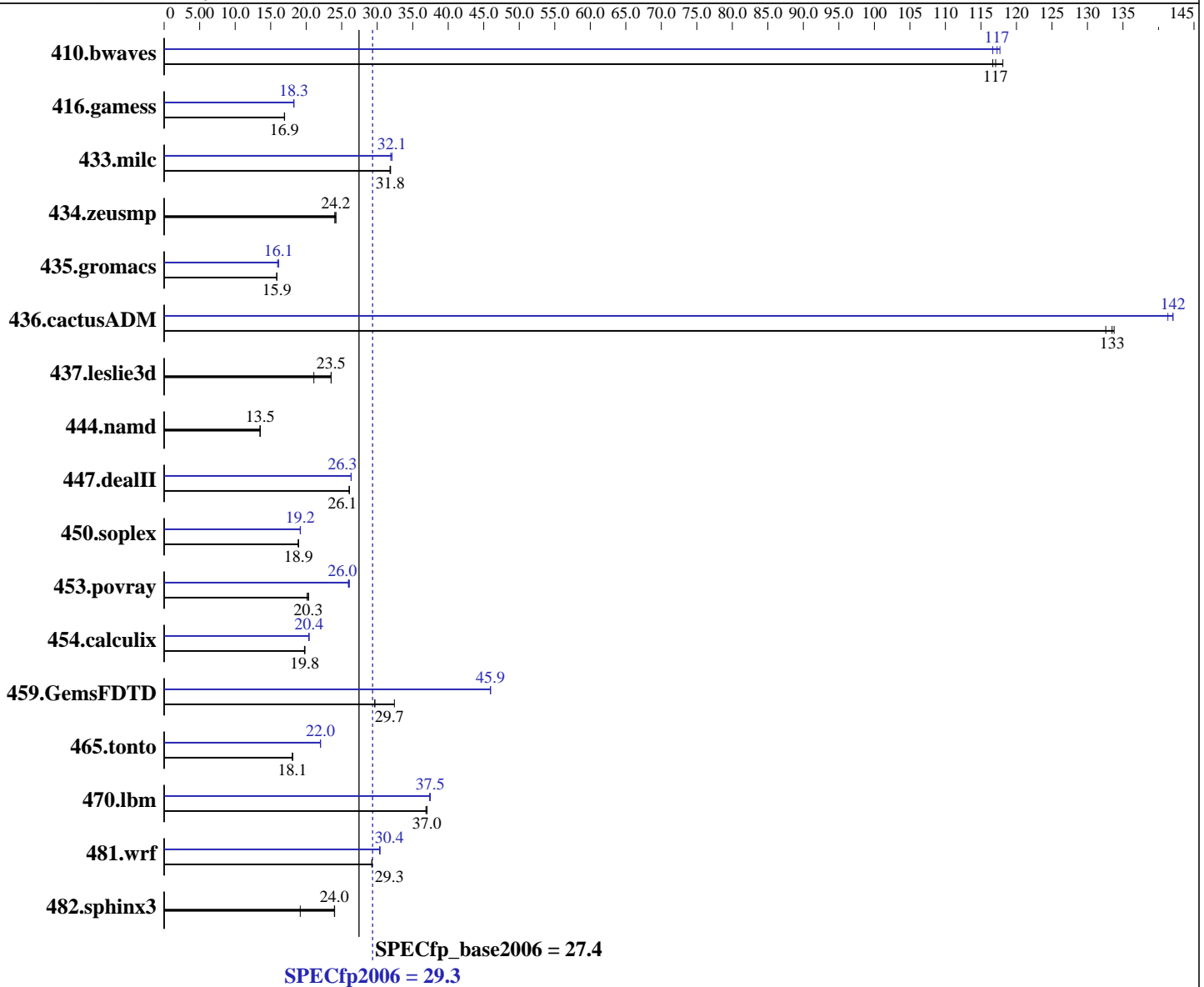
Test date: Feb-2010

Test sponsor: Fujitsu

Hardware Availability: Apr-2010

Tested by: Fujitsu

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5507
 CPU Characteristics:
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64), Kernel 2.6.27.19-5-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: Yes
 File System: ext3
 System State: Multi-User Run Level 3

Continued on next page



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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 96 GB (12x8 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)
Disk Subsystem: 1 x SATA, 160 GB, 5.4 krpm
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
|---------------|-------------|-------------|-------------|-------------|------------|-------------|-------------|-------------|------------|-------------|------------|-------------|
| | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 116 | 117 | 116 | 117 | 115 | 118 | 115 | 118 | 116 | 117 | 116 | 117 |
| 416.gamess | 1156 | 16.9 | 1154 | 17.0 | 1156 | 16.9 | 1072 | 18.3 | 1071 | 18.3 | 1073 | 18.2 |
| 433.milc | 288 | 31.8 | 289 | 31.8 | 288 | 31.9 | 286 | 32.1 | 288 | 31.9 | 286 | 32.1 |
| 434.zeusmp | 376 | 24.2 | 376 | 24.2 | 379 | 24.0 | 376 | 24.2 | 376 | 24.2 | 379 | 24.0 |
| 435.gromacs | 450 | 15.9 | 450 | 15.9 | 450 | 15.9 | 444 | 16.1 | 447 | 16.0 | 443 | 16.1 |
| 436.cactusADM | 90.1 | 133 | 89.5 | 133 | 89.3 | 134 | 84.1 | 142 | 84.1 | 142 | 84.6 | 141 |
| 437.leslie3d | 400 | 23.5 | 446 | 21.1 | 400 | 23.5 | 400 | 23.5 | 446 | 21.1 | 400 | 23.5 |
| 444.namd | 593 | 13.5 | 593 | 13.5 | 594 | 13.5 | 593 | 13.5 | 593 | 13.5 | 594 | 13.5 |
| 447.dealII | 439 | 26.1 | 439 | 26.1 | 439 | 26.1 | 434 | 26.3 | 434 | 26.3 | 435 | 26.3 |
| 450.soplex | 441 | 18.9 | 442 | 18.9 | 441 | 18.9 | 434 | 19.2 | 435 | 19.2 | 435 | 19.2 |
| 453.povray | 262 | 20.3 | 264 | 20.1 | 261 | 20.4 | 204 | 26.1 | 205 | 26.0 | 205 | 25.9 |
| 454.calculix | 416 | 19.8 | 417 | 19.8 | 418 | 19.8 | 405 | 20.4 | 404 | 20.4 | 404 | 20.4 |
| 459.GemsFDTD | 358 | 29.7 | 358 | 29.7 | 327 | 32.4 | 231 | 45.9 | 231 | 45.9 | 231 | 46.0 |
| 465.tonto | 544 | 18.1 | 544 | 18.1 | 545 | 18.1 | 447 | 22.0 | 447 | 22.0 | 447 | 22.0 |
| 470.lbm | 373 | 36.9 | 371 | 37.0 | 372 | 37.0 | 367 | 37.5 | 367 | 37.5 | 368 | 37.4 |
| 481.wrf | 382 | 29.3 | 381 | 29.3 | 382 | 29.2 | 367 | 30.4 | 368 | 30.3 | 368 | 30.4 |
| 482.sphinx3 | 812 | 24.0 | 813 | 24.0 | 1017 | 19.2 | 812 | 24.0 | 813 | 24.0 | 1017 | 19.2 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

Platform Notes

The system automatically configures the memory to run at 800 MHz.
BIOS configuration:
Data Reuse Optimization = Disable

General Notes

OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to granularity=fine,scatter
KMP_STACKSIZE set to 200M

Continued on next page



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General Notes (Continued)

This result was measured on the PRIMERGY TX300 S6. The PRIMERGY TX300 S6 and the PRIMERGY RX300 S6 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Continued on next page



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Base Optimization Flags (Continued)

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias`

470.lbm: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32`

482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealIII: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep- -auto-ilp32`

Continued on next page



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Peak Optimization Flags (Continued)

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.00.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.00.xml>

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