



SPEC® CFP2006 Result

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Supermicro

Motherboard X8DTN+ (Intel Xeon E5620, 2.40 GHz)

SPECfp®2006 = 36.9

SPECfp_base2006 = 34.5

CPU2006 license: 001176

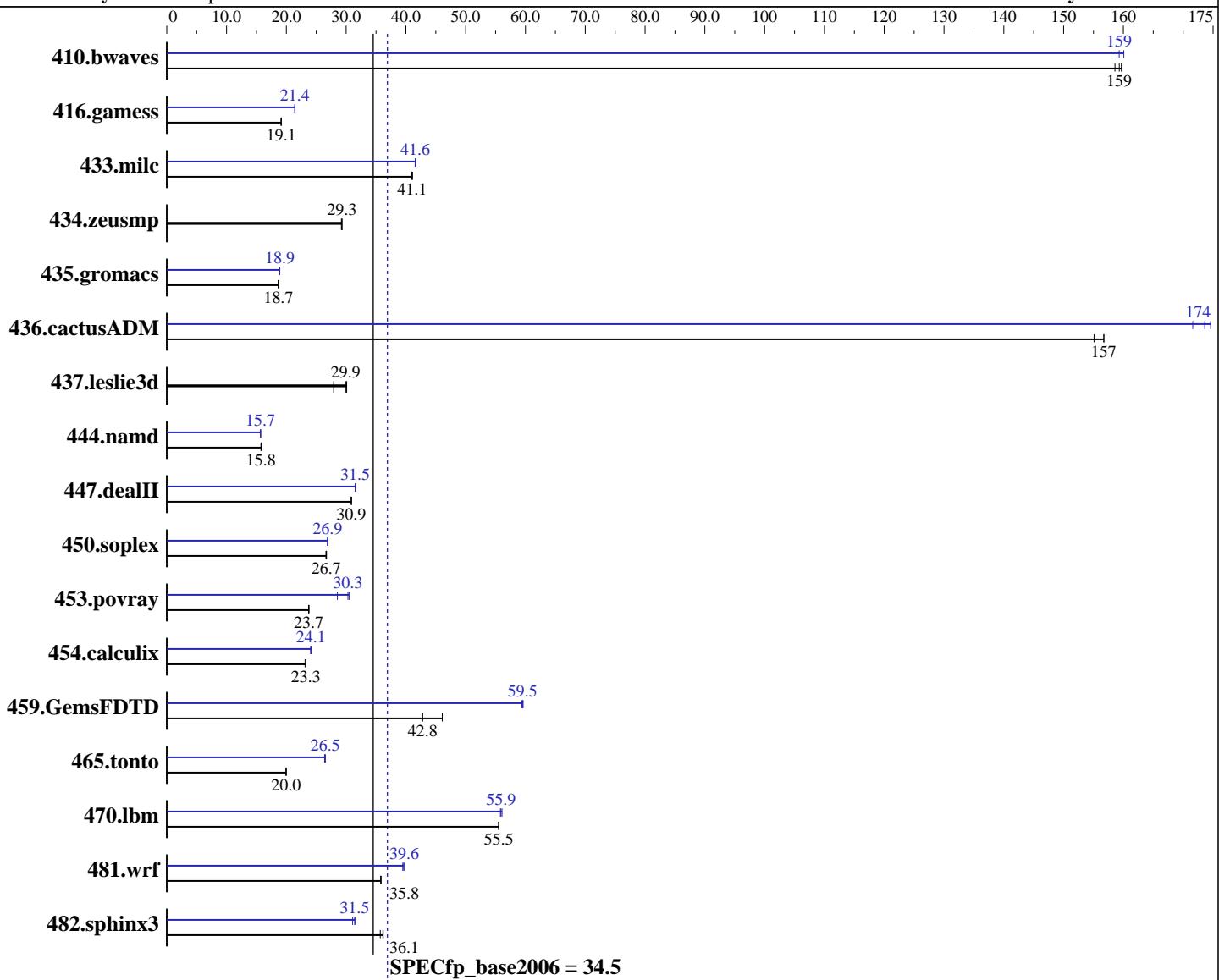
Test date: Apr-2010

Test sponsor: Supermicro

Hardware Availability: Mar-2010

Tested by: Supermicro

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5620
CPU Characteristics: Intel Turbo Boost Technology up to 2.67 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 256 KB I+D on chip per core

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64)
Compiler: Kernel 2.6.27.19-5-default
Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1
Build 20091130 Package ID: l_cproc_p_11.1.064,
l_cprof_p_11.1.064
Auto Parallel: Yes
File System: ReiserFS
System State: Run level 3 (multi-user)

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L3 Cache: 12 MB I+D on chip per chip
 Other Cache: None
 Memory: 48 GB (12 x 4 GB DDR3-1066 RDIMM, ECC, CL7)
 Disk Subsystem: 1 x 500 GB SATA II, 7200 RPM
 Other Hardware: None

Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	85.7	159	85.1	160	85.3	159	85.3	159	84.9	160	85.5	159
416.gamess	1023	19.1	1023	19.1	1023	19.1	914	21.4	914	21.4	913	21.4
433.milc	223	41.1	224	41.0	224	41.1	221	41.6	220	41.6	221	41.5
434.zeusmp	312	29.2	311	29.3	311	29.3	312	29.2	311	29.3	311	29.3
435.gromacs	382	18.7	383	18.7	382	18.7	378	18.9	378	18.9	378	18.9
436.cactusADM	76.2	157	76.2	157	77.0	155	68.4	175	69.6	172	68.8	174
437.leslie3d	337	27.9	314	29.9	312	30.1	337	27.9	314	29.9	312	30.1
444.namd	509	15.8	508	15.8	509	15.8	511	15.7	511	15.7	511	15.7
447.dealII	371	30.9	371	30.9	370	30.9	363	31.5	363	31.5	363	31.5
450.soplex	313	26.7	312	26.7	313	26.6	310	26.9	310	26.9	310	26.9
453.povray	224	23.7	224	23.8	224	23.7	176	30.3	186	28.5	174	30.5
454.calculix	357	23.1	354	23.3	354	23.3	343	24.1	342	24.1	342	24.1
459.GemsFDTD	230	46.1	248	42.8	248	42.8	178	59.6	178	59.5	179	59.4
465.tonto	493	20.0	493	20.0	493	20.0	371	26.5	371	26.5	373	26.4
470.lbm	247	55.6	248	55.5	247	55.5	246	55.9	245	56.1	246	55.8
481.wrf	312	35.8	312	35.8	312	35.8	283	39.5	282	39.6	282	39.7
482.sphinx3	539	36.1	546	35.7	538	36.2	627	31.1	620	31.5	620	31.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
 KMP_AFFINITY set to granularity=fine,scatter
 KMP_STACKSIZE set to 200M

Platform Notes

Fan speed set to Full Speed in BIOS Setup.
 As tested, the system used a Supermicro
 PWS-865-PQ power supply, 2 SNK-P0038P heatsinks,
 and 2 FAN-0077L cooling fans.



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General Notes

Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

Base Compiler Invocation

C benchmarks:

 icc -m64

C++ benchmarks:

 icpc -m64

Fortran benchmarks:

 ifort -m64

Benchmarks using both Fortran and C:

 icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:

 -xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-parallel -ansi-alias -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32
-unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep -auto-ilp32

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Peak Optimization Flags (Continued)

450.soplex: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3 -auto-ilp32

453.povray: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll14 -ansi-alias

Fortran benchmarks:

410.bwaves: -xsse4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-parallel

416.gamess: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -Ob0 -opt-prefetch -parallel

465.tonto: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-inline-calloc -opt-malloc-options=3 -auto -unroll14

Benchmarks using both Fortran and C:

435.gromacs: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: -xsse4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll12 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xsse4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.html>



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You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100316.xml>

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