



# SPEC® CINT2006 Result

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## Fujitsu

SPECint®\_rate2006 = 165

PRIMERGY RX600 S5, Intel Xeon E7520, 1.86 GHz

SPECint\_rate\_base2006 = 156

CPU2006 license: 19

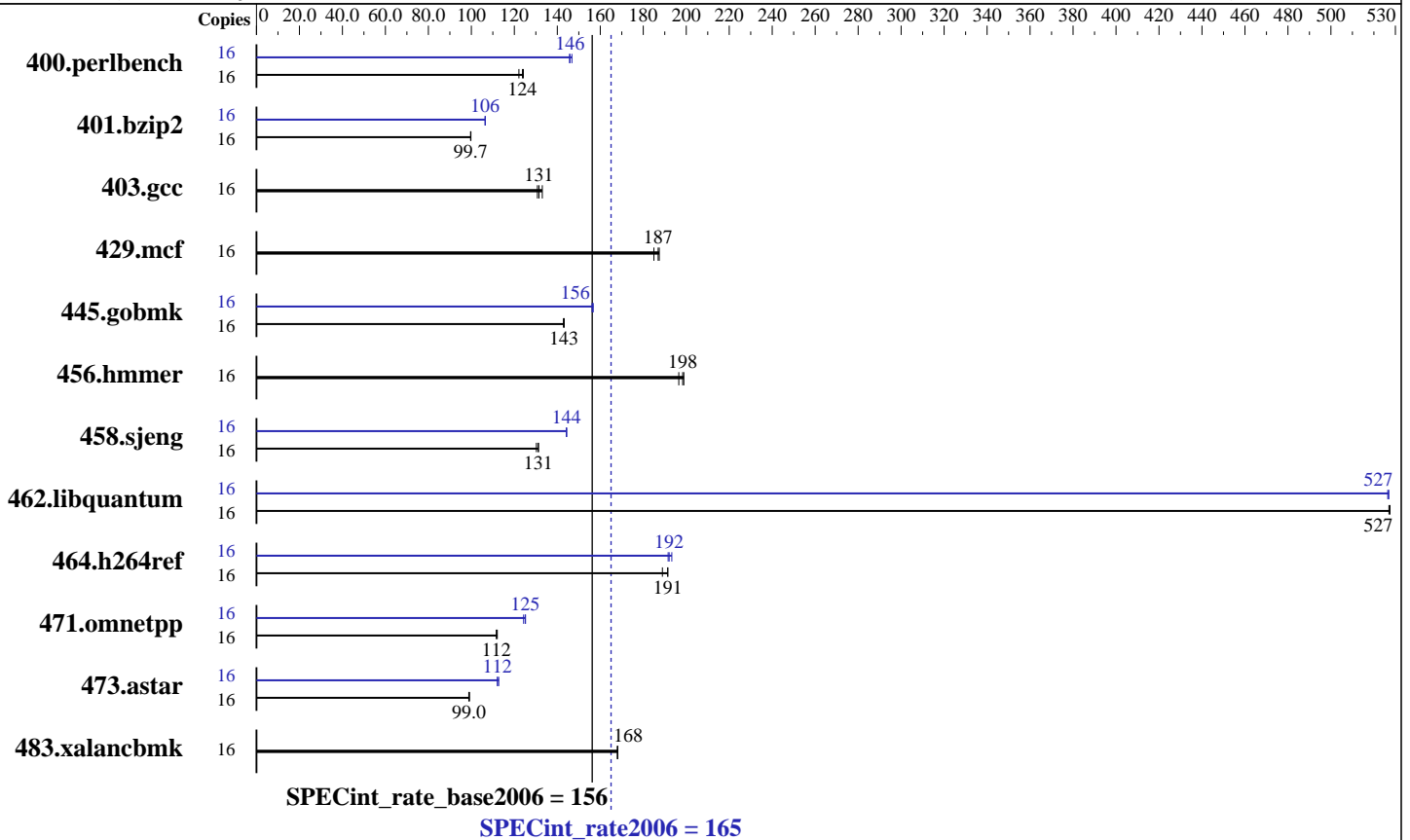
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Jul-2010

Hardware Availability: Jun-2010

Software Availability: Jan-2010



### Hardware

CPU Name: Intel Xeon E7520  
 CPU Characteristics: 1867  
 CPU MHz: 1867  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,3,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 18 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (32x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC, see add'l detail in notes)  
 Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 11 (x86\_64), Kernel 2.6.27.19-5-default  
 Compiler: Intel C++ Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l\_cproc\_p\_11.1.064  
 Auto Parallel: No  
 File System: ext3  
 System State: Multi-User Run Level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V8.1



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1259	124	<b><u>1263</u></b>	<b><u>124</u></b>	1280	122	16	1065	147	<b><u>1071</u></b>	<b><u>146</u></b>	1073	146
401.bzip2	16	<b><u>1549</u></b>	<b><u>99.7</u></b>	1547	99.8	1549	99.7	16	1451	106	<b><u>1450</u></b>	<b><u>106</u></b>	1450	106
403.gcc	16	968	133	985	131	<b><u>980</u></b>	<b><u>131</u></b>	16	968	133	985	131	<b><u>980</u></b>	<b><u>131</u></b>
429.mcf	16	778	187	789	185	<b><u>781</u></b>	<b><u>187</u></b>	16	778	187	789	185	<b><u>781</u></b>	<b><u>187</u></b>
445.gobmk	16	<b><u>1173</u></b>	<b><u>143</u></b>	1172	143	1175	143	16	1075	156	<b><u>1073</u></b>	<b><u>156</u></b>	1071	157
456.hammer	16	<b><u>753</u></b>	<b><u>198</u></b>	751	199	760	197	16	<b><u>753</u></b>	<b><u>198</u></b>	751	199	760	197
458.sjeng	16	1475	131	<b><u>1477</u></b>	<b><u>131</u></b>	1486	130	16	<b><u>1342</u></b>	<b><u>144</u></b>	1341	144	1343	144
462.libquantum	16	629	527	<b><u>629</u></b>	<b><u>527</u></b>	629	527	16	630	526	629	527	<b><u>629</u></b>	<b><u>527</u></b>
464.h264ref	16	1850	191	1874	189	<b><u>1850</u></b>	<b><u>191</u></b>	16	1848	192	<b><u>1844</u></b>	<b><u>192</u></b>	1832	193
471.omnetpp	16	<b><u>895</u></b>	<b><u>112</u></b>	892	112	896	112	16	799	125	<b><u>800</u></b>	<b><u>125</u></b>	804	124
473.astar	16	1137	98.8	1133	99.1	<b><u>1135</u></b>	<b><u>99.0</u></b>	16	1002	112	<b><u>1001</u></b>	<b><u>112</u></b>	996	113
483.xalancbmk	16	657	168	657	168	<b><u>657</u></b>	<b><u>168</u></b>	16	657	168	657	168	<b><u>657</u></b>	<b><u>168</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## Platform Notes

The system automatically configures the memory to run at 800 MHz.

## General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>  
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502

## Base Compiler Invocation

C benchmarks:  
icc -m32

C++ benchmarks:  
icpc -m32



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## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.icl11.1/libic11.1-32bit -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

401.bzip2: icc -m64

458.sjeng: icc -m64

462.libquantum: icc -m64

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

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## Peak Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -ansi-alias

401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -opt-prefetch -ansi-alias -auto-ilp32

403.gcc: basepeak = yes

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2) -O2  
-ipo -no-prec-div -ansi-alias

456.hmmer: basepeak = yes

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll4 -auto-ilp32

462.libquantum: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32  
-opt-prefetch

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -static(pass 2)  
-prof-use(pass 2) -unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-32bit -lsmartheap

473.astar: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs  
-L/home/cmplr/usr3/alrahate/cpu2006.1.1.ic11.1/libic11.1-64bit -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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