



SPEC[®] CFP2006 Result

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Fujitsu

SPECfp[®]_rate2006 = 63.9

PRIMERGY BX620 S6, Intel Xeon E5507, 2.26 GHz

SPECfp_rate_base2006 = 62.0

CPU2006 license: 19

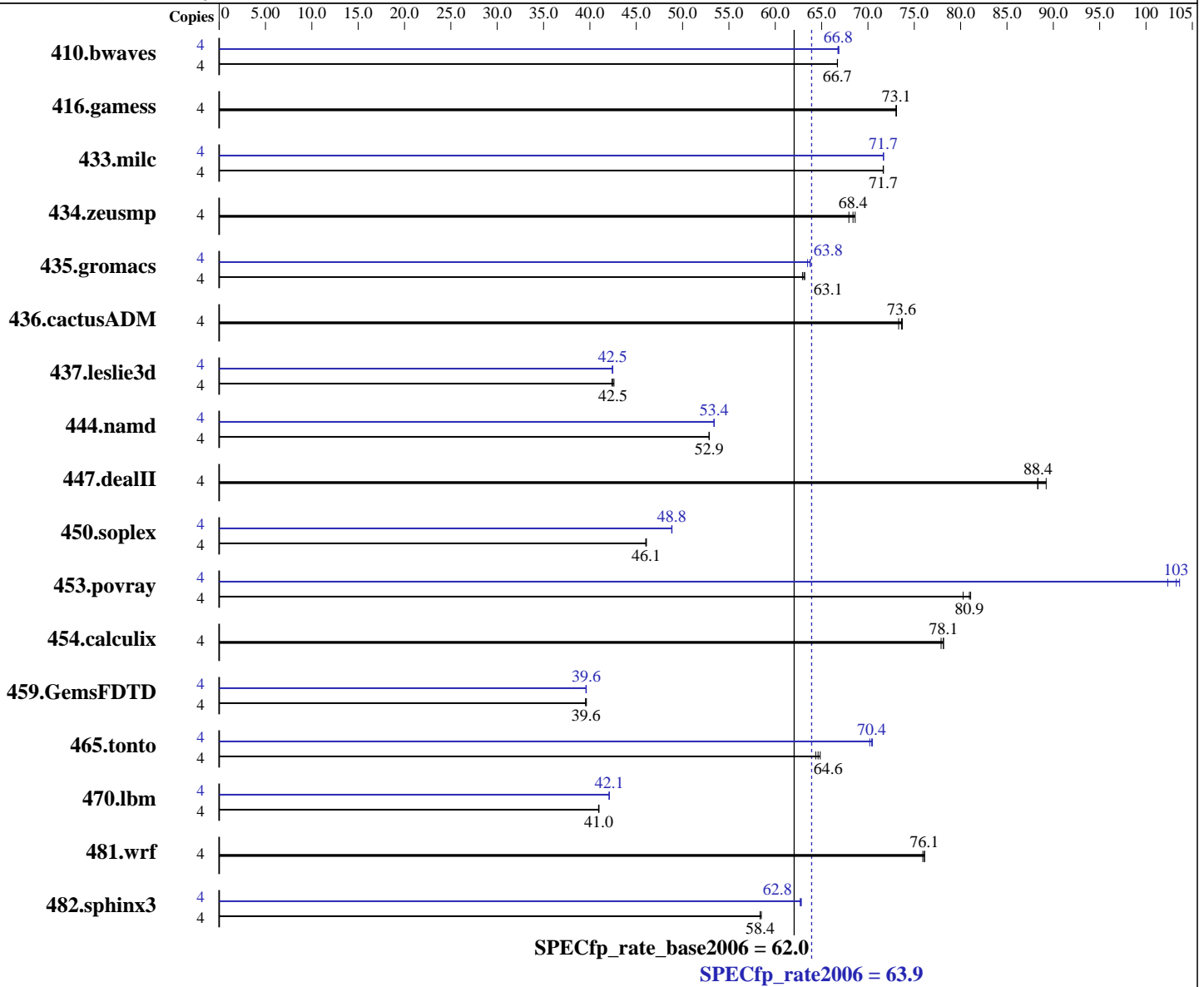
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Sep-2010

Hardware Availability: Jul-2010

Software Availability: Jan-2010



Hardware

CPU Name: Intel Xeon E5507
 CPU Characteristics:
 CPU MHz: 2267
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 11 (x86_64) with SP1, Kernel 2.6.32.12-0.7-default
 Compiler: Intel C++ and Fortran Professional Compiler for IA32 and Intel 64, Version 11.1 Build 20091130 Package ID: l_cproc_p_11.1.064, l_cprof_p_11.1.064
 Auto Parallel: No
 File System: ext3
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 4 MB I+D on chip per chip
Other Cache: None
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R, ECC, running at 800 MHz and CL6)
Disk Subsystem: 1 x SAS, 300 GB, 10000 RPM
Other Hardware: --

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	815	66.7	815	66.7	815	66.7	4	814	66.8	813	66.9	814	66.8
416.gamess	4	1072	73.1	1072	73.0	1072	73.1	4	1072	73.1	1072	73.0	1072	73.1
433.milc	4	513	71.6	512	71.7	512	71.7	4	512	71.7	512	71.7	512	71.7
434.zeusmp	4	536	67.9	532	68.4	530	68.6	4	536	67.9	532	68.4	530	68.6
435.gromacs	4	452	63.2	453	63.1	454	62.9	4	448	63.8	448	63.8	450	63.5
436.cactusADM	4	652	73.3	648	73.7	649	73.6	4	652	73.3	648	73.7	649	73.6
437.leslie3d	4	887	42.4	883	42.6	885	42.5	4	886	42.5	887	42.4	885	42.5
444.namd	4	607	52.9	607	52.9	607	52.9	4	601	53.4	601	53.4	601	53.4
447.dealII	4	518	88.4	513	89.2	518	88.3	4	518	88.4	513	89.2	518	88.3
450.soplex	4	724	46.1	725	46.0	724	46.1	4	683	48.9	683	48.8	683	48.8
453.povray	4	263	80.9	262	81.1	265	80.3	4	208	102	205	104	206	103
454.calculix	4	424	77.9	422	78.2	422	78.1	4	424	77.9	422	78.2	422	78.1
459.GemsFDTD	4	1074	39.5	1071	39.6	1072	39.6	4	1072	39.6	1073	39.6	1071	39.6
465.tonto	4	607	64.9	609	64.6	611	64.4	4	559	70.4	561	70.2	558	70.5
470.lbm	4	1343	40.9	1342	41.0	1341	41.0	4	1306	42.1	1306	42.1	1306	42.1
481.wrf	4	589	75.9	587	76.1	587	76.1	4	589	75.9	587	76.1	587	76.1
482.sphinx3	4	1333	58.5	1336	58.4	1336	58.4	4	1242	62.8	1241	62.8	1244	62.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

For information about Fujitsu please visit: <http://www.fujitsu.com>
Binaries were compiled on SLES 10 with Binutils 2.18.50.0.7.20080502



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Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:
-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:
-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -fno-alias -opt-prefetch

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
 -opt-malloc-options=3 -ansi-alias -auto-ilp32

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Peak Optimization Flags (Continued)

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div -static

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto -inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.1-linux64-revE.20100708.xml>

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For other inquiries, please contact webmaster@spec.org.

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