SPEC® CINT2006 Result

IBM Corporation

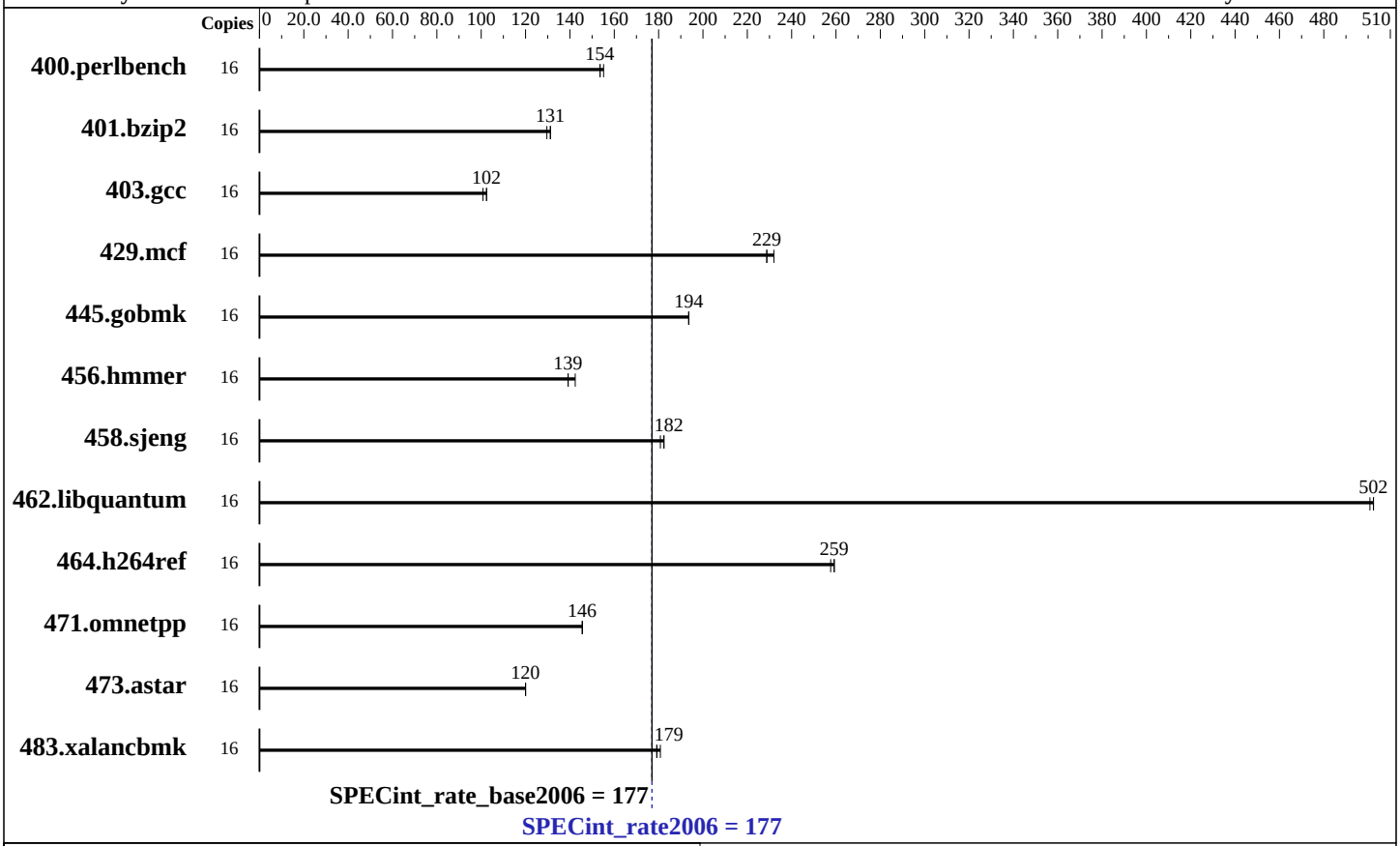
IBM System x3650 M3 (Intel Xeon E5620)

SPECint®_rate2006 = 177

SPECint_rate_base2006 = 177

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Nov-2010
Hardware Availability: Jun-2010
Software Availability: Jan-2010



Hardware

CPU Name:	Intel Xeon E5620
CPU Characteristics:	Intel Turbo Boost Technology up to 2.66 GHz
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	12 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (4 x 4 GB 2Rx4 PC3-10600R, ECC, running at 1066 MHz and CL7)
Disk Subsystem:	2 x 146 GB SAS, 10000 RPM, RAID 1
Other Hardware:	None

Software

Operating System:	Micorsoft Windows 2003 Enterprise R2 SP2 (64-bit)
Compiler:	Intel C++ Compiler Professional 11.1 for IA32 Build 20090903 Package ID: w_cproc_p_11.1.045 Microsoft Visual Studio 2008 Professional SP1 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None SmartHeap Library Version 8.1 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	16	1012	155	1015	154	**1014**	**154**	16	1012	155	1015	154	**1014**	**154**
401.bzip2	16	1195	130	1176	131	**1176**	**131**	16	1195	130	1176	131	**1176**	**131**
403.gcc	16	**1265**	**102**	1248	102	1276	101	16	**1265**	**102**	1248	102	1276	101
429.mcf	16	638	229	**637**	**229**	629	232	16	638	229	**637**	**229**	629	232
445.gobmk	16	867	194	**869**	**194**	869	194	16	867	194	**869**	**194**	869	194
456.hmmer	16	**1069**	**139**	1043	142	1072	139	16	**1069**	**139**	1043	142	1072	139
458.sjeng	16	1065	182	**1065**	**182**	1068	181	16	1065	182	**1065**	**182**	1068	181
462.libquantum	16	661	501	660	502	**660**	**502**	16	661	501	660	502	**660**	**502**
464.h264ref	16	1366	259	1371	258	**1369**	**259**	16	1366	259	1371	258	**1369**	**259**
471.omnetpp	16	686	146	**686**	**146**	686	146	16	686	146	**686**	**146**	686	146
473.astar	16	**934**	**120**	934	120	931	120	16	**934**	**120**	934	120	931	120
483.xalancbmk	16	614	179	**614**	**179**	613	181	16	614	179	**614**	**179**	613	181

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Platform Notes

```
BIOS Settings:
Turbo Mode Enable
Turbo Boost set to Traditional
CPU C State Enable
Data Reuse Disable
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DWIN32 -DSPEC_CPU_NO_INTTYPES`

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Base Portability Flags (Continued)

483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000`

C++ benchmarks:
`-QxSSE4.2 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx-features /F512000000 shlW32M.lib -link /FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Optimization Flags

C benchmarks:

400.perlbench: `basepeak = yes`

401.bzip2: `basepeak = yes`

403.gcc: `basepeak = yes`

429.mcf: `basepeak = yes`

445.gobmk: `basepeak = yes`

456.hmmer: `basepeak = yes`

458.sjeng: `basepeak = yes`

462.libquantum: `basepeak = yes`

464.h264ref: `basepeak = yes`

C++ benchmarks:

471.omnetpp: `basepeak = yes`

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Peak Optimization Flags (Continued)

473.astar: `basepeak = yes`

483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-winx64-revA.20100302.02.xml`

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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