



# SPEC<sup>®</sup> CFP2006 Result

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## IBM Corporation

**SPECfp<sup>®</sup>2006 = 39.1**

IBM System x iDataPlex dx360 M3 (Intel Xeon E5606)

**SPECfp\_base2006 = 36.8**

CPU2006 license: 11

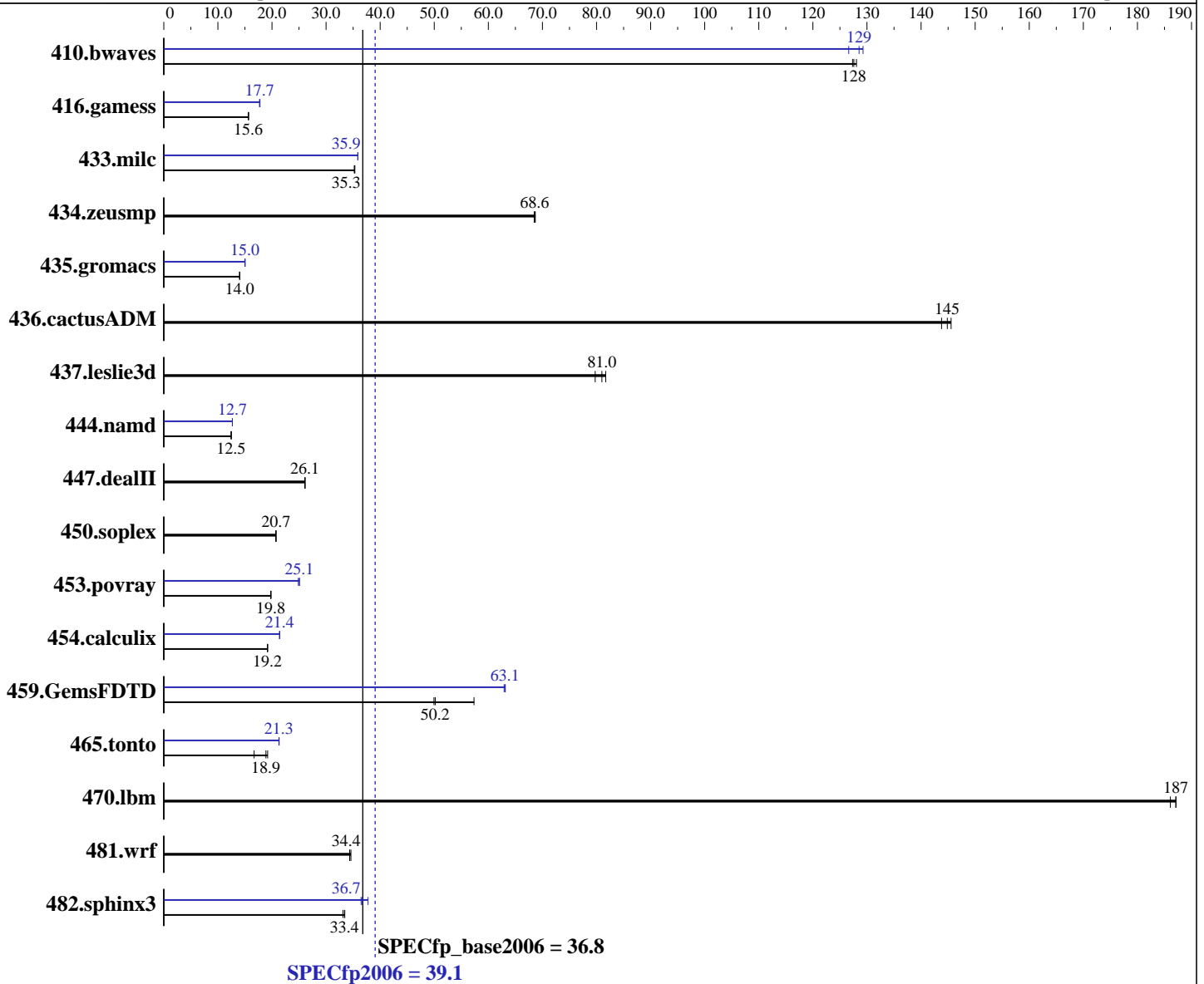
Test date: Apr-2011

Test sponsor: IBM Corporation

Hardware Availability: Feb-2011

Tested by: IBM Corporation

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon E5606  
 CPU Characteristics:  
 CPU MHz: 2133  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1067 MHz)  
 Disk Subsystem: 1 x 146 GB SAS, 15000 RPM  
 Other Hardware: None

Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	106	128	<b>107</b>	<b>128</b>	107	127	<b>106</b>	<b>129</b>	107	127	105	129
416.gamess	1253	15.6	1249	15.7	<b>1253</b>	<b>15.6</b>	<b>1104</b>	<b>17.7</b>	1106	17.7	1101	17.8
433.milc	<b>260</b>	<b>35.3</b>	261	35.2	260	35.3	<b>256</b>	<b>35.9</b>	256	35.9	256	35.9
434.zeusmp	132	68.7	133	68.5	<b>133</b>	<b>68.6</b>	132	68.7	133	68.5	<b>133</b>	<b>68.6</b>
435.gromacs	<b>509</b>	<b>14.0</b>	508	14.1	510	14.0	<b>475</b>	<b>15.0</b>	475	15.0	477	15.0
436.cactusADM	<b>82.5</b>	<b>145</b>	82.1	146	83.1	144	<b>82.5</b>	<b>145</b>	82.1	146	83.1	144
437.leslie3d	115	81.7	118	79.8	<b>116</b>	<b>81.0</b>	115	81.7	118	79.8	<b>116</b>	<b>81.0</b>
444.namd	643	12.5	<b>644</b>	<b>12.5</b>	644	12.5	632	12.7	<b>632</b>	<b>12.7</b>	633	12.7
447.dealII	<b>438</b>	<b>26.1</b>	438	26.1	439	26.1	<b>438</b>	<b>26.1</b>	438	26.1	439	26.1
450.soplex	402	20.8	403	20.7	<b>402</b>	<b>20.7</b>	402	20.8	403	20.7	<b>402</b>	<b>20.7</b>
453.povray	268	19.8	269	19.8	<b>269</b>	<b>19.8</b>	<b>212</b>	<b>25.1</b>	212	25.1	214	24.9
454.calculix	<b>430</b>	<b>19.2</b>	431	19.1	430	19.2	385	21.4	385	21.4	<b>385</b>	<b>21.4</b>
459.GemsFDTD	212	50.0	185	57.4	<b>211</b>	<b>50.2</b>	<b>168</b>	<b>63.1</b>	168	63.1	169	62.9
465.tonto	513	19.2	<b>521</b>	<b>18.9</b>	589	16.7	462	21.3	<b>462</b>	<b>21.3</b>	461	21.3
470.lbm	73.8	186	<b>73.4</b>	<b>187</b>	73.4	187	73.8	186	<b>73.4</b>	<b>187</b>	73.4	187
481.wrf	323	34.6	325	34.4	<b>325</b>	<b>34.4</b>	323	34.6	325	34.4	<b>325</b>	<b>34.4</b>
482.sphinx3	589	33.1	<b>584</b>	<b>33.4</b>	582	33.5	<b>531</b>	<b>36.7</b>	516	37.8	534	36.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

Power C-State enabled in BIOS  
 Data Reuse disabled in BIOS  
 Demand Scrub disabled in BIOS



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## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries compiled on RHEL5.5

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

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## Base Optimization Flags (Continued)

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

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## Peak Optimization Flags (Continued)

447.deallI: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/IBM-platform-linux64-revA.20110420.xml>



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