



# SPEC<sup>®</sup> CFP2006 Result

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## NEC Corporation

SPECfp<sup>®</sup>\_rate2006 = **254**

### Express5800/R120b-2 (Intel Xeon X5650)

SPECfp\_rate\_base2006 = 246

CPU2006 license: 9006

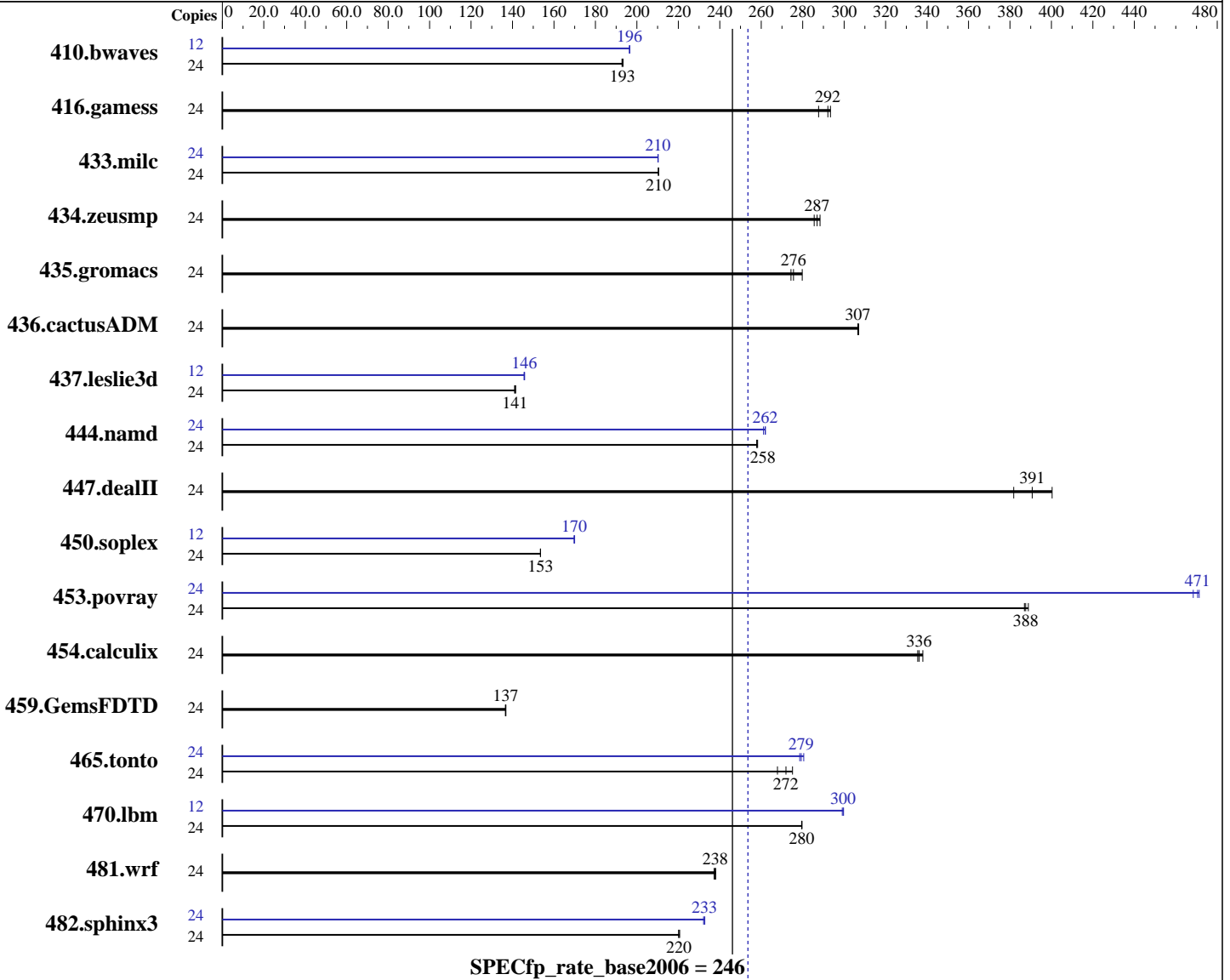
Test date: Jun-2011

Test sponsor: NEC Corporation

Hardware Availability: Feb-2011

Tested by: NEC Corporation

Software Availability: Mar-2011



### Hardware

CPU Name: Intel Xeon X5650  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.06 GHz  
 CPU MHz: 2667  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64, Version 12.0.3.174 Build 20110309  
 Auto Parallel: No  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 96 GB (12 x 8 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	24	1687	193	<b>1689</b>	<b>193</b>	1692	193	12	<b>830</b>	<b>196</b>	830	197	830	196		
416.gamess	24	1602	293	1634	288	<b>1609</b>	<b>292</b>	24	1602	293	1634	288	<b>1609</b>	<b>292</b>		
433.milc	24	1047	210	1047	210	<b>1047</b>	<b>210</b>	24	1048	210	1048	210	<b>1048</b>	<b>210</b>		
434.zeusmp	24	757	288	<b>761</b>	<b>287</b>	765	286	24	757	288	<b>761</b>	<b>287</b>	765	286		
435.gromacs	24	612	280	625	274	<b>622</b>	<b>276</b>	24	612	280	625	274	<b>622</b>	<b>276</b>		
436.cactusADM	24	935	307	934	307	<b>935</b>	<b>307</b>	24	935	307	934	307	<b>935</b>	<b>307</b>		
437.leslie3d	24	1594	142	<b>1597</b>	<b>141</b>	1600	141	12	775	146	<b>774</b>	<b>146</b>	773	146		
444.namd	24	<b>746</b>	<b>258</b>	747	258	745	258	24	735	262	<b>735</b>	<b>262</b>	737	261		
447.dealII	24	719	382	686	400	<b>703</b>	<b>391</b>	24	719	382	686	400	<b>703</b>	<b>391</b>		
450.soplex	24	1304	153	1304	154	<b>1304</b>	<b>153</b>	12	<b>589</b>	<b>170</b>	589	170	590	170		
453.povray	24	328	389	<b>329</b>	<b>388</b>	330	387	24	271	471	<b>271</b>	<b>471</b>	273	468		
454.calculix	24	586	338	590	335	<b>589</b>	<b>336</b>	24	586	338	590	335	<b>589</b>	<b>336</b>		
459.GemsFDTD	24	<b>1862</b>	<b>137</b>	1865	137	1861	137	24	<b>1862</b>	<b>137</b>	1865	137	1861	137		
465.tonto	24	882	268	<b>868</b>	<b>272</b>	858	275	24	<b>846</b>	<b>279</b>	848	279	842	281		
470.lbm	24	1179	280	1180	279	<b>1180</b>	<b>280</b>	12	551	299	<b>551</b>	<b>300</b>	550	300		
481.wrf	24	1126	238	1130	237	<b>1128</b>	<b>238</b>	24	1126	238	1130	237	<b>1128</b>	<b>238</b>		
482.sphinx3	24	2120	221	<b>2124</b>	<b>220</b>	2125	220	24	2010	233	<b>2012</b>	<b>233</b>	2015	232		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.  
numactl was used to bind copies to the cores

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run  
Huge pages were not configured for this run

## Platform Notes

BIOS Settings:  
Performance/Watt: Traditional  
Server Class: Custom

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## Platform Notes (Continued)

Data Reuse Optimization: Disabled  
Memory Voltage: Normal

## General Notes

The Express5800/R120b-1 and the Express5800/R120b-2 models are electronically equivalent. The results have been measured on the Express5800/R120b-1 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64



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## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -ansi-alias

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

470.lbm: -DSPEC\_CPU\_LP64

481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-ansi-alias -opt-prefetch -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3  
-B /usr/share/libhugetlbfs/ -Wl,-hugetlbfs-link=BDT

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xSSE4.2 -ipo -O3 -no-prec-div  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

459.GemsFDTD: basepeak = yes

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

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## Peak Optimization Flags (Continued)

465.tonto (continued):

`-B /usr/share/libhugetlbfs/ -Wl,-melf_x86_64 -Wl,-hugetlbfs-link=BDT`

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revF.xml>

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