



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.

SPECfp<sup>®</sup>2006 = 30.4

PowerEdge R410 (Intel Xeon E5603, 1.6 GHz)

SPECfp\_base2006 = 29.2

CPU2006 license: 55

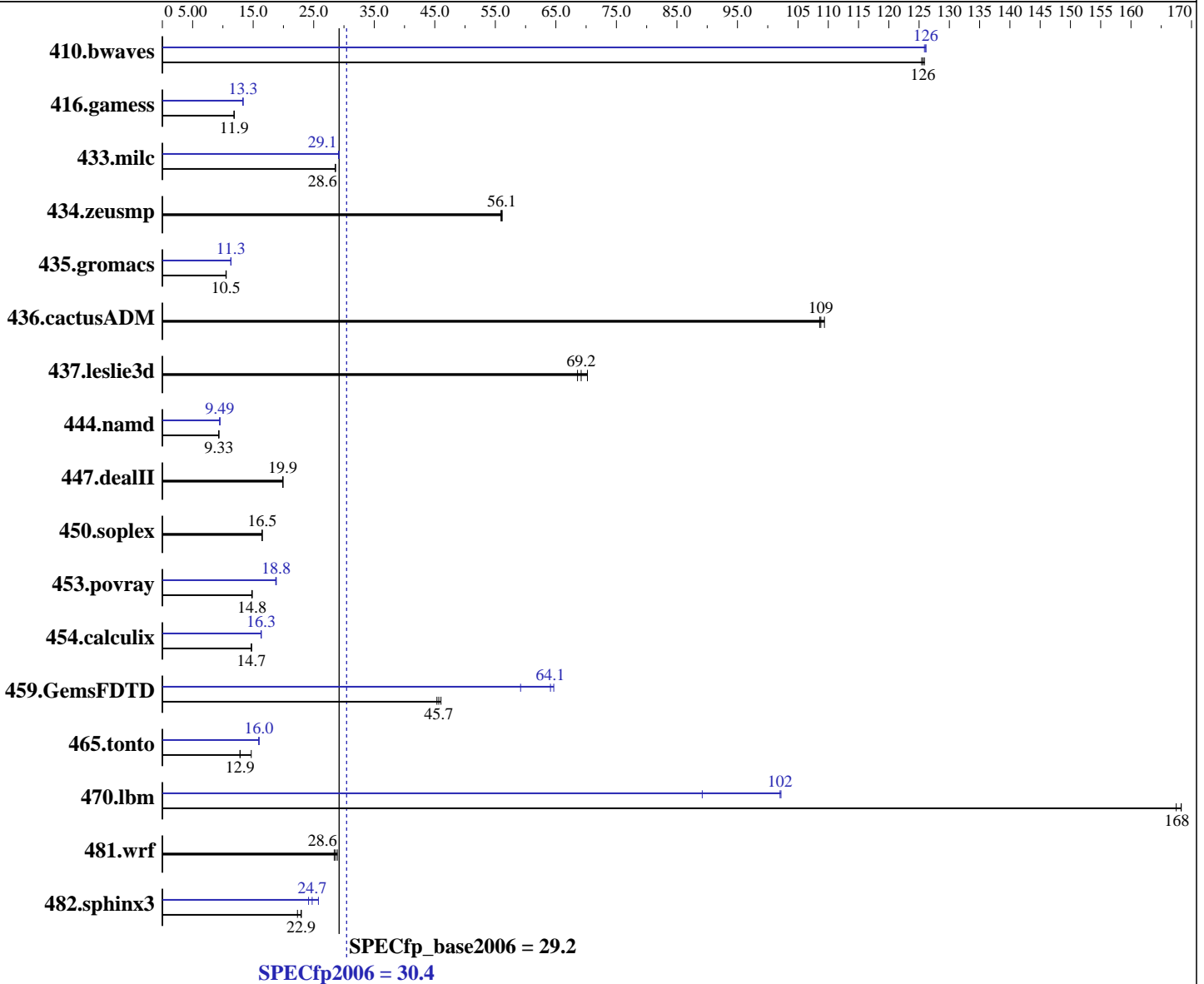
Test date: Mar-2011

Test sponsor: Dell Inc.

Hardware Availability: Feb-2011

Tested by: Dell Inc.

Software Availability: Apr-2011



## Hardware

CPU Name: Intel Xeon E5603  
 CPU Characteristics:  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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## Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 4 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (6 x 8 GB 2Rx4 PC3-10600R-9, ECC, running at 1066 MHz)  
Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	108	125	108	126	<b>108</b>	<b>126</b>	108	126	<b>108</b>	<b>126</b>	108	126
416.gamess	1650	11.9	<b>1651</b>	<b>11.9</b>	1654	11.8	<b>1469</b>	<b>13.3</b>	1469	13.3	1472	13.3
433.milc	322	28.5	<b>321</b>	<b>28.6</b>	321	28.6	315	29.1	315	29.2	<b>315</b>	<b>29.1</b>
434.zeusmp	163	55.9	162	56.1	<b>162</b>	<b>56.1</b>	163	55.9	162	56.1	<b>162</b>	<b>56.1</b>
435.gromacs	680	10.5	<b>679</b>	<b>10.5</b>	676	10.6	<b>630</b>	<b>11.3</b>	630	11.3	631	11.3
436.cactusADM	<b>110</b>	<b>109</b>	109	109	110	109	<b>110</b>	<b>109</b>	109	109	110	109
437.leslie3d	134	70.2	137	68.6	<b>136</b>	<b>69.2</b>	134	70.2	137	68.6	<b>136</b>	<b>69.2</b>
444.namd	<b>859</b>	<b>9.33</b>	861	9.31	859	9.33	845	9.49	845	9.49	<b>845</b>	<b>9.49</b>
447.dealII	575	19.9	574	19.9	<b>574</b>	<b>19.9</b>	575	19.9	574	19.9	<b>574</b>	<b>19.9</b>
450.soplex	506	16.5	505	16.5	<b>506</b>	<b>16.5</b>	506	16.5	505	16.5	<b>506</b>	<b>16.5</b>
453.povray	<b>359</b>	<b>14.8</b>	360	14.8	358	14.9	284	18.7	<b>283</b>	<b>18.8</b>	283	18.8
454.calculix	561	14.7	560	14.7	<b>561</b>	<b>14.7</b>	<b>505</b>	<b>16.3</b>	505	16.3	506	16.3
459.GemsFDTD	<b>232</b>	<b>45.7</b>	234	45.4	231	46.0	<b>165</b>	<b>64.1</b>	179	59.2	164	64.7
465.tonto	670	14.7	<b>766</b>	<b>12.9</b>	768	12.8	617	16.0	<b>617</b>	<b>16.0</b>	617	15.9
470.lbm	82.0	167	81.6	168	<b>81.6</b>	<b>168</b>	134	102	154	89.2	<b>135</b>	<b>102</b>
481.wrf	386	28.9	<b>390</b>	<b>28.6</b>	393	28.4	386	28.9	<b>390</b>	<b>28.6</b>	393	28.4
482.sphinx3	849	23.0	<b>851</b>	<b>22.9</b>	873	22.3	756	25.8	807	24.1	<b>788</b>	<b>24.7</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
```

## Platform Notes

BIOS Settings:  
Power Management = Maximum Performance (Default = Active Power Controller)  
Data Reuse = Disabled (Default = Enabled)



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## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries were compiled on RHEL5.5  
The Dell PowerEdge R410 and  
the Bull NovaScale R430 F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge R410 model.

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

470.lbm: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -parallel  
-ansi-alias -static -auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel

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## Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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