



# SPEC<sup>®</sup> CFP2006 Result

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## Bull SAS

SPECfp<sup>®</sup>2006 = **51.4**

NovaScale T860 F2 (Intel Xeon E5645, 2.40 GHz)

SPECfp\_base2006 = **48.0**

CPU2006 license: 20

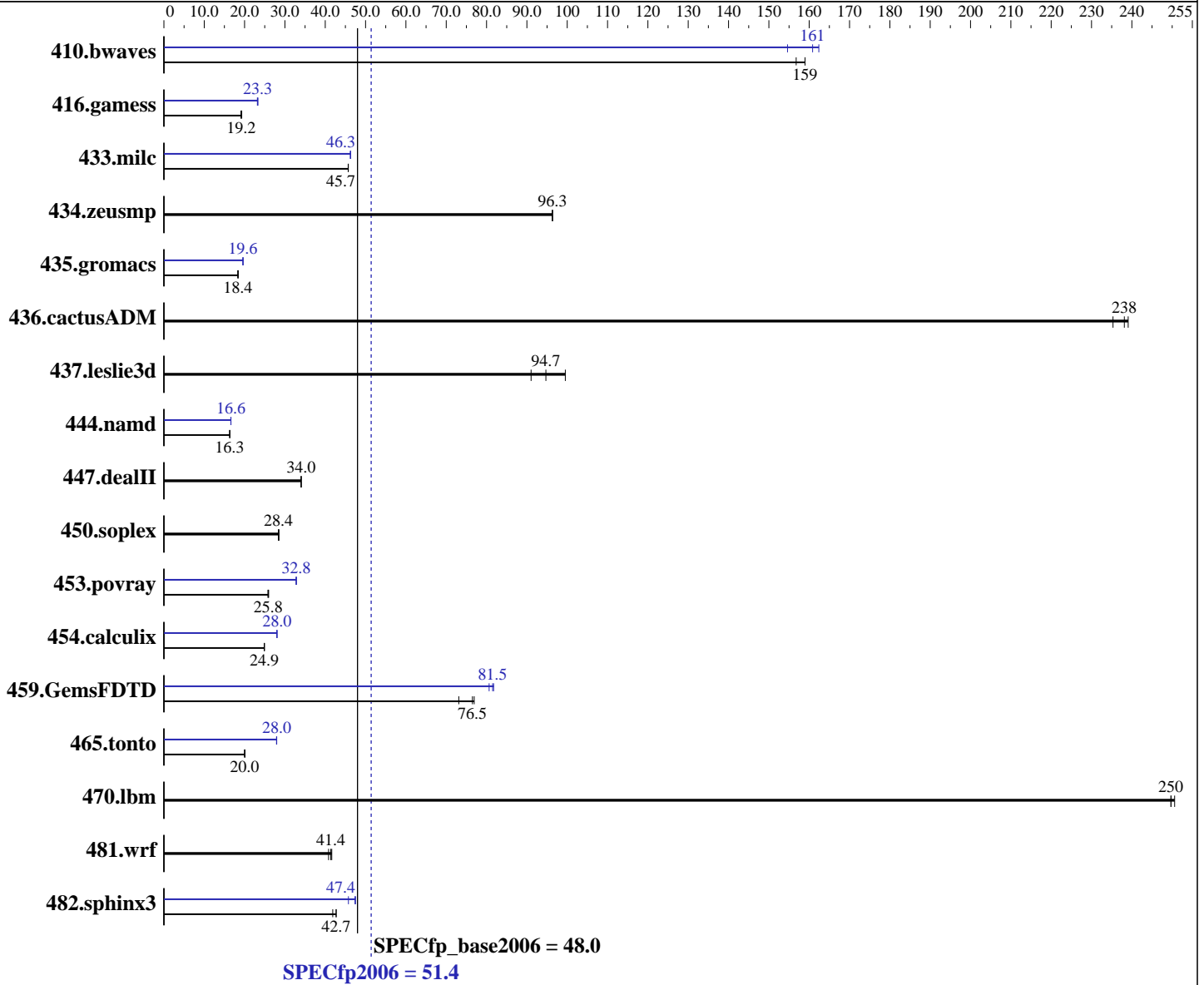
Test sponsor: Bull SAS

Tested by: Dell Inc.

Test date: May-2011

Hardware Availability: Feb-2011

Software Availability: Apr-2011



### Hardware

CPU Name: Intel Xeon E5645  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: SUSE Linux Enterprise Server 11 SP1 (x86\_64), Kernel 2.6.32.12-0.7-default  
 Compiler: Intel C++ and Fortran Intel 64 Compiler XE for applications running on Intel 64 Version 12.0 Update 3  
 Auto Parallel: Yes  
 File System: ext3  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit

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L3 Cache: 12 MB I+D on chip per chip  
Other Cache: None  
Memory: 48 GB (12 x 4 GB 2Rx4 PC3-10600R-9, ECC)  
Disk Subsystem: 1 x 146 GB 15000 RPM SAS  
Other Hardware: None

Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	86.7	157	85.5	159	<b>85.5</b>	<b>159</b>	83.7	162	87.9	155	<b>84.5</b>	<b>161</b>
416.gamess	<b>1022</b>	<b>19.2</b>	1024	19.1	1015	19.3	845	23.2	<b>842</b>	<b>23.3</b>	840	23.3
433.milc	201	45.7	<b>201</b>	<b>45.7</b>	201	45.7	198	46.3	<b>198</b>	<b>46.3</b>	199	46.2
434.zeusmp	94.5	96.3	94.5	96.3	<b>94.5</b>	<b>96.3</b>	94.5	96.3	94.5	96.3	<b>94.5</b>	<b>96.3</b>
435.gromacs	390	18.3	388	18.4	<b>388</b>	<b>18.4</b>	<b>364</b>	<b>19.6</b>	365	19.6	364	19.6
436.cactusADM	50.8	235	<b>50.2</b>	<b>238</b>	50.0	239	50.8	235	<b>50.2</b>	<b>238</b>	50.0	239
437.leslie3d	94.4	99.5	<b>99.2</b>	<b>94.7</b>	103	91.0	94.4	99.5	<b>99.2</b>	<b>94.7</b>	103	91.0
444.namd	491	16.3	<b>491</b>	<b>16.3</b>	491	16.3	482	16.6	<b>483</b>	<b>16.6</b>	483	16.6
447.dealII	336	34.0	336	34.0	<b>336</b>	<b>34.0</b>	336	34.0	336	34.0	<b>336</b>	<b>34.0</b>
450.soplex	294	28.4	292	28.5	<b>293</b>	<b>28.4</b>	294	28.4	292	28.5	<b>293</b>	<b>28.4</b>
453.povray	206	25.8	<b>206</b>	<b>25.8</b>	205	26.0	<b>162</b>	<b>32.8</b>	162	32.8	162	32.8
454.calculix	331	24.9	<b>331</b>	<b>24.9</b>	332	24.9	294	28.0	294	28.0	<b>294</b>	<b>28.0</b>
459.GemsFDTD	<b>139</b>	<b>76.5</b>	145	73.1	138	76.9	132	80.6	130	81.8	<b>130</b>	<b>81.5</b>
465.tonto	<b>491</b>	<b>20.0</b>	489	20.1	492	20.0	352	28.0	<b>352</b>	<b>28.0</b>	353	27.9
470.lbm	54.8	251	55.0	250	<b>55.0</b>	<b>250</b>	54.8	251	55.0	250	<b>55.0</b>	<b>250</b>
481.wrf	268	41.6	<b>270</b>	<b>41.4</b>	274	40.8	268	41.6	<b>270</b>	<b>41.4</b>	274	40.8
482.sphinx3	456	42.7	<b>456</b>	<b>42.7</b>	466	41.9	<b>411</b>	<b>47.4</b>	410	47.5	426	45.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

```
'mount -t hugetlbfs nodev /mnt/hugepages' was used to enable large pages
echo 900 > /proc/sys/vm/nr_hugepages
export HUGETLB_MORECORE=yes
export LD_PRELOAD=/usr/lib64/libhugetlbfs.so
```

## Platform Notes

BIOS Settings:

Power Management = Maximum Performance (Default = Active Power Controller)

Data Reuse = Disabled (Default = Enabled)

Logical Processor = Disabled (Default = Enabled)



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## General Notes

OMP\_NUM\_THREADS set to number of cores  
Binaries were compiled on RHEL5.5  
The Dell PowerEdge T710 and  
the Bull NovaScale T860 F2 models are electronically equivalent.  
The results have been measured on a Dell PowerEdge T710 model

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.deallI: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias

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## Base Optimization Flags (Continued)

C++ benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias`

Fortran benchmarks:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:

`-xSSE4.2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch  
-ansi-alias`

## Peak Compiler Invocation

C benchmarks:

`icc -m64`

C++ benchmarks:

`icpc -m64`

Fortran benchmarks:

`ifort -m64`

Benchmarks using both Fortran and C:

`icc -m64 ifort -m64`

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

`433.milc: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias`

`470.lbm: basepeak = yes`

`482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -ansi-alias  
-parallel`

C++ benchmarks:

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## Peak Optimization Flags (Continued)

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias  
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -parallel  
-static

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
-inline-level=0 -opt-prefetch -parallel  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc  
-opt-malloc-options=3 -auto -unroll4  
-B /usr/share/libhugetlbfs/ -Wl,-melf\_x86\_64 -Wl,-hugetlbfs-link=BDT

### Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32  
-ansi-alias

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.0-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20110524.00.xml>

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