



SPEC® CFP2006 Result

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Supermicro

SPECfp®2006 = **87.7**

SuperServer 6027TR-DTRF (X9DRT-HF, Intel E5-2690)

SPECfp_base2006 = **81.7**

CPU2006 license: 001176

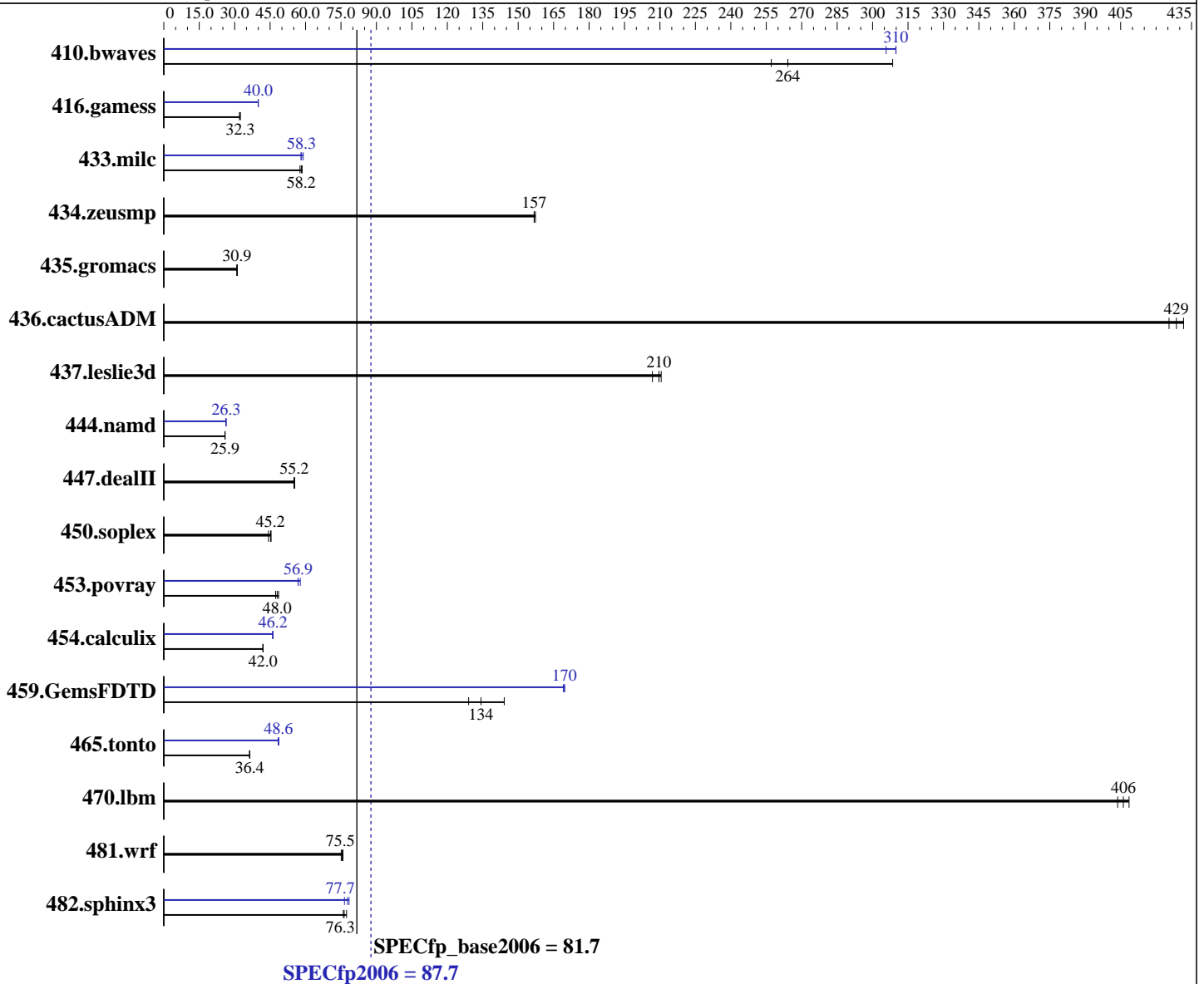
Test date: Jan-2012

Test sponsor: Supermicro

Hardware Availability: Mar-2012

Tested by: Supermicro

Software Availability: Oct-2011



Hardware

CPU Name: Intel Xeon E5-2690
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz
 CPU MHz: 2900
 FPU: Integrated
 CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip, 2 threads/core
 CPU(s) orderable: 2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.1 (Santiago)
 2.6.32-131.0.15.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4

Continued on next page



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L3 Cache: 20 MB I+D on chip per chip
 Other Cache: None
 Memory: 64 GB (8 x 8 GB 2Rx4 PC3-12800R-11, ECC)
 Disk Subsystem: 1 x 1 TB SATA II, 7200 RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	51.4	264	52.9	257	44.1	309	43.9	310	43.8	310	44.5	306
416.gamess	611	32.0	606	32.3	605	32.3	490	40.0	490	40.0	489	40.0
433.milc	158	58.2	159	57.6	157	58.6	157	58.3	158	58.1	156	59.0
434.zeusmp	58.0	157	57.8	157	58.0	157	58.0	157	57.8	157	58.0	157
435.gromacs	229	31.2	231	30.9	231	30.9	229	31.2	231	30.9	231	30.9
436.cactusADM	27.9	429	27.7	432	28.1	425	27.9	429	27.7	432	28.1	425
437.leslie3d	44.6	211	45.5	207	44.8	210	44.6	211	45.5	207	44.8	210
444.namd	310	25.9	310	25.9	309	25.9	304	26.3	305	26.3	304	26.3
447.dealII	207	55.2	208	55.1	207	55.4	207	55.2	208	55.1	207	55.4
450.soplex	188	44.3	184	45.2	184	45.4	188	44.3	184	45.2	184	45.4
453.povray	109	48.6	111	48.0	112	47.3	93.6	56.8	92.1	57.8	93.6	56.9
454.calculix	197	41.9	196	42.0	196	42.0	178	46.2	179	46.0	179	46.2
459.GemsFDTD	82.2	129	79.0	134	73.6	144	62.6	170	62.7	169	62.5	170
465.tonto	270	36.4	271	36.3	271	36.4	202	48.6	203	48.4	202	48.6
470.lbm	34.0	404	33.6	409	33.8	406	34.0	404	33.6	409	33.8	406
481.wrf	149	75.1	148	75.5	147	75.8	149	75.1	148	75.5	147	75.8
482.sphinx3	252	77.4	256	76.0	255	76.3	249	78.4	251	77.7	255	76.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runspec before the start of the run:
 KMP_AFFINITY = "granularity=fine,scatter"
 LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64"
 OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB memory using RHEL5.5
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias



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Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-ansi-alias

470.lbm: basepeak = yes

482.sphinx3: -xAVX -ipo -O3 -no-prec-div -unroll2 -ansi-alias
-parallel

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealIII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xAVX -ipo -O3 -no-prec-div -opt-prefetch -parallel
-static

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Peak Optimization Flags (Continued)

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -opt-prefetch -parallel

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -inline-calloc
-opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-revA.xml>

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For other inquiries, please contact webmaster@spec.org.

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