SPEC® CINT2006 Result
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Fujitsu

PRIMERGY TX120 S3p, Intel Xeon E3-1230 v2, 3.30 GHz

$SPECint^{®}\_rate2006 =$ 192
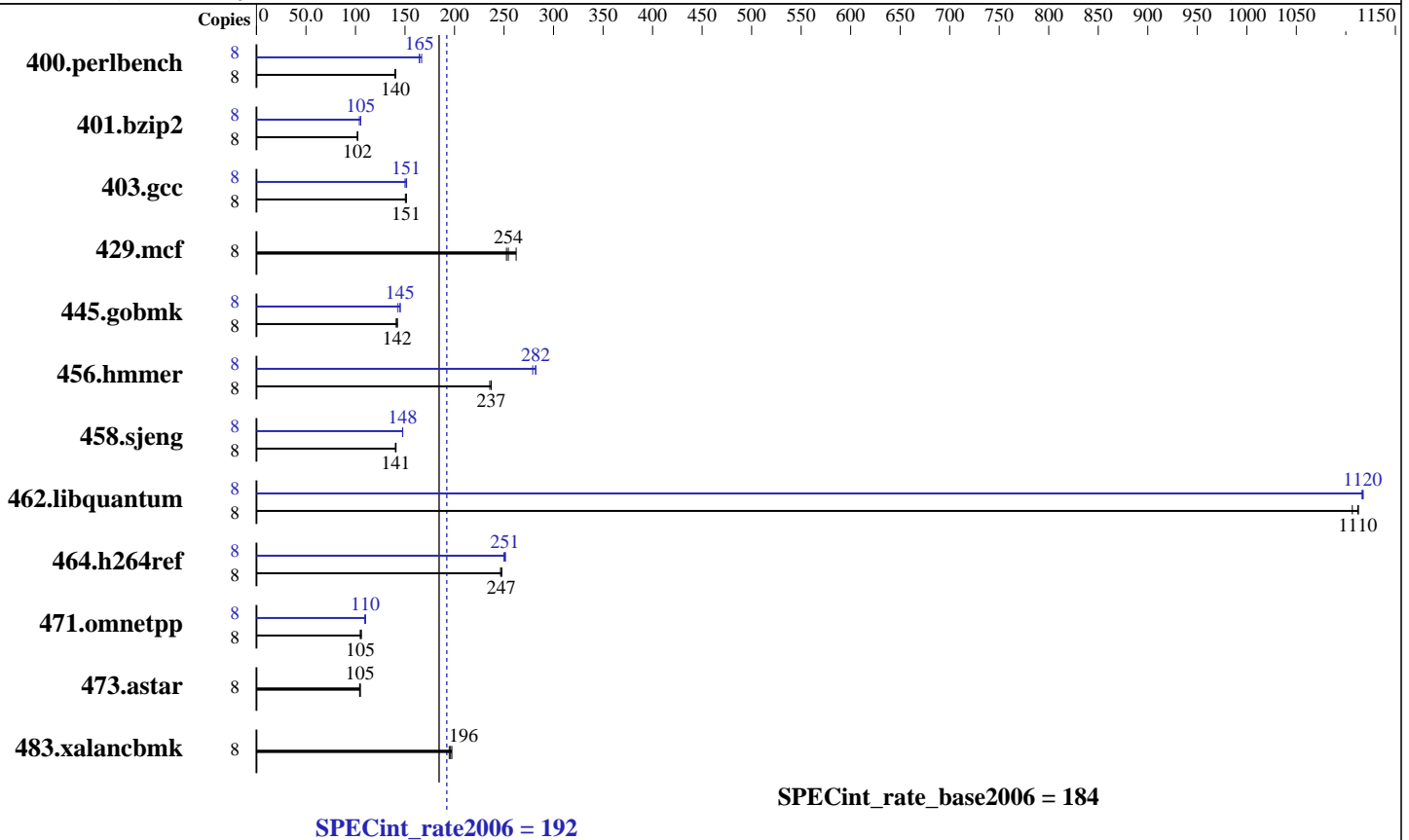
$SPECint\_rate\_base2006 =$ 184

CPU2006 license: 19
Test sponsor: Fujitsu
Tested by: Fujitsu

Test date: May-2012
Hardware Availability: May-2012
Software Availability: Feb-2012

Copies	0 50.0 100 150 200 250 300 350 400 450 500 550 600 650 700 750 800 850 900 950 1000 1050 1150
400.perlbench	8 → 165; 8 → 140
401.bzip2	8 → 105; 8 → 102
403.gcc	8 → 151; 8 → 151
429.mcf	8 → 254
445.gobmk	8 → 145; 8 → 142
456.hmmer	8 → 282; 8 → 237
458.sjeng	8 → 148; 8 → 141
462.libquantum	8 → 1120; 8 → 1110
464.h264ref	8 → 251; 8 → 247
471.omnetpp	8 → 110; 8 → 105
473.astar	8 → 105
483.xalancbmk	8 → 196

SPECint_rate_base2006 = 184

SPECint_rate2006 = 192

Hardware

CPU Name:	Intel Xeon E3-1230 v2
CPU Characteristics:	Intel Turbo Boost Technology up to 3.7 GHz
CPU MHz:	3300
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip, 2 threads/core
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
Disk Subsystem:	1 x SATA, 500 GB, 7200 RPM
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 6.2 (Santiago) 2.6.32-220.el6.x86_64
Compiler:	C/C++: Version 12.1.0.293 of Intel C++ Studio XE for Linux
Auto Parallel:	No
File System:	ext4
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark		Base							Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	556	141	559	140	**557**	**140**	8	**475**	**165**	469	167	476	164
401.bzip2	8	759	102	**755**	**102**	754	102	8	743	104	733	105	**734**	**105**
403.gcc	8	426	151	428	150	**426**	**151**	8	425	152	**426**	**151**	429	150
429.mcf	8	**287**	**254**	289	252	278	262	8	**287**	**254**	289	252	278	262
445.gobmk	8	590	142	595	141	**591**	**142**	8	587	143	**579**	**145**	578	145
456.hmmer	8	317	236	**315**	**237**	315	237	8	268	279	265	282	**265**	**282**
458.sjeng	8	**688**	**141**	690	140	687	141	8	**656**	**148**	655	148	656	148
462.libquantum	8	150	1110	149	1110	**149**	**1110**	8	148	1120	**148**	**1120**	148	1120
464.h264ref	8	718	246	715	248	**716**	**247**	8	709	250	**706**	**251**	704	251
471.omnetpp	8	472	106	477	105	**476**	**105**	8	454	110	**455**	**110**	457	109
473.astar	8	539	104	**537**	**105**	536	105	8	539	104	**537**	**105**	536	105
483.xalancbmk	8	**282**	**196**	280	197	284	195	8	**282**	**196**	280	197	284	195

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
   The numactl mechanism was used to bind copies to processors. The config file option 'submit'
was used to generate numactl commands to bind each copy to a specific processor.
For details, please see the config file.
```

Operating System Notes

```
   Stack size set to unlimited using "ulimit -s unlimited"
```

General Notes

```
   Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/SPECcpu2006/libs/32:/SPECcpu2006/libs/64"

   Binaries compiled on a system with 2x E5-2650 CPU + 96 GB
memory using RHEL6.2
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>
This result was measured on the PRIMERGY TX140S1p. The PRIMERGY TX140S1p
and the PRIMERGY TX120 S3p are electronically equivalent.
For information about Fujitsu please visit: http://www.fujitsu.com
```

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Base Compiler Invocation

C benchmarks:
    icc -m32

C++ benchmarks:
    icpc -m32

Base Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
    -Wl,-z,muldefs -L/opt/SmartHeap/lib -lsmartheap

Base Other Flags

C benchmarks:

        403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m32

    400.perlbench: icc -m64

       401.bzip2: icc -m64

      456.hmmer: icc -m64

       458.sjeng: icc -m64

C++ benchmarks:
    icpc -m32

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Test sponsor: Fujitsu		**Hardware Availability:**	May-2012
Tested by: Fujitsu		**Software Availability:**	Feb-2012

Peak Portability Flags

```
  400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
      401.bzip2: -DSPEC_CPU_LP64
     456.hmmer: -DSPEC_CPU_LP64
      458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LINUX
 483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
  400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -auto-ilp32

      401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -opt-prefetch -auto-ilp32 -ansi-alias

        403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div

        429.mcf: basepeak = yes

      445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
                 -ansi-alias -opt-mem-layout-trans=3

      456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

      458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll4 -auto-ilp32

 462.libquantum: -xAVX -ipo -O3 -no-prec-div -opt-prefetch
                 -opt-mem-layout-trans=3

     464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -unroll2 -ansi-alias
```

C++ benchmarks:

```
    471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                 -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                 -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs
                 -L/opt/SmartHeap/lib -lsmartheap

       473.astar: basepeak = yes
```

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Peak Optimization Flags (Continued)

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.html
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20111122.xml
http://www.spec.org/cpu2006/flags/Fujitsu-Platform.20120320.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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