



SPEC[®] CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECfp[®]_rate2006 = 329

Huawei BH621 V2 (Intel Xeon E5-2440)

SPECfp_rate_base2006 = 321

CPU2006 license: 3175

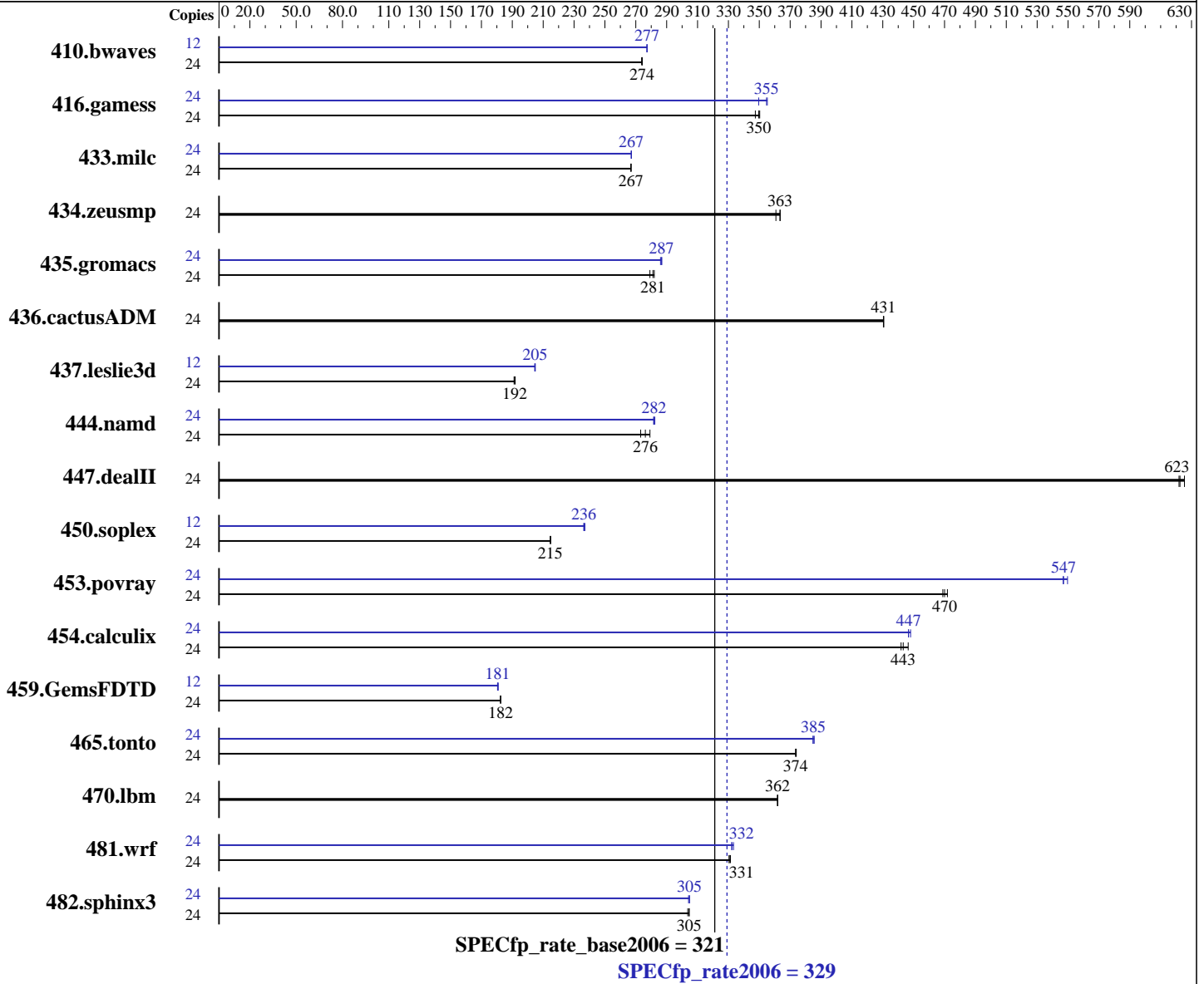
Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2012

Hardware Availability: May-2012

Software Availability: Dec-2011



Hardware

CPU Name: Intel Xeon E5-2440
 CPU Characteristics: Intel Turbo Boost Technology up to 2.90 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: Red Hat Enterprise Linux Server release 6.2 (Santiago)
 2.6.32-220.el6.x86_64
 Compiler: C/C++: Version 12.1.0.225 of Intel C++ Studio XE for Linux;
 Fortran: Version 12.1.0.225 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: ext4

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 329

Huawei BH621 V2 (Intel Xeon E5-2440)

SPECfp_rate_base2006 = 321

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 96 GB (12 x 8 GB 1Rx4 PC3-10600R-9, ECC)
 Disk Subsystem: 1 x 300 GB SAS, 10K RPM
 Other Hardware: None

System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	24	<u>1190</u>	<u>274</u>	1191	274	1189	274	12	588	277	588	277	<u>588</u>	<u>277</u>
416.gamess	24	<u>1344</u>	<u>350</u>	1353	347	1342	350	24	<u>1325</u>	<u>355</u>	1344	350	1323	355
433.milc	24	<u>825</u>	<u>267</u>	826	267	825	267	24	825	267	<u>825</u>	<u>267</u>	825	267
434.zeusmp	24	<u>601</u>	<u>363</u>	601	364	605	361	24	<u>601</u>	<u>363</u>	601	364	605	361
435.gromacs	24	608	282	614	279	<u>610</u>	<u>281</u>	24	599	286	597	287	<u>598</u>	<u>287</u>
436.cactusADM	24	666	431	666	430	<u>666</u>	<u>431</u>	24	666	431	666	430	<u>666</u>	<u>431</u>
437.leslie3d	24	1176	192	1180	191	<u>1178</u>	<u>192</u>	12	552	205	551	205	<u>551</u>	<u>205</u>
444.namd	24	689	279	705	273	<u>697</u>	<u>276</u>	24	684	282	682	282	<u>683</u>	<u>282</u>
447.dealII	24	439	625	<u>441</u>	<u>623</u>	441	622	24	439	625	<u>441</u>	<u>623</u>	441	622
450.soplex	24	932	215	<u>932</u>	<u>215</u>	933	215	12	<u>423</u>	<u>236</u>	423	236	422	237
453.povray	24	271	472	<u>272</u>	<u>470</u>	272	469	24	232	550	233	547	<u>233</u>	<u>547</u>
454.calculix	24	<u>447</u>	<u>443</u>	443	446	448	442	24	442	448	<u>443</u>	<u>447</u>	443	447
459.GemsFDTD	24	1396	182	<u>1396</u>	<u>182</u>	1396	182	12	704	181	706	180	<u>705</u>	<u>181</u>
465.tonto	24	631	374	632	374	<u>632</u>	<u>374</u>	24	614	385	<u>613</u>	<u>385</u>	612	386
470.lbm	24	911	362	<u>911</u>	<u>362</u>	911	362	24	911	362	<u>911</u>	<u>362</u>	911	362
481.wrf	24	<u>811</u>	<u>331</u>	811	330	809	331	24	804	333	807	332	<u>807</u>	<u>332</u>
482.sphinx3	24	1536	305	<u>1536</u>	<u>305</u>	1540	304	24	1535	305	1538	304	<u>1535</u>	<u>305</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled
 Filesystem page cache cleared with:
 echo 1> /proc/sys/vm/drop_caches
 runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
 Select only test related files when installing the operating system



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 329

Huawei BH621 V2 (Intel Xeon E5-2440)

SPECfp_rate_base2006 = 321

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Performance

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6800

\$Rev: 6800 \$ \$Date:: 2011-10-11 #\$ 6f2ebdff5032aaa42e583f96b07f99d3

running on BH622-RH6.2 Wed Jun 20 06:11:56 2012

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2440 0 @ 2.40GHz

2 "physical id"s (chips)

24 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 6

siblings : 12

physical 0: cores 0 1 2 3 4 5

physical 1: cores 0 1 2 3 4 5

cache size : 15360 KB

From /proc/meminfo

MemTotal: 99042080 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

redhat-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)

system-release: Red Hat Enterprise Linux Server release 6.2 (Santiago)

system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:

Linux BH622-RH6.2 2.6.32-220.el6.x86_64 #1 SMP Wed Nov 9 08:03:13 EST 2011

x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 19 16:59

SPEC is set to: /spec

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdal	ext4	197G	46G	142G	25%	/

Additional information from dmidecode:

Memory:

12x Samsu M393B 8 GB 1333 MHz 1 rank

(End of data from sysinfo program)



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 329

Huawei BH621 V2 (Intel Xeon E5-2440)

SPECfp_rate_base2006 = 321

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/spec/libs/32:/spec/libs/64"

Binaries compiled on a system with 2 x Xeon X5645 CPU + 16GB memory
using RHEL 6.1

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.deallI: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 329

Huawei BH621 V2 (Intel Xeon E5-2440)

SPECfp_rate_base2006 = 321

CPU2006 license: 3175

Test date: Jun-2012

Test sponsor: Huawei

Hardware Availability: May-2012

Tested by: Huawei

Software Availability: Dec-2011

Base Optimization Flags (Continued)

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:

-xAVX -ipo -O3 -no-prec-div -static -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 329

Huawei BH621 V2 (Intel Xeon E5-2440)

SPECfp_rate_base2006 = 321

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static -auto-ilp32
-opt-mem-layout-trans=3

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -static
-unroll2

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32
-opt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -static

416.gamess: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep- -static

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -static -opt-prefetch

459.GemsFDTD: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -opt-malloc-options=3

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo -O3 -no-prec-div
-prof-use(pass 2) -xSSE4.2 -opt-prefetch -static
-auto-ilp32 -opt-mem-layout-trans=3

Continued on next page



SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Huawei

SPECfp_rate2006 = 329

Huawei BH621 V2 (Intel Xeon E5-2440)

SPECfp_rate_base2006 = 321

CPU2006 license: 3175

Test sponsor: Huawei

Tested by: Huawei

Test date: Jun-2012

Hardware Availability: May-2012

Software Availability: Dec-2011

Peak Optimization Flags (Continued)

436.cactusADM: basepeak = yes

454.calculix: -xAVX -ipo -O3 -no-prec-div -static -auto-ilp32
-opt-mem-layout-trans=3

481.wrf: Same as 454.calculix

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic12.1-official-linux64.20120425.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-revE.20120703.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Thu Jul 24 10:15:58 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 25 July 2012.