



# SPEC® CINT2006 Result

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## Acer Incorporated

### SPECint®\_rate2006 = 196

## Altos R380 F2 (Intel Xeon E5-2620)

### SPECint\_rate\_base2006 = 190

CPU2006 license: 97

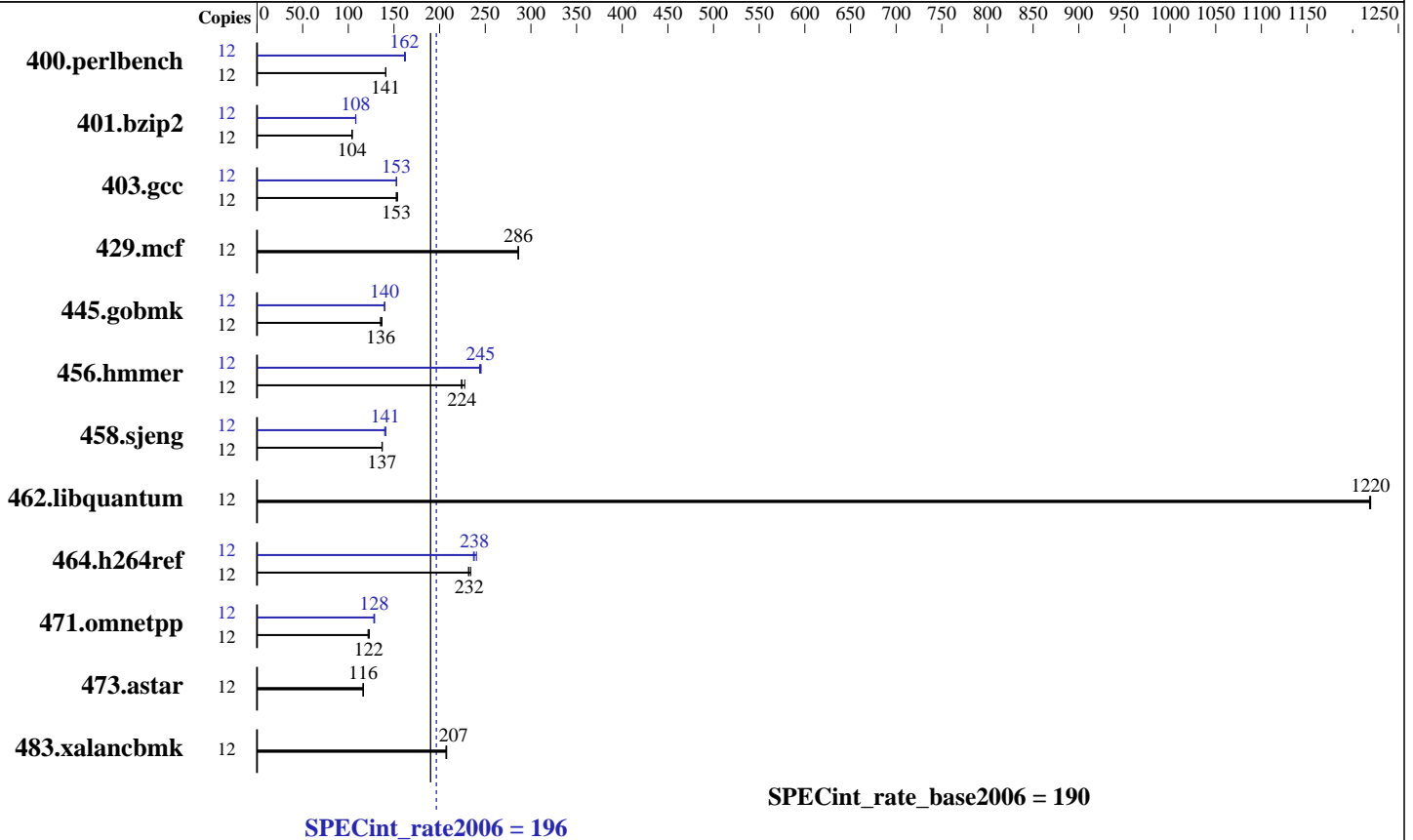
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2013

Hardware Availability: May-2013

Software Availability: Jun-2012



### Hardware

CPU Name: Intel Xeon E5-2620  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.50 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1, 2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (8 x 8 GB 1Rx4 PC3-12800R-11, ECC)  
 Disk Subsystem: 1 x 1 TB SATA, 7200 RPM  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
 2.6.32-279.el6.x86\_64  
 Compiler: C/C++: Version 13.0.0.133 of Intel C++ Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	12	832	141	830	141	<u>832</u>	<u>141</u>	12	<u>725</u>	<u>162</u>	726	162	721	163
401.bzip2	12	1109	104	1116	104	<u>1113</u>	<u>104</u>	12	<u>1072</u>	<u>108</u>	1073	108	1069	108
403.gcc	12	633	153	<u>633</u>	<u>153</u>	628	154	12	632	153	<u>632</u>	<u>153</u>	633	153
429.mcf	12	382	286	<u>383</u>	<u>286</u>	383	286	12	382	286	<u>383</u>	<u>286</u>	383	286
445.gobmk	12	919	137	930	135	<u>929</u>	<u>136</u>	12	902	140	900	140	<u>900</u>	<u>140</u>
456.hammer	12	500	224	492	227	<u>499</u>	<u>224</u>	12	459	244	<u>457</u>	<u>245</u>	456	245
458.sjeng	12	1061	137	1057	137	<u>1061</u>	<u>137</u>	12	1039	140	1030	141	<u>1032</u>	<u>141</u>
462.libquantum	12	204	1220	<u>204</u>	<u>1220</u>	204	1220	12	204	1220	<u>204</u>	<u>1220</u>	204	1220
464.h264ref	12	1135	234	1147	232	<u>1143</u>	<u>232</u>	12	<u>1116</u>	<u>238</u>	1105	240	1120	237
471.omnetpp	12	<u>613</u>	<u>122</u>	609	123	617	122	12	582	129	586	128	<u>585</u>	<u>128</u>
473.astar	12	<u>725</u>	<u>116</u>	722	117	725	116	12	<u>725</u>	<u>116</u>	722	117	725	116
483.xalancbmk	12	<u>399</u>	<u>207</u>	399	208	400	207	12	<u>399</u>	<u>207</u>	399	208	400	207

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818  
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191  
running on altosr380f2 Thu May 16 14:32:11 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Genuine Intel(R) CPU @ 2.00GHz
1 "physical id"s (chips)
12 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 6
siblings : 12
```

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## Platform Notes (Continued)

physical 0: cores 0 1 2 3 4 5  
cache size : 15360 KB

From /proc/meminfo  
MemTotal: 65922972 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d  
Red Hat Enterprise Linux Server release 6.3 (Santiago)

From /etc/\*release\* /etc/\*version\*  
redhat-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
system-release: Red Hat Enterprise Linux Server release 6.3 (Santiago)  
system-release-cpe: cpe:/o:redhat:enterprise\_linux:6server:ga:server

uname -a:  
Linux altosr380f2 2.6.32-279.el6.x86\_64 #1 SMP Wed Jun 13 18:24:36 EDT 2012  
x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 May 16 12:48 last=5

SPEC is set to: /usr/cpu2006  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/vg\_altosr380f2-lv\_root  
ext4 243G 34G 196G 15% /

Additional information from dmidecode:  
BIOS Intel Corp. SE5C600.86B.01.06.0002.110120121539 11/01/2012  
Memory:  
8x 8 GB  
16x NO DIMM NO DIMM  
8x Nanya NT8GC72B4PB0NL-DI 8 GB 1600 MHz 1 rank

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB  
memory using RHEL5.5  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
Altos R360 F2 and Altos R380 F2 are electronically equivalent.  
This result was measured on Altos R380 F2.



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## Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3  
-Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



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## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
 401.bzip2: -DSPEC\_CPU\_LP64  
 456.hmmer: -DSPEC\_CPU\_LP64  
 458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LINUX  
 483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch  
 -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
 -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll4  
 -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -unroll2  
 -ansi-alias

C++ benchmarks:

471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias  
 -opt-ra-region-strategy=block -Wl,-z,muldefs  
 -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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