



SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint®_rate2006 = 197

Acer AT310 F2 (Intel Xeon E3-1270 v2)

SPECint_rate_base2006 = 191

CPU2006 license: 97

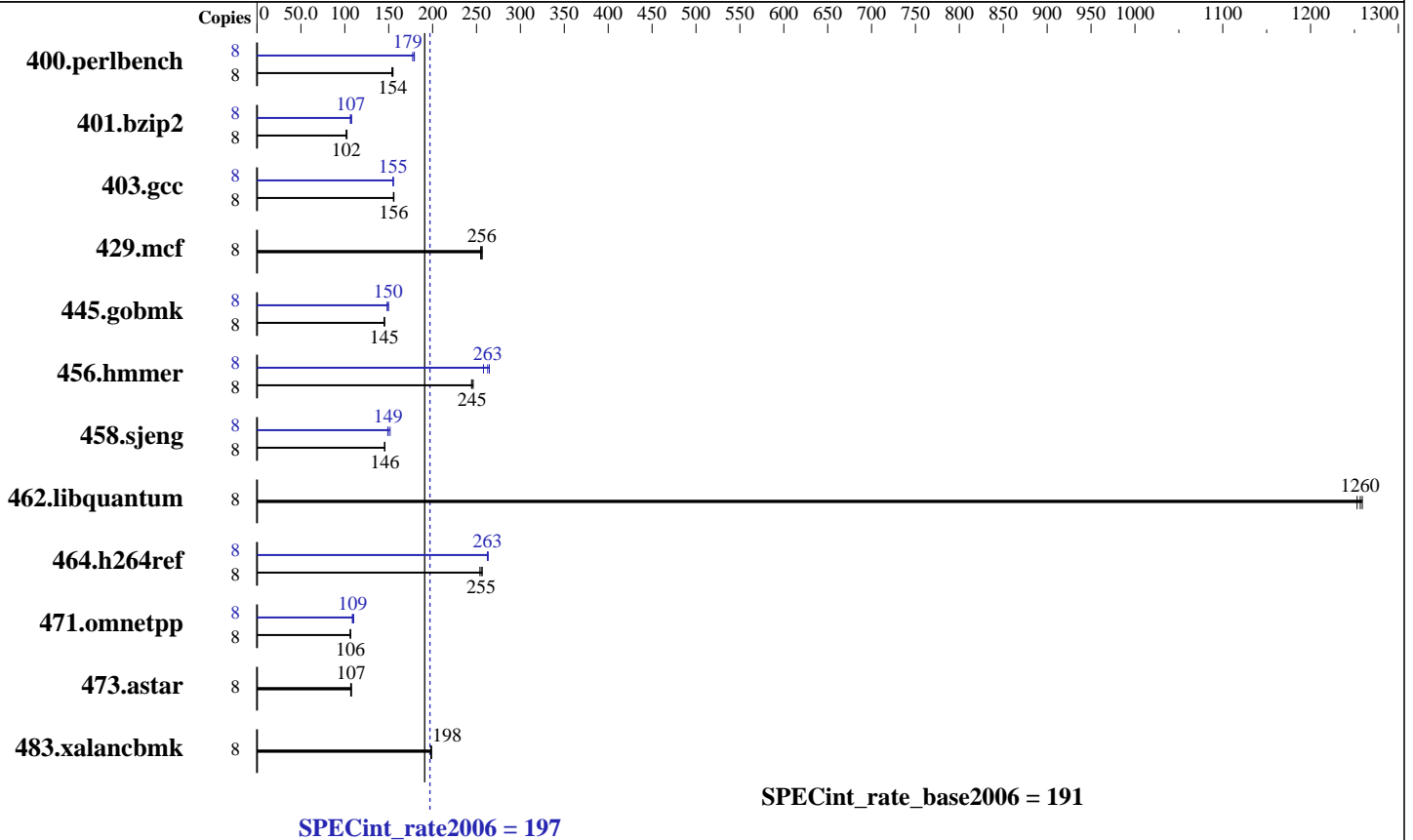
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2013

Hardware Availability: Jan-2012

Software Availability: Feb-2013



Hardware

CPU Name: Intel Xeon E3-1270 v2
 CPU Characteristics: Intel Turbo Boost Technology up to 3.9 GHz
 CPU MHz: 3500
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 8 MB I+D on chip per chip
 Other Cache: None
 Memory: 16 GB (2 x 8 GB 2Rx8 PC3-12800E-11, ECC)
 Disk Subsystem: 1 x 500 GB SATA, 7200 RPM SATA HDD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 6.4 (Santiago)
 2.6.32-358.el6.x86_64
 Compiler: C/C++: Version 13.0.0.133 of Intel C++ Studio XE for Linux
 Auto Parallel: No
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 197

Acer AT310 F2 (Intel Xeon E3-1270 v2)

SPECint_rate_base2006 = 191

CPU2006 license: 97

Test date: May-2013

Test sponsor: Acer Incorporated

Hardware Availability: Jan-2012

Tested by: Acer Incorporated

Software Availability: Feb-2013

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	505	155	<u>506</u>	<u>154</u>	509	154	8	441	177	<u>437</u>	<u>179</u>	436	179
401.bzip2	8	<u>758</u>	<u>102</u>	759	102	753	102	8	<u>723</u>	<u>107</u>	716	108	726	106
403.gcc	8	<u>414</u>	<u>156</u>	415	155	413	156	8	<u>415</u>	<u>155</u>	415	155	415	155
429.mcf	8	284	257	<u>285</u>	<u>256</u>	286	255	8	284	257	<u>285</u>	<u>256</u>	286	255
445.gobmk	8	<u>579</u>	<u>145</u>	577	145	579	145	8	<u>561</u>	<u>150</u>	561	150	567	148
456.hammer	8	303	246	305	244	<u>305</u>	<u>245</u>	8	289	258	<u>284</u>	<u>263</u>	282	265
458.sjeng	8	664	146	<u>665</u>	<u>146</u>	667	145	8	<u>648</u>	<u>149</u>	650	149	638	152
462.libquantum	8	132	1260	<u>132</u>	<u>1260</u>	132	1250	8	132	1260	<u>132</u>	<u>1260</u>	132	1250
464.h264ref	8	690	257	<u>693</u>	<u>255</u>	697	254	8	<u>674</u>	<u>263</u>	672	263	675	262
471.omnetpp	8	471	106	469	107	<u>470</u>	<u>106</u>	8	454	110	461	109	<u>458</u>	<u>109</u>
473.astar	8	522	108	<u>525</u>	<u>107</u>	526	107	8	522	108	<u>525</u>	<u>107</u>	526	107
483.xalancbmk	8	279	198	278	199	<u>278</u>	<u>198</u>	8	279	198	278	199	<u>278</u>	<u>198</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818
\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
running on localhost.localdomain Sat May 18 02:58:43 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E3-1270 V2 @ 3.50GHz
1 "physical id"s (chips)
8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings  : 8
```

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 197

Acer AT310 F2 (Intel Xeon E3-1270 v2)

SPECint_rate_base2006 = 191

CPU2006 license: 97
Test sponsor: Acer Incorporated
Tested by: Acer Incorporated

Test date: May-2013
Hardware Availability: Jan-2012
Software Availability: Feb-2013

Platform Notes (Continued)

physical 0: cores 0 1 2 3
cache size : 8192 KB

From /proc/meminfo
MemTotal: 16298700 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
Red Hat Enterprise Linux Server release 6.4 (Santiago)

From /etc/*release* /etc/*version*
redhat-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.4 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server

uname -a:
Linux localhost.localdomain 2.6.32-358.el6.x86_64 #1 SMP Tue Jan 29 11:47:41 EST 2013 x86_64 x86_64 x86_64 GNU/Linux

run-level 3 May 18 02:57

SPEC is set to: /usr/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/VolGroup-lv_root
ext4 434G 20G 392G 5% /

Additional information from dmidecode:
BIOS Acer P10 11/23/2012
Memory:
2x 8 GB
2x Kingston ACR1GX72D3E16C11G 8 GB 1600 MHz 2 rank
2x [Empty] [Empty]

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/redhat_transparent_hugepage/enabled



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 197

Acer AT310 F2 (Intel Xeon E3-1270 v2)

SPECint_rate_base2006 = 191

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2013

Hardware Availability: Jan-2012

Software Availability: Feb-2013

Base Compiler Invocation

C benchmarks:

icc -m32

C++ benchmarks:

icpc -m32

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3

C++ benchmarks:

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -opt-mem-layout-trans=3
-Wl,-z,muldefs -L/sh -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 197

Acer AT310 F2 (Intel Xeon E3-1270 v2)

SPECint_rate_base2006 = 191

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2013

Hardware Availability: Jan-2012

Software Availability: Feb-2013

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
               -no-prec-div(pass 2) -prof-use(pass 2) -auto-ilp32

401.bzip2: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -opt-prefetch
           -auto-ilp32 -ansi-alias

403.gcc: -xAVX -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xAVX(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
           -ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xAVX -ipo -O3 -no-prec-div -unroll2 -auto-ilp32

458.sjeng: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
           -no-prec-div(pass 2) -prof-use(pass 2) -unroll4
           -auto-ilp32

462.libquantum: basepeak = yes

464.h264ref: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -unroll2
             -ansi-alias
```

C++ benchmarks:

```
471.omnetpp: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
             -no-prec-div(pass 2) -prof-use(pass 2) -ansi-alias
             -opt-ra-region-strategy=block -Wl,-z,muldefs
             -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes
```



SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Acer Incorporated

SPECint_rate2006 = 197

Acer AT310 F2 (Intel Xeon E3-1270 v2)

SPECint_rate_base2006 = 191

CPU2006 license: 97

Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: May-2013

Hardware Availability: Jan-2012

Software Availability: Feb-2013

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic13-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Acer-Platform-Settings-V1.2-revA.20130423.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Thu Jul 24 15:54:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 18 June 2013.