



SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 45.6

PowerEdge T620 (Intel Xeon E5-2630L v2, 2.40 GHz)

SPECint_base2006 = 42.9

CPU2006 license: 55

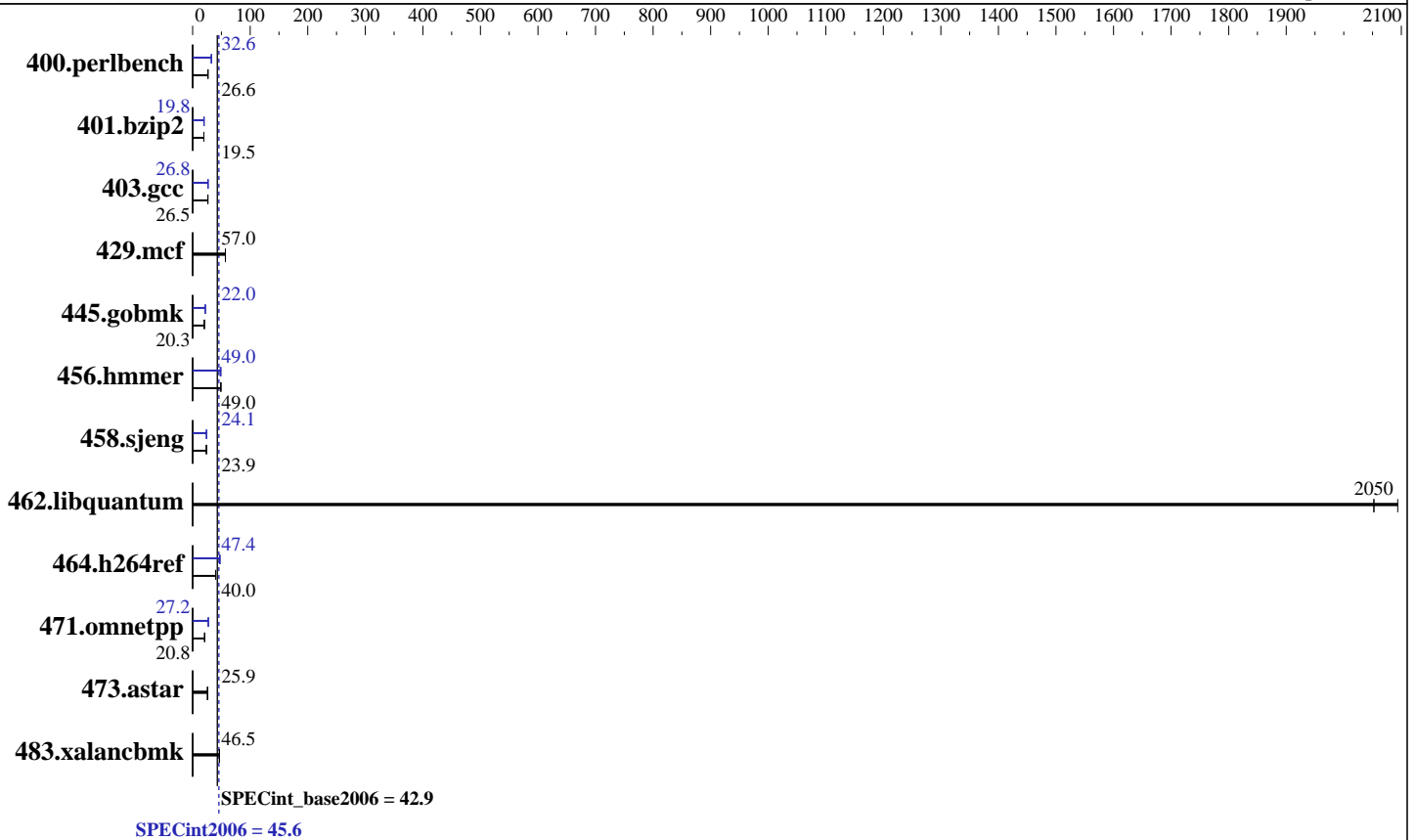
Test date: Sep-2013

Test sponsor: Dell Inc.

Hardware Availability: Sep-2013

Tested by: Dell Inc.

Software Availability: Sep-2013



Hardware

CPU Name: Intel Xeon E5-2630L v2
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz
 CPU MHz: 2400
 FPU: Integrated
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core
 L3 Cache: 15 MB I+D on chip per chip
 Other Cache: None
 Memory: 256 GB (16 x 16 GB 2Rx4 PC3-14900R-13, ECC, running at 1600 MHz)
 Disk Subsystem: 1 x 300 GB 15000 RPM SAS
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 11 SP3 (x86_64) 3.0.76-0.11-default
 Compiler: C/C++; Version 14.0.0.080 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext2
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	367	26.6	367	26.6	<u>367</u>	<u>26.6</u>	300	32.6	301	32.5	<u>300</u>	<u>32.6</u>
401.bzip2	494	19.5	494	19.5	<u>494</u>	<u>19.5</u>	<u>487</u>	<u>19.8</u>	487	19.8	488	19.8
403.gcc	303	26.5	305	26.4	<u>303</u>	<u>26.5</u>	<u>301</u>	<u>26.8</u>	301	26.7	300	26.8
429.mcf	160	56.9	160	57.0	<u>160</u>	<u>57.0</u>	160	56.9	160	57.0	<u>160</u>	<u>57.0</u>
445.gobmk	<u>517</u>	<u>20.3</u>	517	20.3	517	20.3	<u>478</u>	<u>22.0</u>	478	22.0	478	22.0
456.hammer	<u>190</u>	<u>49.0</u>	190	49.0	190	49.0	<u>190</u>	<u>49.0</u>	190	49.0	195	48.0
458.sjeng	507	23.9	507	23.9	<u>507</u>	<u>23.9</u>	503	24.1	<u>503</u>	<u>24.1</u>	503	24.0
462.libquantum	10.1	2050	<u>10.1</u>	<u>2050</u>	9.90	2090	10.1	2050	<u>10.1</u>	<u>2050</u>	9.90	2090
464.h264ref	553	40.0	553	40.0	<u>553</u>	<u>40.0</u>	472	46.9	<u>467</u>	<u>47.4</u>	466	47.5
471.omnetpp	299	20.9	300	20.8	<u>300</u>	<u>20.8</u>	229	27.2	<u>229</u>	<u>27.2</u>	230	27.2
473.astar	272	25.8	271	25.9	<u>271</u>	<u>25.9</u>	272	25.8	271	25.9	<u>271</u>	<u>25.9</u>
483.xalancbmk	<u>148</u>	<u>46.5</u>	148	46.5	148	46.6	<u>148</u>	<u>46.5</u>	148	46.5	148	46.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
 Virtualization Technology disabled
 Execute Disable disabled
 Logical Processor disabled
 System Profile set to Performance
 Sysinfo program /root/cpu2006.1.2.ic13/config/sysinfo.rev6818
 \$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191
 running on linux Mon Sep 9 08:47:35 2013

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2630L v2 @ 2.40GHz
 2 "physical id"s (chips)
12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 6
siblings : 6
physical 0: cores 0 1 2 3 4 5
```

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Platform Notes (Continued)

physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB

From /proc/meminfo
MemTotal: 264601764 kB
HugePages_Total: 0
Hugepagesize: 2048 kB

/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 11 (x86_64)
VERSION = 11
PATCHLEVEL = 3

uname -a:
Linux linux 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013 (ccab990)
x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Sep 9 08:45 last=S

SPEC is set to: /root/cpu2006.1.2.ic13
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 ext2 267G 56G 211G 21% /

Additional information from dmidecode:
BIOS Dell Inc. 2.0.19 08/30/2013
Memory:
2x 00AD00B300AD HMT42GR7MFR4C-RD 16 GB 1600 MHz
12x 00AD04B300AD HMT42GR7AFR4C-RD 16 GB 1600 MHz
2x 00CE00B300CE M393B2G70CB0-CMA 16 GB 1600 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/root/cpu2006.1.2.ic13/libs/32:/root/cpu2006.1.2.ic13/libs/64:/root/cpu2006.1.2.ic13/sh"
OMP_NUM_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
memory using RHEL5.5
Transparent Huge Pages enabled with:
echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
echo 1> /proc/sys/vm/drop_caches



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Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
 401.bzip2: -DSPEC_CPU_LP64
 403.gcc: -DSPEC_CPU_LP64
 429.mcf: -DSPEC_CPU_LP64
 445.gobmk: -DSPEC_CPU_LP64
 456.hmmer: -DSPEC_CPU_LP64
 458.sjeng: -DSPEC_CPU_LP64
 462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
 464.h264ref: -DSPEC_CPU_LP64
 471.omnetpp: -DSPEC_CPU_LP64
 473.astar: -DSPEC_CPU_LP64
 483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

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Peak Compiler Invocation (Continued)

400.perlbench: `icc -m32`

445.gobmk: `icc -m32`

464.h264ref: `icc -m32`

C++ benchmarks (except as noted below):

`icpc -m64`

471.omnetpp: `icpc -m32`

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`

401.bzip2: `-DSPEC_CPU_LP64`

403.gcc: `-DSPEC_CPU_LP64`

429.mcf: `-DSPEC_CPU_LP64`

456.hmmer: `-DSPEC_CPU_LP64`

458.sjeng: `-DSPEC_CPU_LP64`

462.libquantum: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

473.astar: `-DSPEC_CPU_LP64`

483.xalancbmk: `-DSPEC_CPU_LP64 -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-prefetch -ansi-alias`

401.bzip2: `-xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias`

403.gcc: `-xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32`

429.mcf: `basepeak = yes`

445.gobmk: `-xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias`

456.hmmer: `-xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias`

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Peak Optimization Flags (Continued)

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>
<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revB.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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