SPEC® CINT2006 Result

Hewlett-Packard Company

ProLiant DL380e Gen8
(1.70 GHz, Intel Xeon E5-2450L v2)

SPECint®2006 = 37.3

SPECint_base2006 = 34.7

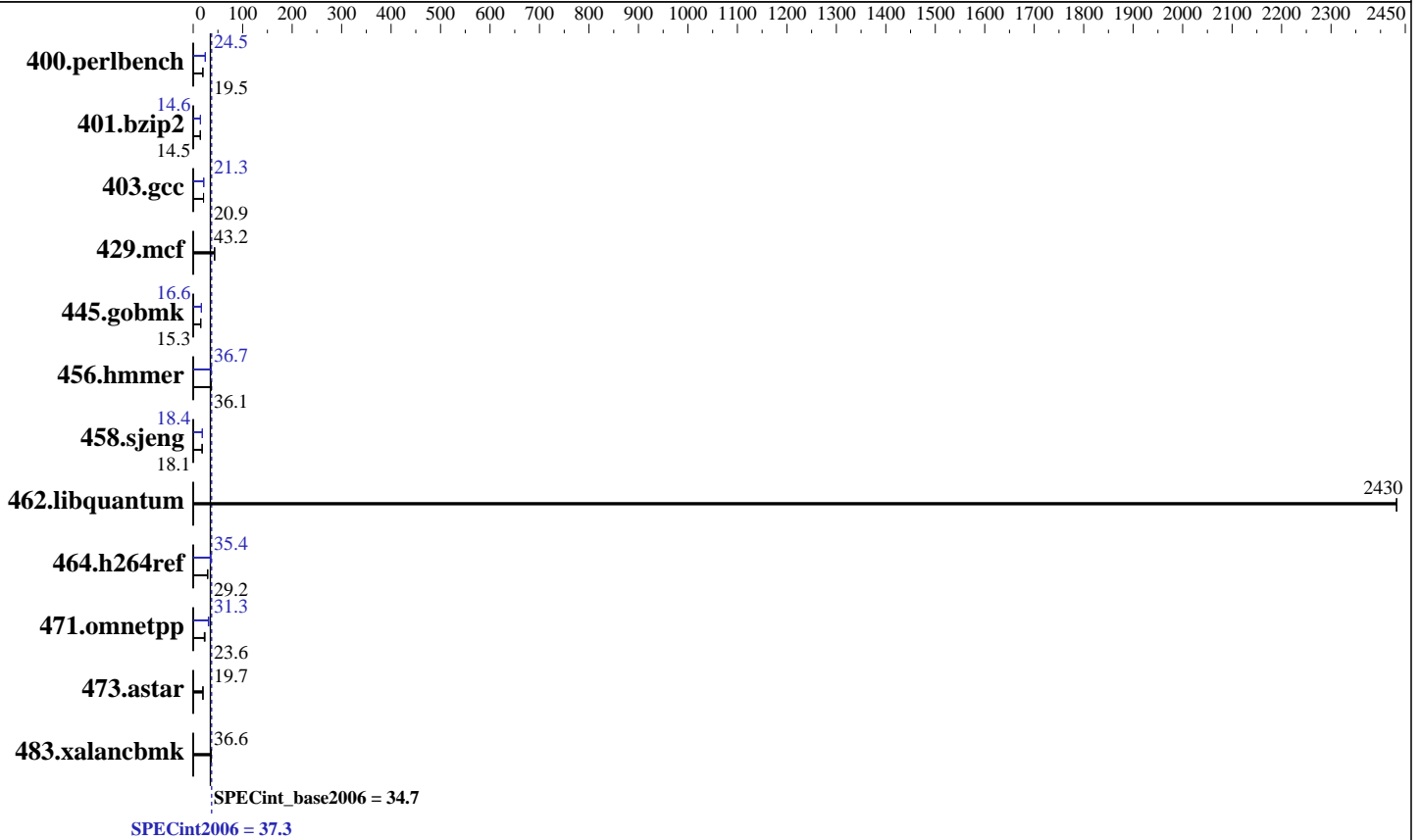|	
CPU2006 license: 3	**Test date:** Jan-2014
Test sponsor: Hewlett-Packard Company	**Hardware Availability:** Jan-2014
Tested by: Hewlett-Packard Company	**Software Availability:** Sep-2013

Benchmark results (scale 0 to 2450):

- **400.perlbench** 24.5 / 19.5
- **401.bzip2** 14.6 / 14.5
- **403.gcc** 21.3 / 20.9
- **429.mcf** 43.2
- **445.gobmk** 16.6 / 15.3
- **456.hmmer** 36.7 / 36.1
- **458.sjeng** 18.4 / 18.1
- **462.libquantum** 2430
- **464.h264ref** 35.4 / 29.2
- **471.omnetpp** 31.3 / 23.6
- **473.astar** 19.7
- **483.xalancbmk** 36.6

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Hardware

CPU Name:	Intel Xeon E5-2450L v2
CPU Characteristics:	Intel Turbo Boost Technology up to 2.10 GHz
CPU MHz:	1700
FPU:	Integrated
CPU(s) enabled:	20 cores, 2 chips, 10 cores/chip
CPU(s) orderable:	1,2 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core
L3 Cache:	25 MB I+D on chip per chip
Other Cache:	None
Memory:	96 GB (12 x 8 GB 2Rx4 PC3-12800R-11, ECC)
Disk Subsystem:	1 x 300 GB 15 K SAS, RAID 0
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 11 (x86_64) SP3 Kernel 3.0.76-0.11-default
Compiler:	C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V10.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	500	19.5	501	19.5	**501**	**19.5**	400	24.4	399	24.5	**399**	**24.5**
401.bzip2	668	14.4	**667**	**14.5**	667	14.5	**659**	**14.6**	659	14.6	660	14.6
403.gcc	384	20.9	**384**	**20.9**	387	20.8	**379**	**21.3**	378	21.3	379	21.2
429.mcf	**211**	**43.2**	211	43.1	211	43.3	**211**	**43.2**	211	43.1	211	43.3
445.gobmk	688	15.3	687	15.3	**688**	**15.3**	**633**	**16.6**	634	16.6	633	16.6
456.hmmer	259	36.0	**258**	**36.1**	254	36.7	**255**	**36.7**	254	36.7	255	36.6
458.sjeng	668	18.1	**668**	**18.1**	667	18.1	656	18.4	**656**	**18.4**	656	18.4
462.libquantum	**8.52**	**2430**	8.52	2430	8.52	2430	**8.52**	**2430**	8.52	2430	8.52	2430
464.h264ref	**758**	**29.2**	757	29.2	759	29.2	626	35.3	624	35.4	**625**	**35.4**
471.omnetpp	**265**	**23.6**	279	22.4	265	23.6	**199**	**31.3**	204	30.6	199	31.3
473.astar	357	19.7	356	19.7	**357**	**19.7**	357	19.7	356	19.7	**357**	**19.7**
483.xalancbmk	189	36.5	188	36.7	**189**	**36.6**	189	36.5	188	36.7	**189**	**36.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
Filesystem page cache cleared with:
 echo 1 > /proc/sys/vm/drop_caches
Reclaim mode enabled with:
 echo 1 > /proc/sys/vm/zone_reclaim_mode
runspec command invoked through numactl i.e.:
 numactl --localalloc runspec <etc>
```

Platform Notes

```
BIOS Configuration:
 Intel Hyperthreading Options set to Disabled
 HP Power Profile set to Maximum Performance
 Minimum Processor Idle Power Core State set to C1E state
 Minimum Processor Idle Power Package State set to Package C6 (retention) State
 Memory Power Savings Mode set to Maximum Performance
 Thermal Configuration set to Maximum Cooling
 Collaborative Power Control set to Disabled
 Dynamic Power Capping Functionality set to Disabled
 Processor Power and Utilization Monitoring set to Disabled
 Memory Refresh Rate set to 1x

 Sysinfo program /cpu2006/config/sysinfo.rev6818
 $Rev: 6818 $ $Date:: 2012-07-17 #$ e86d102572650a6e4d596a3cee98f191
 running on dl380e-gen8-cxd Tue Jan 14 14:55:51 2014
```

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Platform Notes (Continued)

```
This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-2450L v2 @ 1.70GHz
      2 "physical id"s (chips)
      20 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 10
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       98903452 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

/usr/bin/lsb_release -d
   SUSE Linux Enterprise Server 11 (x86_64)

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 11 (x86_64)
      VERSION = 11
      PATCHLEVEL = 3

uname -a:
   Linux dl380e-gen8-cxd 3.0.76-0.11-default #1 SMP Fri Jun 14 08:21:43 UTC 2013
   (ccab990) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 14 14:51 last=S

SPEC is set to: /cpu2006
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda3       ext3  273G   13G  247G   5% /

Additional information from dmidecode:
  BIOS HP P73 11/12/2013
  Memory:
   12x HP 689911-071 8 GB 1600 MHz

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact,1,0"
LD_LIBRARY_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"
OMP_NUM_THREADS = "20"
 Binaries compiled on a system with 1x Core i7-860 CPU + 8GB
 memory using RedHat EL 6.4
```

Base Compiler Invocation

C benchmarks:
    icc -m64

C++ benchmarks:
    icpc -m64

Base Portability Flags

        400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
          401.bzip2: -DSPEC_CPU_LP64
            403.gcc: -DSPEC_CPU_LP64
            429.mcf: -DSPEC_CPU_LP64
          445.gobmk: -DSPEC_CPU_LP64
          456.hmmer: -DSPEC_CPU_LP64
           458.sjeng: -DSPEC_CPU_LP64
      462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
         464.h264ref: -DSPEC_CPU_LP64
         471.omnetpp: -DSPEC_CPU_LP64
           473.astar: -DSPEC_CPU_LP64
      483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
    -xSSE4.2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh -lsmartheap64

Base Other Flags

C benchmarks:

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Base Other Flags (Continued)

    403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
    icc -m64

    400.perlbench: icc -m32

    445.gobmk: icc -m32

    464.h264ref: icc -m32

C++ benchmarks (except as noted below):
    icpc -m64

    471.omnetpp: icpc -m32

Peak Portability Flags

    400.perlbench: -DSPEC_CPU_LINUX_IA32
    401.bzip2: -DSPEC_CPU_LP64
    403.gcc: -DSPEC_CPU_LP64
    429.mcf: -DSPEC_CPU_LP64
    456.hmmer: -DSPEC_CPU_LP64
    458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
    473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

    400.perlbench: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
                   -O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
                   -opt-prefetch -ansi-alias

    401.bzip2: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
               -O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32
               -opt-prefetch -ansi-alias

    403.gcc: -xSSE4.2 -ipo -O3 -no-prec-div -inline-calloc
             -opt-malloc-options=3 -auto-ilp32

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Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xSSE4.2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)
-ansi-alias

456.hmmer: -xSSE4.2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-ansi-alias

458.sjeng: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll4

462.libquantum: basepeak = yes

464.h264ref: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias

C++ benchmarks:

471.omnetpp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2)
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)
-opt-ra-region-strategy=block   -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-revB.20131009.xml

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