



# SPEC® CINT2006 Result

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Inspur Corporation

**SPECint®\_rate2006 = 928**

Inspur NF5280M4 (Intel Xeon E5-2660 v3)

**SPECint\_rate\_base2006 = 890**

CPU2006 license: 3358

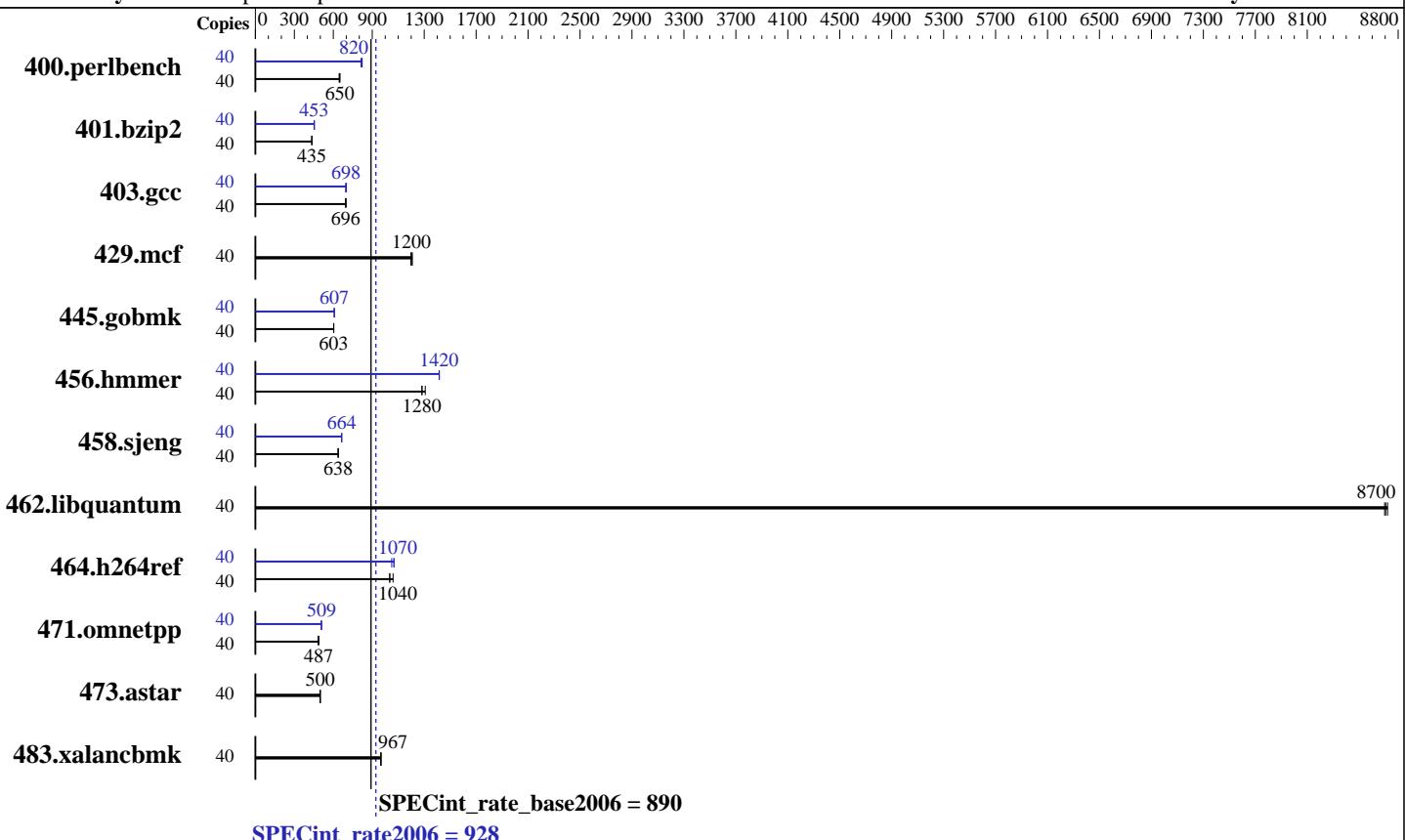
Test date: Dec-2014

Test sponsor: Inspur Corporation

Hardware Availability: Sep-2014

Tested by: Inspur Corporation

Software Availability: Nov-2014



## Hardware

CPU Name: Intel Xeon E5-2660 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.30 GHz  
 CPU MHz: 2600  
 FPU: Integrated  
 CPU(s) enabled: 20 cores, 2 chips, 10 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 450 GB SATA SSD  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Compiler: 3.10.0-123.el7.x86\_64  
 Auto Parallel: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	40	<b>601</b>	<b>650</b>	601	650	606	645	40	<b>476</b>	<b>820</b>	475	823	481	813
401.bzip2	40	<b>887</b>	<b>435</b>	887	435	888	435	40	852	453	<b>852</b>	<b>453</b>	850	454
403.gcc	40	464	693	461	698	<b>462</b>	<b>696</b>	40	463	695	<b>461</b>	<b>698</b>	459	701
429.mcf	40	302	1210	<b>303</b>	<b>1200</b>	305	1200	40	302	1210	<b>303</b>	<b>1200</b>	305	1200
445.gobmk	40	695	603	<b>696</b>	<b>603</b>	696	603	40	689	609	<b>691</b>	<b>607</b>	691	607
456.hammer	40	291	1280	<b>291</b>	<b>1280</b>	285	1310	40	264	1420	<b>264</b>	<b>1420</b>	263	1420
458.sjeng	40	758	638	759	637	<b>759</b>	<b>638</b>	40	<b>728</b>	<b>664</b>	729	664	728	665
462.libquantum	40	95.3	8700	<b>95.2</b>	<b>8700</b>	95.1	8720	40	95.3	8700	<b>95.2</b>	<b>8700</b>	95.1	8720
464.h264ref	40	<b>855</b>	<b>1040</b>	856	1030	835	1060	40	829	1070	843	1050	<b>830</b>	<b>1070</b>
471.omnetpp	40	<b>513</b>	<b>487</b>	518	483	512	488	40	493	507	491	509	<b>491</b>	<b>509</b>
473.astar	40	564	498	562	500	<b>562</b>	<b>500</b>	40	564	498	562	500	<b>562</b>	<b>500</b>
483.xalancbmk	40	285	967	<b>285</b>	<b>967</b>	286	966	40	285	967	<b>285</b>	<b>967</b>	286	966

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

```
Sysinfo program /home/CPU2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date::: 2014-06-25 #$
running on localhost.localdomain Mon Dec 1 17:40:14 2014
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v3 @ 2.60GHz
  2 "physical id"s (chips)
  40 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 5
  siblings  : 10
```

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## Platform Notes (Continued)

```
physical 0: cores 0 1 2 3 4 8 9 10 11 12
physical 1: cores 0 1 2 3 4 8 9 10 11 12
cache size : 12800 KB

From /proc/meminfo
MemTotal:           263858652 kB
HugePages_Total:     0
Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
os-release:
  NAME="Red Hat Enterprise Linux Server"
  VERSION="7.0 (Maipo)"
  ID="rhel"
  ID_LIKE="fedora"
  VERSION_ID="7.0"
  PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
  ANSI_COLOR="0;31"
  CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
  redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
  system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
  system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

uname -a:
Linux localhost.localdomain 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57
EDT 2014 x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Dec 1 17:37

SPEC is set to: /home/CPU2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   393G   13G   380G   4% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program
reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to
hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 4.0.1 10/30/2014
Memory:
 8x NO DIMM NO DIMM
 16x Samsung M393A2G40DB0-CPB 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/home/CPU2006/libs/32:/home/CPU2006/libs/64:/home/CPU2006/sh"

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## General Notes (Continued)

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-opt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

400.perlbench: icc -m64

401.bzip2: icc -m64

456.hmmer: icc -m64

458.sjeng: icc -m64

C++ benchmarks:

icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-auto-ilp32

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -auto-ilp32 -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias -opt-mem-layout-trans=3

456.hmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll12 -auto-ilp32

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll14 -auto-ilp32

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## Peak Optimization Flags (Continued)

462.libquantum: basepeak = yes

464.h264ref: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll12 -ansi-alias

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs  
-L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>  
<http://www.spec.org/cpu2006/flags/Inspur-Platform-Settings-V1.0-HSW.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>  
<http://www.spec.org/cpu2006/flags/Inspur-Platform-Settings-V1.0-HSW.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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