



# SPEC® CINT2006 Result

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## Lenovo Group Limited

Lenovo ThinkServer RD650 (Intel Xeon E5-2603 v3, 1.60 GHz)

**SPECint®2006 = 29.9**

**SPECint\_base2006 = 28.8**

CPU2006 license: 9017

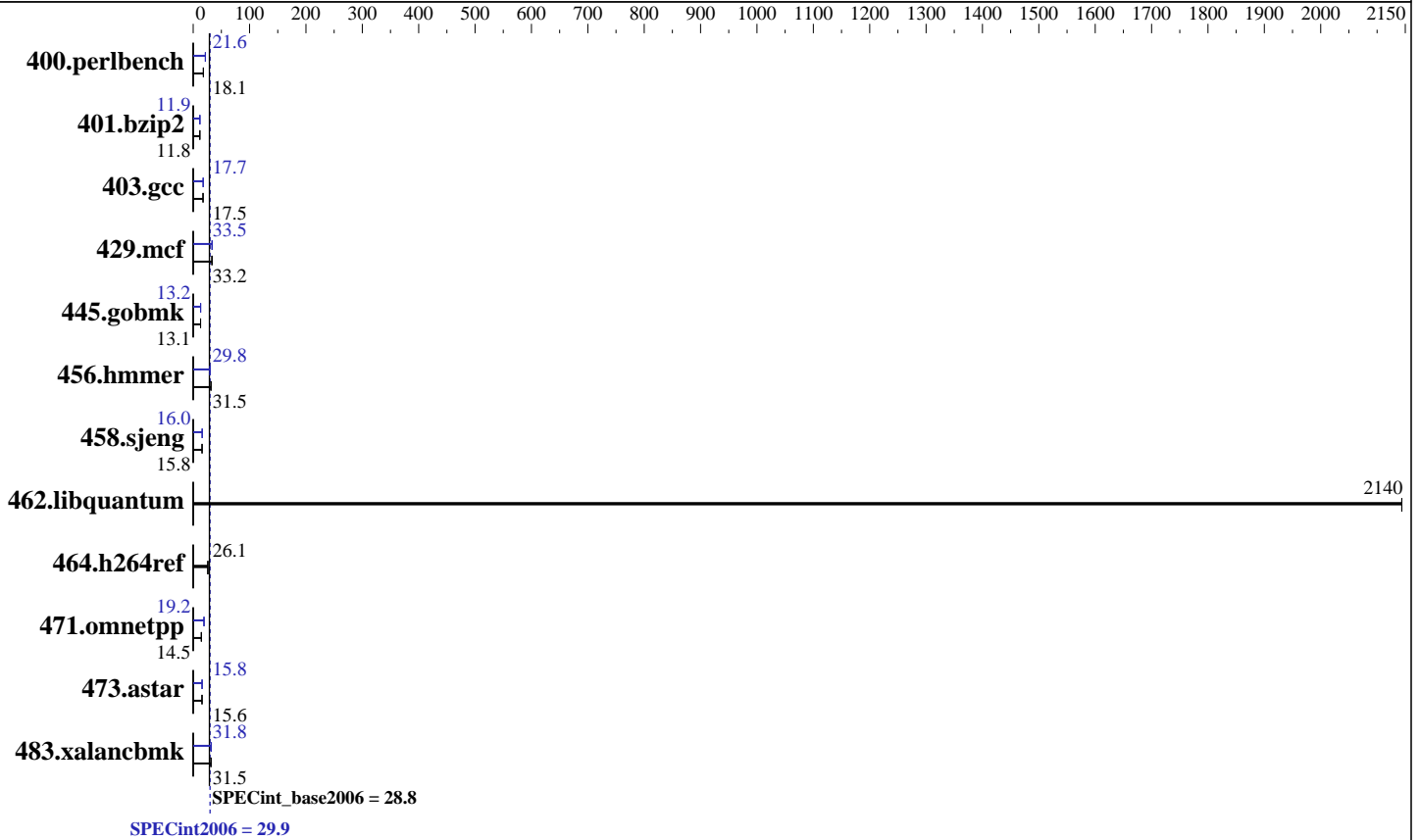
Test sponsor: Lenovo Group Limited

Tested by: Lenovo Group Limited

Test date: Oct-2014

Hardware Availability: Sep-2014

Software Availability: Jan-2014



### Hardware

CPU Name: Intel Xeon E5-2603 v3  
 CPU Characteristics: 1600  
 CPU MHz: 1600  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R, running at 1600 MHz)  
 Disk Subsystem: 1 x 240 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 6.5 (Santiago)  
 2.6.32-431.el6.x86\_64  
 Compiler: C/C++: Version 14.0.0.080 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	540	18.1	541	18.1	<b>540</b>	<b>18.1</b>	452	21.6	<b>452</b>	<b>21.6</b>	452	21.6
401.bzip2	<b>821</b>	<b>11.8</b>	821	11.8	821	11.8	814	11.9	<b>814</b>	<b>11.9</b>	814	11.9
403.gcc	460	17.5	<b>460</b>	<b>17.5</b>	461	17.5	<b>454</b>	<b>17.7</b>	454	17.7	454	17.7
429.mcf	<b>275</b>	<b>33.2</b>	270	33.8	276	33.0	<b>272</b>	<b>33.5</b>	273	33.4	272	33.5
445.gobmk	<b>800</b>	<b>13.1</b>	800	13.1	802	13.1	<b>795</b>	<b>13.2</b>	794	13.2	795	13.2
456.hammer	295	31.6	296	31.5	<b>296</b>	<b>31.5</b>	<b>313</b>	<b>29.8</b>	313	29.8	313	29.8
458.sjeng	769	15.7	767	15.8	<b>768</b>	<b>15.8</b>	<b>758</b>	<b>16.0</b>	759	16.0	758	16.0
462.libquantum	9.66	2140	9.67	2140	<b>9.66</b>	<b>2140</b>	9.66	2140	9.67	2140	<b>9.66</b>	<b>2140</b>
464.h264ref	<b>848</b>	<b>26.1</b>	855	25.9	848	26.1	<b>848</b>	<b>26.1</b>	855	25.9	848	26.1
471.omnetpp	<b>431</b>	<b>14.5</b>	430	14.5	432	14.5	326	19.2	<b>326</b>	<b>19.2</b>	326	19.2
473.astar	449	15.6	447	15.7	<b>449</b>	<b>15.6</b>	<b>443</b>	<b>15.8</b>	442	15.9	449	15.6
483.xalancbmk	218	31.7	220	31.4	<b>219</b>	<b>31.5</b>	<b>217</b>	<b>31.8</b>	217	31.9	217	31.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

Performance Profile set to Custom

C1E Support set to Disabled

Core C3 set to Disabled

Core C6 set to Disabled

Thermal Profile set to High Fan Speed

Memory Power Savings set to Disabled

Sysinfo program /usr/cpu2006/config/sysinfo.rev6818

\$Rev: 6818 \$ \$Date:: 2012-07-17 #\$ e86d102572650a6e4d596a3cee98f191

running on RD650 Mon Oct 20 23:53:22 2014

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E5-2603 v3 @ 1.60GHz

2 "physical id"s (chips)

12 "processors"

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### Platform Notes (Continued)

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 6
siblings  : 6
physical 0: cores 0 1 2 3 4 5
physical 1: cores 0 1 2 3 4 5
cache size : 15360 KB
```

From /proc/meminfo

```
MemTotal:      264416512 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

/usr/bin/lsb\_release -d

```
Red Hat Enterprise Linux Server release 6.5 (Santiago)
```

From /etc/\*release\* /etc/\*version\*

```
redhat-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release: Red Hat Enterprise Linux Server release 6.5 (Santiago)
system-release-cpe: cpe:/o:redhat:enterprise_linux:6server:ga:server
```

uname -a:

```
Linux RD650 2.6.32-431.el6.x86_64 #1 SMP Sun Nov 10 22:19:54 EST 2013 x86_64
x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 20 23:52

SPEC is set to: /usr/cpu2006

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2        ext4  217G   12G  195G   6% /
```

Additional information from dmidecode:

```
BIOS LENOVO PB2TS110 10/06/2014
```

Memory:

```
16x 16 GB
16x Hynix Semiconductor HMA42GR7MFR4N-TFTD 16 GB 1600 MHz 2 rank
8x NO DIMM NO DIMM
```

(End of data from sysinfo program)

RD650 support 4 channels and 12 DIMMs per processor, total 8 channels and 24 DIMMs. 16 DIMM slots installed with 16 GB DIMM for this run.

### General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

OMP\_NUM\_THREADS = "12"

Binaries compiled on a system with 1x Core i7-860 CPU + 8GB

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## General Notes (Continued)

memory using RedHat EL 6.4  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/redhat\_transparent\_hugepage/enabled  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64

## Base Other Flags

C benchmarks:  
403.gcc: -Dalloca=\_alloca



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## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32

445.gobmk: icc -m32

C++ benchmarks (except as noted below):

icpc -m32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-prefetch -ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

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## Peak Optimization Flags (Continued)

456.hmmr: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32  
-ansi-alias

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmarheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmarheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmarheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic14.0-official-linux64.20140128.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Settings-V1.2-HSW-revA.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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