



# SPEC® CINT2006 Result

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## Hewlett-Packard Company

**SPECint®2006 = 66.6**

ProLiant XL170r Gen9  
(2.30 GHz, Intel Xeon E5-2699 v3)

**SPECint\_base2006 = 64.7**

CPU2006 license: 3

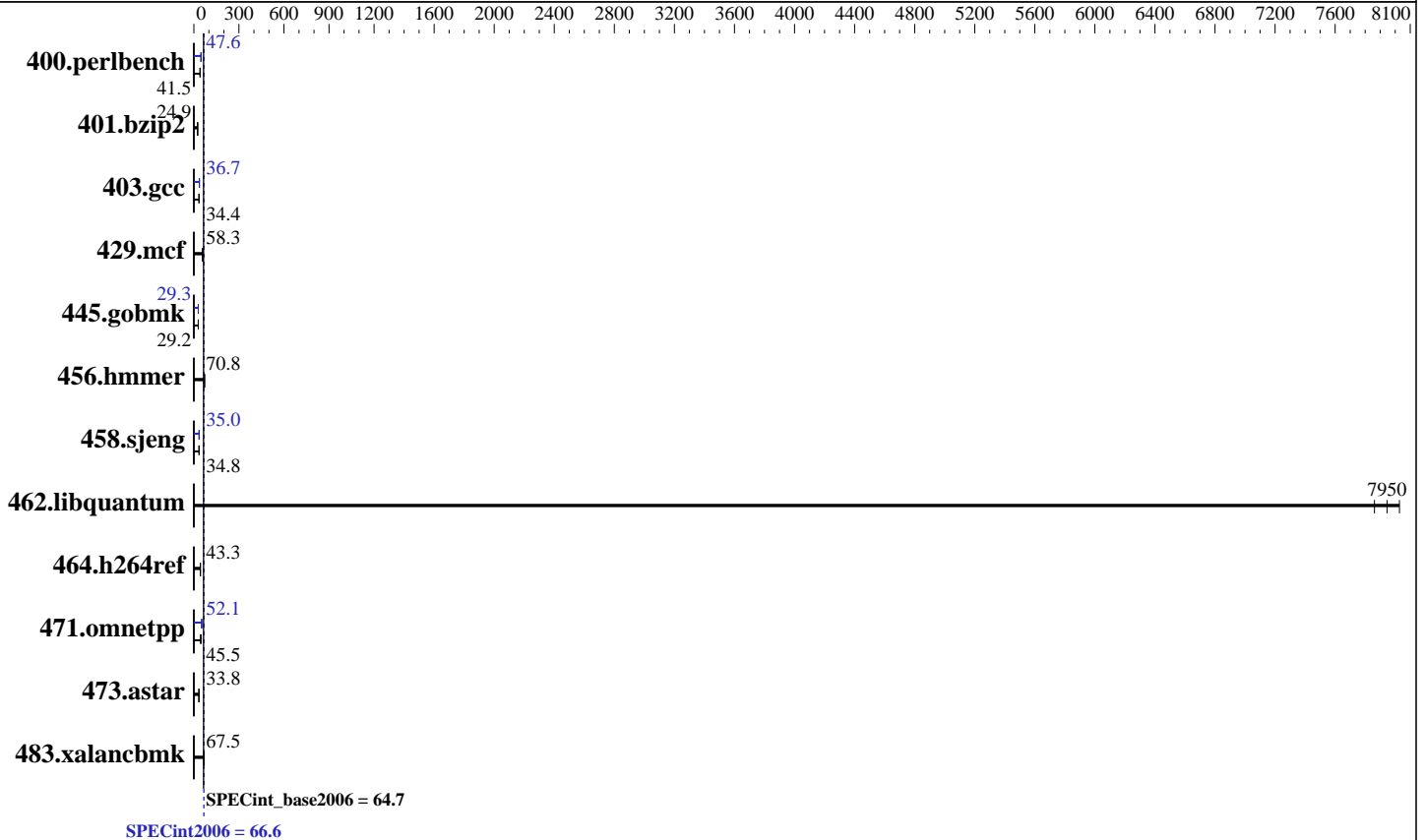
Test date: Mar-2015

Test sponsor: Hewlett-Packard Company

Hardware Availability: Mar-2015

Tested by: Hewlett-Packard Company

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E5-2699 v3  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.60 GHz  
 CPU MHz: 2300  
 FPU: Integrated  
 CPU(s) enabled: 36 cores, 2 chips, 18 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 45 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2133P-R)  
 Disk Subsystem: 1 x 400 GB SATA SSD  
 Other Hardware: None

### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 Kernel 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.0



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>236</b>	<b>41.5</b>	236	41.4	236	41.5	206	47.5	205	47.6	<b>205</b>	<b>47.6</b>
401.bzip2	388	24.9	<b>387</b>	<b>24.9</b>	386	25.0	388	24.9	<b>387</b>	<b>24.9</b>	386	25.0
403.gcc	<b>234</b>	<b>34.4</b>	234	34.5	234	34.4	<b>220</b>	<b>36.7</b>	218	36.9	220	36.7
429.mcf	157	58.1	<b>157</b>	<b>58.3</b>	156	58.4	157	58.1	<b>157</b>	<b>58.3</b>	156	58.4
445.gobmk	<b>359</b>	<b>29.2</b>	359	29.2	358	29.3	357	29.4	<b>357</b>	<b>29.3</b>	358	29.3
456.hammer	132	70.8	<b>132</b>	<b>70.8</b>	132	70.9	132	70.8	<b>132</b>	<b>70.8</b>	132	70.9
458.sjeng	<b>348</b>	<b>34.8</b>	348	34.7	348	34.8	<b>346</b>	<b>35.0</b>	346	35.0	345	35.0
462.libquantum	<b>2.61</b>	<b>7950</b>	2.58	8030	2.64	7860	<b>2.61</b>	<b>7950</b>	2.58	8030	2.64	7860
464.h264ref	<b>511</b>	<b>43.3</b>	510	43.4	511	43.3	<b>511</b>	<b>43.3</b>	510	43.4	511	43.3
471.omnetpp	138	45.4	132	47.2	<b>137</b>	<b>45.5</b>	121	51.6	120	52.2	<b>120</b>	<b>52.1</b>
473.astar	207	33.9	208	33.8	<b>207</b>	<b>33.8</b>	207	33.9	208	33.8	<b>207</b>	<b>33.8</b>
483.xalancbmk	<b>102</b>	<b>67.5</b>	102	67.7	103	67.3	<b>102</b>	<b>67.5</b>	102	67.7	103	67.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled with:  
echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Platform Notes

BIOS Configuration:  
HP Power Profile set to Custom  
HP Power Regulator to HP Static High Performance Mode  
Minimum Processor Idle Power Core State set to C6 State  
Minimum Processor Idle Power Package State set to No Package State  
QPI Snoop Configuration set to Early Snoop  
Thermal Configuration set to Maximum Cooling  
Collaborative Power Control set to Disabled  
Processor Power and Utilization Monitoring set to Disabled  
Memory Double Refresh Rate set to 1x Refresh  
Intel Hyperthreading Options set to Disabled  
Sysinfo program /cpu2006/config/sysinfo.rev6914  
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1  
running on R110-xl170-A Tue Mar 17 19:50:48 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
Continued on next page



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### Platform Notes (Continued)

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2699 v3 @ 2.30GHz
 2 "physical id"s (chips)
 36 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 18
  siblings  : 18
  physical 0: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
  physical 1: cores 0 2 3 4 8 9 10 11 16 17 18 19 20 24 25 26 27
cache size : 46080 KB

```

```

From /proc/meminfo
MemTotal:      263844828 kB
HugePages_Total:    0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.0 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.0"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.0:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.0:ga:server

```

```

uname -a:
Linux R110-xl170-A 3.10.0-123.el7.x86_64 #1 SMP Mon May 5 11:16:57 EDT 2014
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Mar 17 19:46

```

SPEC is set to: /cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda4       xfs   307G  166G  141G  54% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HP U14 02/25/2015  
Memory:

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## Platform Notes (Continued)

5x HP 752369-081 16 GB 2 rank 2133 MHz  
11x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh"  
OMP\_NUM\_THREADS = "36"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

## Base Compiler Invocation

C benchmarks:  
icc -m64  
  
C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32  
  
C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmarthheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

C++ benchmarks (except as noted below):

icpc -m64

471.omnetpp: icpc -m32 -L/opt/intel/composer\_xe\_2015/lib/ia32

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)

-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)

-opt-prefetch -ansi-alias

401.bzip2: basepeak = yes

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-alloc

-opt-malloc-options=3 -auto-ilp32

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## Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -prof-use(pass 2)  
-ansi-alias

456.hmmcr: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen(pass 1) -ipo(pass 2)  
-O3(pass 2) -no-prec-div(pass 2) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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