



# SPEC® CFP2006 Result

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## Huawei

SPECfp®\_rate2006 = 1060

Huawei CH242 V3 (Intel Xeon E7-8893 v2)

SPECfp\_rate\_base2006 = 1030

CPU2006 license: 3175

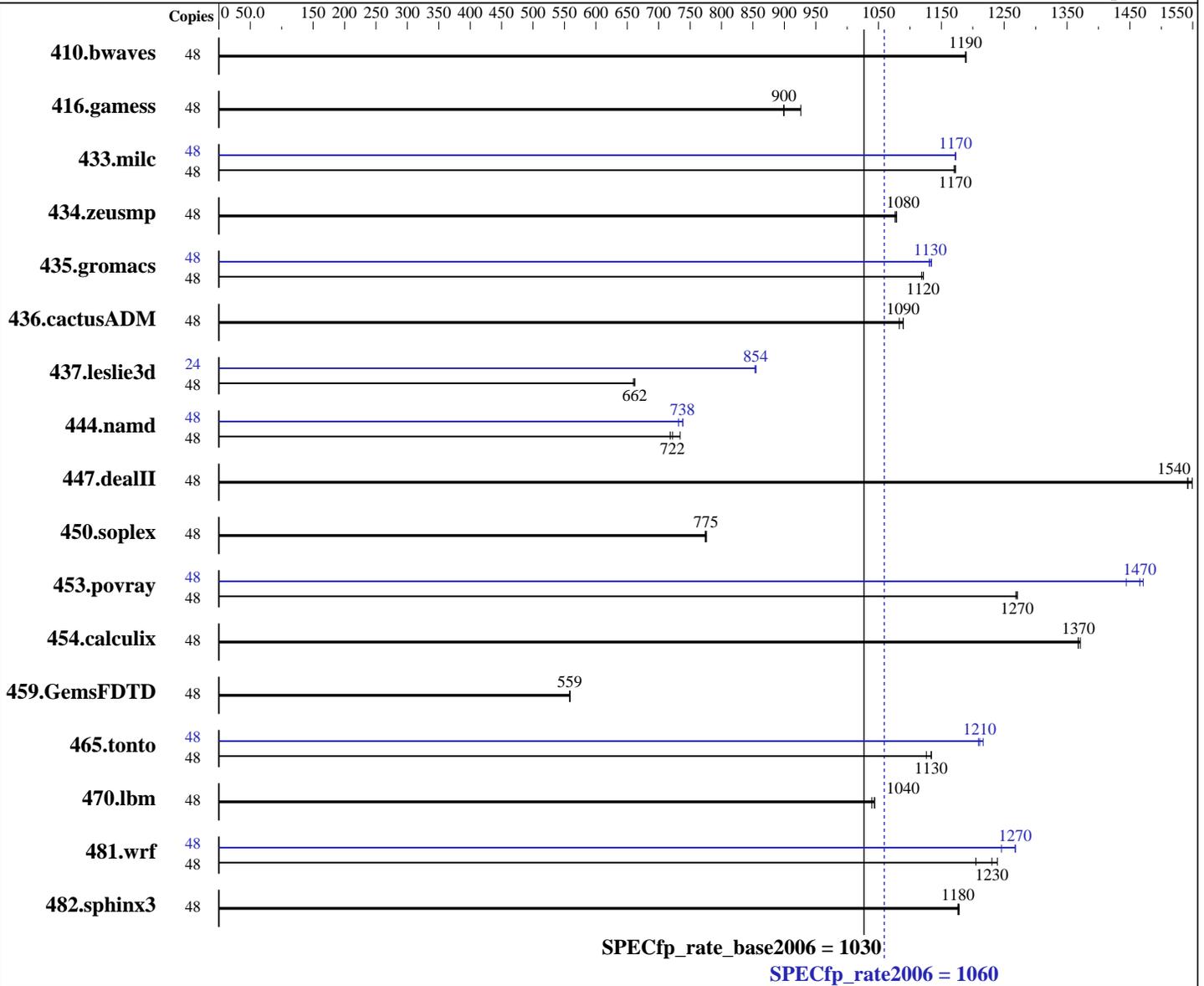
Test sponsor: Huawei

Tested by: Huawei

Test date: Jan-2015

Hardware Availability: Sep-2014

Software Availability: Sep-2014



### Hardware

CPU Name: Intel Xeon E7-8893 v2  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 4 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 2,4 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.0 (Maipo)  
 3.10.0-123.el7.x86\_64  
 Compiler: C/C++: Version 15.0.0.090 of Intel C++ Studio XE for Linux;  
 Fortran: Version 15.0.0.090 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs

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L3 Cache: 37.5 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (32 x 8 GB 2Rx4 PC3-12800R-11, ECC, running at 1333 MHz)  
Disk Subsystem: 1 x 500 GB SATA, 7200 RPM  
Other Hardware: None

System State: Run level 3 (multi-user)  
Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	<b>549</b>	<b>1190</b>	549	1190	548	1190	48	<b>549</b>	<b>1190</b>	549	1190	548	1190
416.gamess	48	1045	899	1015	926	<b>1045</b>	<b>900</b>	48	1045	899	1015	926	<b>1045</b>	<b>900</b>
433.milc	48	<b>376</b>	<b>1170</b>	376	1170	377	1170	48	<b>376</b>	<b>1170</b>	376	1170	376	1170
434.zeusmp	48	406	1080	<b>405</b>	<b>1080</b>	405	1080	48	406	1080	<b>405</b>	<b>1080</b>	405	1080
435.gromacs	48	306	1120	306	1120	<b>306</b>	<b>1120</b>	48	<b>303</b>	<b>1130</b>	303	1130	302	1130
436.cactusADM	48	530	1080	<b>527</b>	<b>1090</b>	527	1090	48	530	1080	<b>527</b>	<b>1090</b>	527	1090
437.leslie3d	48	682	662	<b>682</b>	<b>662</b>	684	660	24	<b>264</b>	<b>854</b>	264	855	264	853
444.namd	48	525	734	<b>533</b>	<b>722</b>	536	718	48	521	739	<b>522</b>	<b>738</b>	526	732
447.dealII	48	<b>356</b>	<b>1540</b>	354	1550	356	1540	48	<b>356</b>	<b>1540</b>	354	1550	356	1540
450.soplex	48	516	776	517	774	<b>516</b>	<b>775</b>	48	516	776	517	774	<b>516</b>	<b>775</b>
453.povray	48	201	1270	201	1270	<b>201</b>	<b>1270</b>	48	<b>174</b>	<b>1470</b>	177	1440	174	1470
454.calculix	48	<b>289</b>	<b>1370</b>	290	1370	289	1370	48	<b>289</b>	<b>1370</b>	290	1370	289	1370
459.GemsFDTD	48	912	559	911	559	<b>912</b>	<b>559</b>	48	912	559	911	559	<b>912</b>	<b>559</b>
465.tonto	48	416	1130	419	1130	<b>417</b>	<b>1130</b>	48	<b>390</b>	<b>1210</b>	388	1220	391	1210
470.lbm	48	635	1040	<b>632</b>	<b>1040</b>	632	1040	48	635	1040	<b>632</b>	<b>1040</b>	632	1040
481.wrf	48	445	1200	<b>436</b>	<b>1230</b>	433	1240	48	<b>423</b>	<b>1270</b>	423	1270	430	1250
482.sphinx3	48	794	1180	795	1180	<b>795</b>	<b>1180</b>	48	794	1180	795	1180	<b>795</b>	<b>1180</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"



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Hardware Availability: Sep-2014

Software Availability: Sep-2014

## Platform Notes

BIOS configuration:

Set Power Efficiency Mode to Custom

Baseboard Management Controller used to adjust the fan speed to 100%

Sysinfo program /spec/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1

running on localhost.localdomain Wed Jan 21 23:40:37 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) CPU E7-8893 v2 @ 3.40GHz

4 "physical id"s (chips)

48 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 6

siblings : 12

physical 0: cores 3 4 5 6 10 11

physical 1: cores 3 4 5 6 10 11

physical 2: cores 3 4 5 6 10 11

physical 3: cores 3 4 5 6 10 11

cache size : 38400 KB

From /proc/meminfo

MemTotal: 263801328 kB

HugePages\_Total: 0

Hugepagesize: 2048 kB

From /etc/\*release\* /etc/\*version\*

os-release:

NAME="Red Hat Enterprise Linux Server"

VERSION="7.0 (Maipo)"

ID="rhel"

ID\_LIKE="fedora"

VERSION\_ID="7.0"

PRETTY\_NAME="Red Hat Enterprise Linux Server 7.0 (Maipo)"

ANSI\_COLOR="0;31"

CPE\_NAME="cpe:/o:redhat:enterprise\_linux:7.0:GA:server"

redhat-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release: Red Hat Enterprise Linux Server release 7.0 (Maipo)

system-release-cpe: cpe:/o:redhat:enterprise\_linux:7.0:ga:server

uname -a:

Linux localhost.localdomain 3.10.0-123.el7.x86\_64 #1 SMP Mon May 5 11:16:57

EDT 2014 x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 3 Jan 21 13:57

SPEC is set to: /spec

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## Platform Notes (Continued)

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda2	xfs	445G	117G	328G	27%	/

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. BLISV308 11/28/2014

Memory:

32x Micron 36KSF1G72PZ-1G6K1 8 GB 2 rank 1600 MHz, configured at 1333 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/spec/libs/32:/spec/libs/64:/spec/sh"

Binaries compiled on a system with 1x Core i5-4670K CPU + 16GB memory using RedHat EL 7.0

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

Filesystem page cache cleared with:

echo 1> /proc/sys/vm/drop\_caches

runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

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Software Availability: Sep-2014

## Base Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

C++ benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

Fortran benchmarks:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch

```

Benchmarks using both Fortran and C:

```

-xAVX -ipo -O3 -no-prec-div -opt-prefetch -auto-p32 -ansi-alias
-opt-mem-layout-trans=3

```

## Peak Compiler Invocation

C benchmarks:

```

icc -m64

```

C++ benchmarks:

```

icpc -m64

```

Fortran benchmarks:

```

ifort -m64

```

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Software Availability: Sep-2014

## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

433.milc: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -auto-ilp32

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: -xAVX -ipo -O3 -no-prec-div -opt-prefetch

459.GemsFDTD: basepeak = yes

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## Peak Optimization Flags (Continued)

465.tonto: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xAVX(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: -xAVX -ipo -O3 -no-prec-div -auto-ilp32

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic15.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Huawei-Platform-Settings-V1.0-IVB-RevG.xml>

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