



SPEC® CINT2006 Result

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Fujitsu

SPECint®2006 = 64.1

PRIMERGY TX1330 M2, Intel Core i3-6100, 3.70 GHz

SPECint_base2006 = 61.9

CPU2006 license: 19

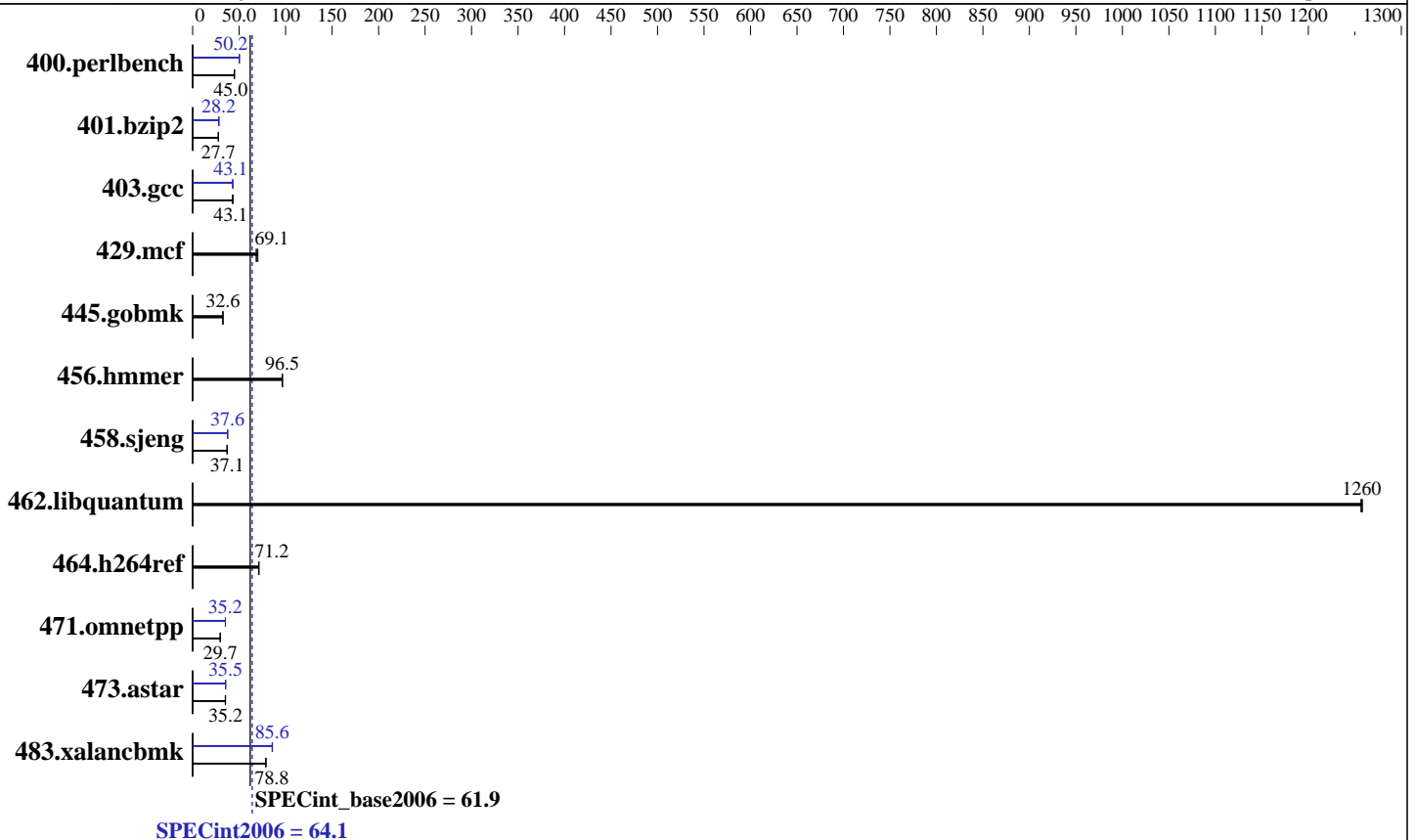
Test date: Nov-2015

Test sponsor: Fujitsu

Hardware Availability: Feb-2016

Tested by: Fujitsu

Software Availability: Sep-2015



Hardware

CPU Name: Intel Core i3-6100
 CPU Characteristics: 3700
 CPU MHz: Integrated
 FPU: 2 cores, 1 chip, 2 cores/chip, 2 threads/core
 CPU(s) enabled: 1 chip
 CPU(s) orderable: 32 KB I + 32 KB D on chip per core
 Primary Cache: 256 KB I+D on chip per core
 Secondary Cache: 3 MB I+D on chip per chip
 L3 Cache: None
 Other Cache: 64 GB (4 x 16 GB 2Rx8 PC4-2133P-E)
 Memory: 1 x SATA, 500 GB, 7200 RPM
 Disk Subsystem: None
 Other Hardware:

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64)
 Kernel 3.12.48-52.27-default
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux
 Auto Parallel: Yes
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	219	44.7	<u>217</u>	<u>45.0</u>	217	45.1	194	50.3	195	50.1	<u>195</u>	<u>50.2</u>
401.bzip2	<u>349</u>	<u>27.7</u>	348	27.7	350	27.6	341	28.3	344	28.0	<u>343</u>	<u>28.2</u>
403.gcc	186	43.2	<u>187</u>	<u>43.1</u>	187	43.0	186	43.2	<u>187</u>	<u>43.1</u>	187	43.0
429.mcf	<u>132</u>	<u>69.1</u>	134	68.2	130	70.2	<u>132</u>	<u>69.1</u>	134	68.2	130	70.2
445.gobmk	322	32.5	<u>322</u>	<u>32.6</u>	322	32.6	<u>322</u>	<u>32.5</u>	<u>322</u>	<u>32.6</u>	322	32.6
456.hammer	<u>96.7</u>	<u>96.5</u>	96.8	96.4	96.6	96.6	<u>96.7</u>	<u>96.5</u>	96.8	96.4	96.6	96.6
458.sjeng	326	37.1	<u>326</u>	<u>37.1</u>	326	37.1	<u>322</u>	<u>37.6</u>	<u>322</u>	<u>37.6</u>	322	37.6
462.libquantum	<u>16.5</u>	<u>1260</u>	16.5	1260	16.5	1260	<u>16.5</u>	<u>1260</u>	16.5	1260	16.5	1260
464.h264ref	<u>311</u>	<u>71.2</u>	311	71.2	311	71.1	<u>311</u>	<u>71.2</u>	311	71.2	311	71.1
471.omnetpp	210	29.8	211	29.6	<u>211</u>	<u>29.7</u>	177	35.3	178	35.2	<u>177</u>	<u>35.2</u>
473.astar	200	35.1	198	35.4	<u>199</u>	<u>35.2</u>	198	35.4	198	35.5	<u>198</u>	<u>35.5</u>
483.xalancbmk	<u>87.6</u>	<u>78.8</u>	87.5	78.9	87.7	78.7	80.3	85.9	<u>80.6</u>	<u>85.6</u>	80.7	85.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
\$Rev: 6914 \$ \$Date:: 2014-06-25 #\$ e3fbb8667b5a285932ceab81e28219e1
running on TX1330M2 Mon Nov 23 16:23:53 2015

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Core(TM) i3-6100 CPU @ 3.70GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 2
siblings : 4
physical 0: cores 0 1
```

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Platform Notes (Continued)

cache size : 3072 KB

From /proc/meminfo

MemTotal: 65902560 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 0

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12"

VERSION_ID="12"

PRETTY_NAME="SUSE Linux Enterprise Server 12"

ID="sles"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12"

uname -a:

Linux TX1330M2 3.12.28-4-default #1 SMP Thu Sep 25 17:02:34 UTC 2014 (9879bd4) x86_64 x86_64 x86_64 GNU/Linux

run-level 5 Nov 20 16:43

SPEC is set to: /home/SPECcpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	237G	12G	225G	5%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.1.0 for D3373-A1x 10/30/2015

Memory:

4x SK Hynix HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)



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General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

OMP_NUM_THREADS = "2"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent_hugepage/enabled

For information about Fujitsu please visit: <http://www.fujitsu.com>

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh -lsmartheap64



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Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32
-opt-prefetch -ansi-alias

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Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-opt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2)
-opt-ra-region-strategy=block -ansi-alias
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>



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For other inquiries, please contact webmaster@spec.org.

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