



# SPEC® CFP2006 Result

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## Supermicro

Supermicro X11SAT-F motherboard  
(X11SAT-F, Intel Core i5-6400T)

SPECfp®\_rate2006 = 152

SPECfp\_rate\_base2006 = 149

CPU2006 license: 001176

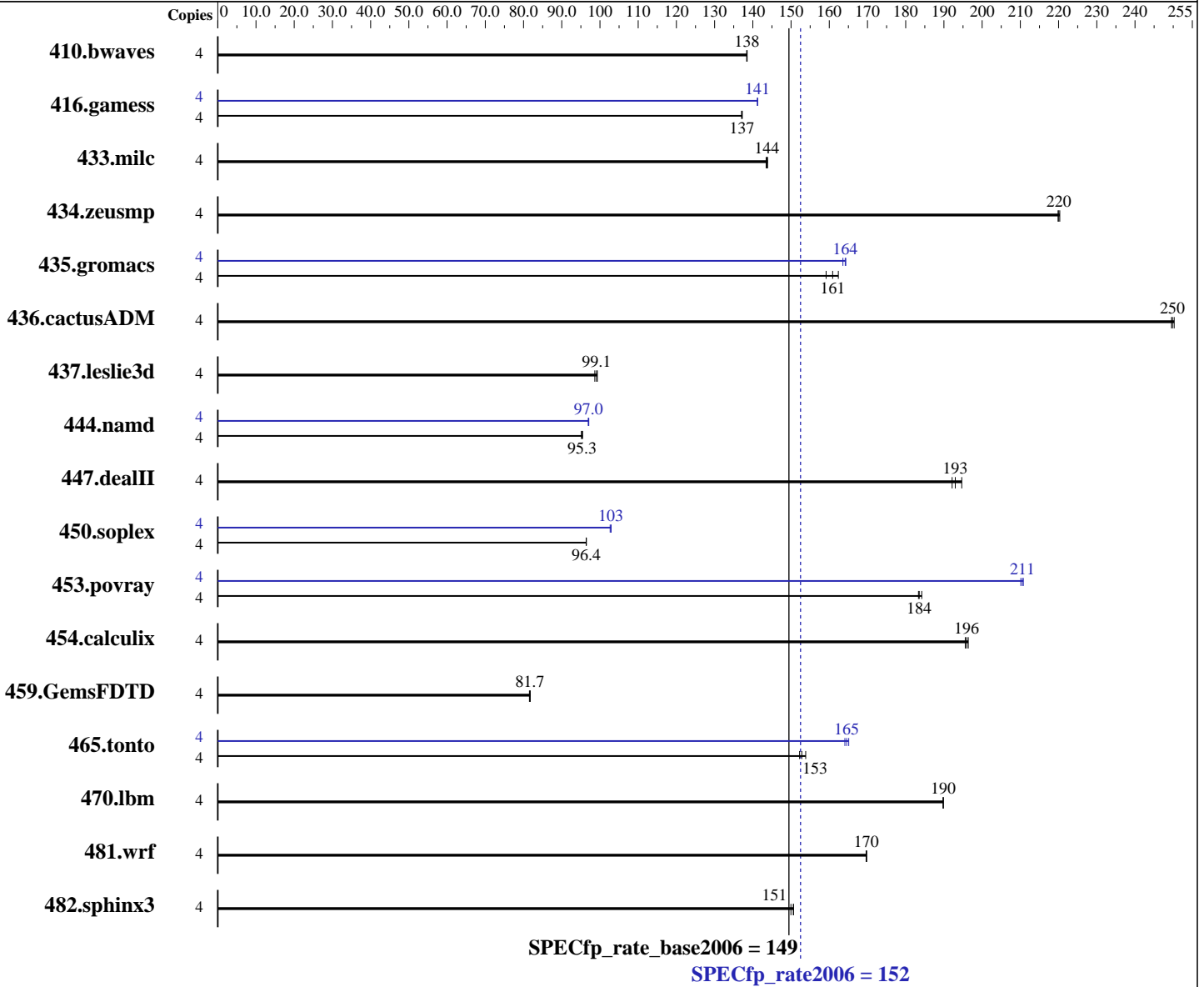
Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Core i5-6400T  
 CPU Characteristics: Intel Turbo Boost Technology up to 2.80 GHz  
 CPU MHz: 2200  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

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### Software

Operating System: Red Hat Enterprise Linux Server release 7.1, Kernel 3.10.0-229.el7.x86\_64  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: xfs  
 System State: Run level 3 (multi-user)

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L3 Cache: 6 MB I+D on chip per chip  
Other Cache: None  
Memory: 16 GB (4 x 4 GB 1Rx8 PC4-2133P-U)  
Disk Subsystem: 1 x 200 GB SATA III SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	<u>393</u>	<u>138</u>	393	138	392	139	4	<u>393</u>	<u>138</u>	393	138	392	139
416.gamess	4	<u>571</u>	<u>137</u>	571	137	571	137	4	555	141	<u>555</u>	<u>141</u>	555	141
433.milc	4	255	144	256	143	<u>255</u>	<u>144</u>	4	255	144	256	143	<u>255</u>	<u>144</u>
434.zeusmp	4	166	220	165	220	<u>165</u>	<u>220</u>	4	166	220	165	220	<u>165</u>	<u>220</u>
435.gromacs	4	176	162	<u>178</u>	<u>161</u>	179	159	4	175	164	<u>174</u>	<u>164</u>	174	164
436.cactusADM	4	191	250	<u>191</u>	<u>250</u>	192	250	4	191	250	<u>191</u>	<u>250</u>	192	250
437.leslie3d	4	379	99.3	<u>379</u>	<u>99.1</u>	381	98.7	4	379	99.3	<u>379</u>	<u>99.1</u>	381	98.7
444.namd	4	336	95.5	337	95.1	<u>336</u>	<u>95.3</u>	4	<u>331</u>	<u>97.0</u>	331	96.9	330	97.1
447.dealII	4	238	192	<u>237</u>	<u>193</u>	235	195	4	238	192	<u>237</u>	<u>193</u>	235	195
450.soplex	4	346	96.4	346	96.4	<u>346</u>	<u>96.4</u>	4	<u>324</u>	<u>103</u>	324	103	325	103
453.povray	4	116	183	116	184	<u>116</u>	<u>184</u>	4	101	210	<u>101</u>	<u>211</u>	101	211
454.calculix	4	168	196	<u>168</u>	<u>196</u>	169	196	4	168	196	<u>168</u>	<u>196</u>	169	196
459.GemsFDTD	4	520	81.5	519	81.7	<u>520</u>	<u>81.7</u>	4	520	81.5	519	81.7	<u>520</u>	<u>81.7</u>
465.tonto	4	256	154	<u>258</u>	<u>153</u>	259	152	4	238	165	<u>239</u>	<u>165</u>	240	164
470.lbm	4	290	190	290	190	<u>290</u>	<u>190</u>	4	290	190	290	190	<u>290</u>	<u>190</u>
481.wrf	4	263	170	<u>263</u>	<u>170</u>	263	170	4	263	170	<u>263</u>	<u>170</u>	263	170
482.sphinx3	4	520	150	518	151	<u>518</u>	<u>151</u>	4	520	150	518	151	<u>518</u>	<u>151</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

As tested, the system used a Supermicro CSE-732D4-903B chassis. The chassis is configured with a PWS-903-PQ power supply, 1 SNK-P0046A4 heatsink, as well as 1 FAN-0124L4 chassis fan.

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SPECfp\_rate\_base2006 = 149

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: Jan-2016

Hardware Availability: Sep-2015

Software Availability: Sep-2015

### Platform Notes (Continued)

```
Sysinfo program /usr/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on X11SAT-01 Thu Jan 7 19:15:36 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Core(TM) i5-6400T CPU @ 2.20GHz
1 "physical id"s (chips)
4 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
cpu cores : 4
siblings : 4
physical 0: cores 0 1 2 3
cache size : 6144 KB
```

```
From /proc/meminfo
MemTotal: 16248396 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.1 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.1"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.1 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.1:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.1 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.1:ga:server
```

```
uname -a:
Linux X11SAT-01 3.10.0-229.el7.x86_64 #1 SMP Thu Jan 29 18:37:38 EST 2015
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 7 01:36
```

```
SPEC is set to: /usr/cpu2006
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda2 xfs 183G 45G 139G 25% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately  
Continued on next page



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## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 1.0 12/18/2015

Memory:

4x Micron 8ATF51264AZ-2G1A2 4 GB 1 rank 2133 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/usr/cpu2006/libs/32:/usr/cpu2006/libs/64:/usr/cpu2006/sh"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

416.gamess: -DSPEC\_CPU\_LP64

433.milc: -DSPEC\_CPU\_LP64

434.zeusmp: -DSPEC\_CPU\_LP64

435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main

436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main

437.leslie3d: -DSPEC\_CPU\_LP64

444.namd: -DSPEC\_CPU\_LP64

447.dealII: -DSPEC\_CPU\_LP64

450.soplex: -DSPEC\_CPU\_LP64

453.povray: -DSPEC\_CPU\_LP64

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CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: Jan-2016  
Hardware Availability: Sep-2015  
Software Availability: Sep-2015

## Base Portability Flags (Continued)

454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks (except as noted below):  
icpc -m64  
450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

```

416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
         -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
         -par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
         -auto-ilp32

```

447.dealII: basepeak = yes

```

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
          -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
          -par-num-threads=1(pass 1) -prof-use(pass 2)
          -opt-malloc-options=3

```

```

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
          -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
          -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14
          -ansi-alias

```

Fortran benchmarks:

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## Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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