



# SPEC® CINT2006 Result

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## Fujitsu

PRIMERGY TX1320 M2, Intel Xeon E3-1270 v5, 3.60 GHz

**SPECint®2006 = 75.3**

**SPECint\_base2006 = 72.7**

CPU2006 license: 19

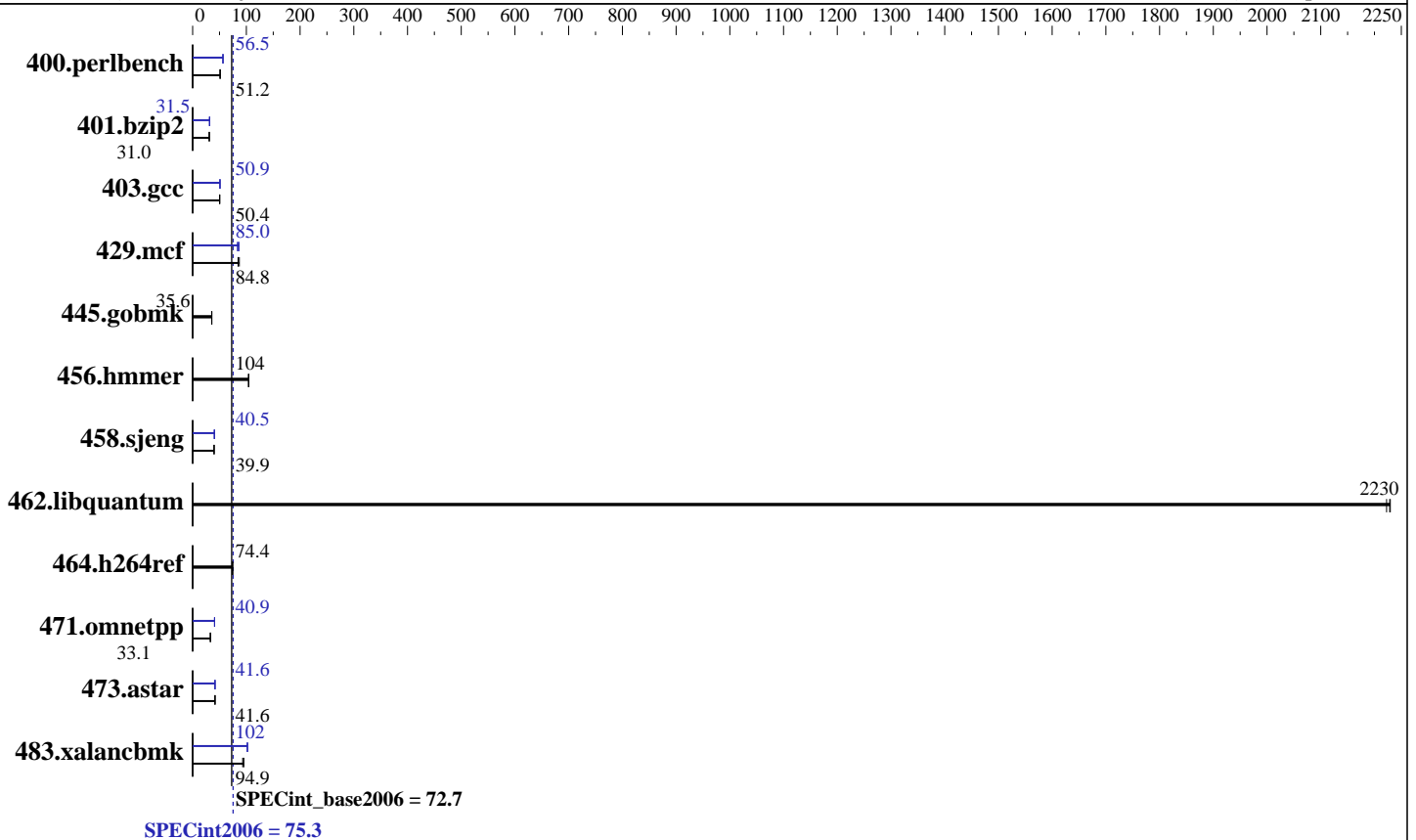
Test sponsor: Fujitsu

Tested by: Fujitsu

Test date: Dec-2015

Hardware Availability: Feb-2016

Software Availability: Sep-2015



### Hardware

CPU Name: Intel Xeon E3-1270 v5  
 CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
 CPU MHz: 3600  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip, 2 threads/core  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core  
 L3 Cache: 8 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 64 GB (4 x 16 GB 2Rx8 PC4-2133P-E)  
 Disk Subsystem: 1 x SATA, 500 GB, 7200 RPM  
 Other Hardware: None

### Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64)  
 Kernel 3.12.48-52.27-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux  
 Auto Parallel: Yes  
 File System: ext4  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	191	51.3	<b><u>191</u></b>	<b><u>51.2</u></b>	191	51.1	<b><u>173</u></b>	<b><u>56.3</u></b>	173	56.6	<b><u>173</u></b>	<b><u>56.5</u></b>
401.bzip2	313	30.9	<b><u>312</u></b>	<b><u>31.0</u></b>	311	31.1	<b><u>307</u></b>	<b><u>31.5</u></b>	306	31.5	<b><u>306</u></b>	<b><u>31.5</u></b>
403.gcc	160	50.4	<b><u>160</u></b>	<b><u>50.4</u></b>	160	50.2	<b><u>158</u></b>	<b><u>50.9</u></b>	158	50.8	158	50.9
429.mcf	105	86.9	<b><u>108</u></b>	<b><u>84.8</u></b>	108	84.5	<b><u>107</u></b>	<b><u>85.0</u></b>	110	83.3	106	86.2
445.gobmk	294	35.7	295	35.6	<b><u>294</u></b>	<b><u>35.6</u></b>	294	35.7	295	35.6	<b><u>294</u></b>	<b><u>35.6</u></b>
456.hammer	89.4	104	<b><u>89.5</u></b>	<b><u>104</u></b>	89.6	104	89.4	104	<b><u>89.5</u></b>	<b><u>104</u></b>	89.6	104
458.sjeng	303	40.0	<b><u>303</u></b>	<b><u>39.9</u></b>	303	39.9	299	40.5	299	40.5	<b><u>299</u></b>	<b><u>40.5</u></b>
462.libquantum	<b><u>9.30</u></b>	<b><u>2230</u></b>	9.29	2230	9.32	2220	<b><u>9.30</u></b>	<b><u>2230</u></b>	9.29	2230	9.32	2220
464.h264ref	297	74.5	<b><u>297</u></b>	<b><u>74.4</u></b>	298	74.4	297	74.5	<b><u>297</u></b>	<b><u>74.4</u></b>	298	74.4
471.omnetpp	188	33.3	<b><u>189</u></b>	<b><u>33.1</u></b>	190	32.9	<b><u>153</u></b>	<b><u>40.9</u></b>	153	41.0	154	40.6
473.astar	<b><u>169</u></b>	<b><u>41.6</u></b>	169	41.5	168	41.8	<b><u>169</u></b>	<b><u>41.6</u></b>	168	41.8	169	41.5
483.xalancbmk	72.6	95.0	74.1	93.1	<b><u>72.7</u></b>	<b><u>94.9</u></b>	67.6	102	67.8	102	<b><u>67.7</u></b>	<b><u>102</u></b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The config file option 'submit' was used.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS configuration:

```
Sysinfo program /home/SPECcpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 $# e3fbb8667b5a285932ceab81e28219e1
running on TX1320M2 Tue Dec 8 18:46:21 2015
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E3-1270 v5 @ 3.60GHz
1 "physical id"s (chips)
8 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 4
siblings : 8
physical 0: cores 0 1 2 3
```

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### Platform Notes (Continued)

cache size : 8192 KB

From /proc/meminfo

MemTotal: 65906076 kB  
HugePages\_Total: 0  
Hugepagesize: 2048 kB

/usr/bin/lsb\_release -d

SUSE Linux Enterprise Server 12

From /etc/\*release\* /etc/\*version\*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86\_64)

VERSION = 12

PATCHLEVEL = 0

# This file is deprecated and will be removed in a future service pack or release.

# Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12"

VERSION\_ID="12"

PRETTY\_NAME="SUSE Linux Enterprise Server 12"

ID="sles"

ANSI\_COLOR="0;32"

CPE\_NAME="cpe:/o:suse:sles:12"

uname -a:

Linux TX1320M2 3.12.48-52.27-default #1 SMP Mon Oct 5 10:08:10 UTC 2015  
(314f0e3) x86\_64 x86\_64 x86\_64 GNU/Linux

run-level 5 Dec 8 18:44

SPEC is set to: /home/SPECcpu2006

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	1.6T	3.9G	1.6T	1%	/home

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.11 R1.1.0 for D3373-Alx  
10/30/2015

Memory:

4x SK Hynix HMA82GU7MFR8N-TF 16 GB 2 rank 2133 MHz

(End of data from sysinfo program)



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## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/home/SPECcpu2006/libs/32:/home/SPECcpu2006/libs/64:/home/SPECcpu2006/sh"

OMP\_NUM\_THREADS = "4"

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

echo always > /sys/kernel/mm/transparent\_hugepage/enabled

For information about Fujitsu please visit: <http://www.fujitsu.com>

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -opt-prefetch -auto-p32

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh -lsmartheap64



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## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

C++ benchmarks (except as noted below):

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -opt-prefetch  
-ansi-alias

401.bzip2: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div  
-par-num-threads=1(pass 1) -prof-use(pass 2) -auto-ilp32  
-opt-prefetch -ansi-alias

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## Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
-opt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
-opt-prefetch -auto-p32

445.gobmk: basepeak = yes

456.hmmer: basepeak = yes

458.sjeng: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2)  
-opt-ra-region-strategy=block -ansi-alias  
-Wl,-z,muldefs -L/sh -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-auto-p32 -Wl,-z,muldefs -L/sh -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch  
-ansi-alias -Wl,-z,muldefs -L/sh -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Fujitsu-Platform-Settings-V1.2-HSW-RevA.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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