



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF , Intel Xeon E5-2660 v4)

SPECfp<sup>®</sup>\_rate2006 = 889

SPECfp\_rate\_base2006 = 867

CPU2006 license: 001176

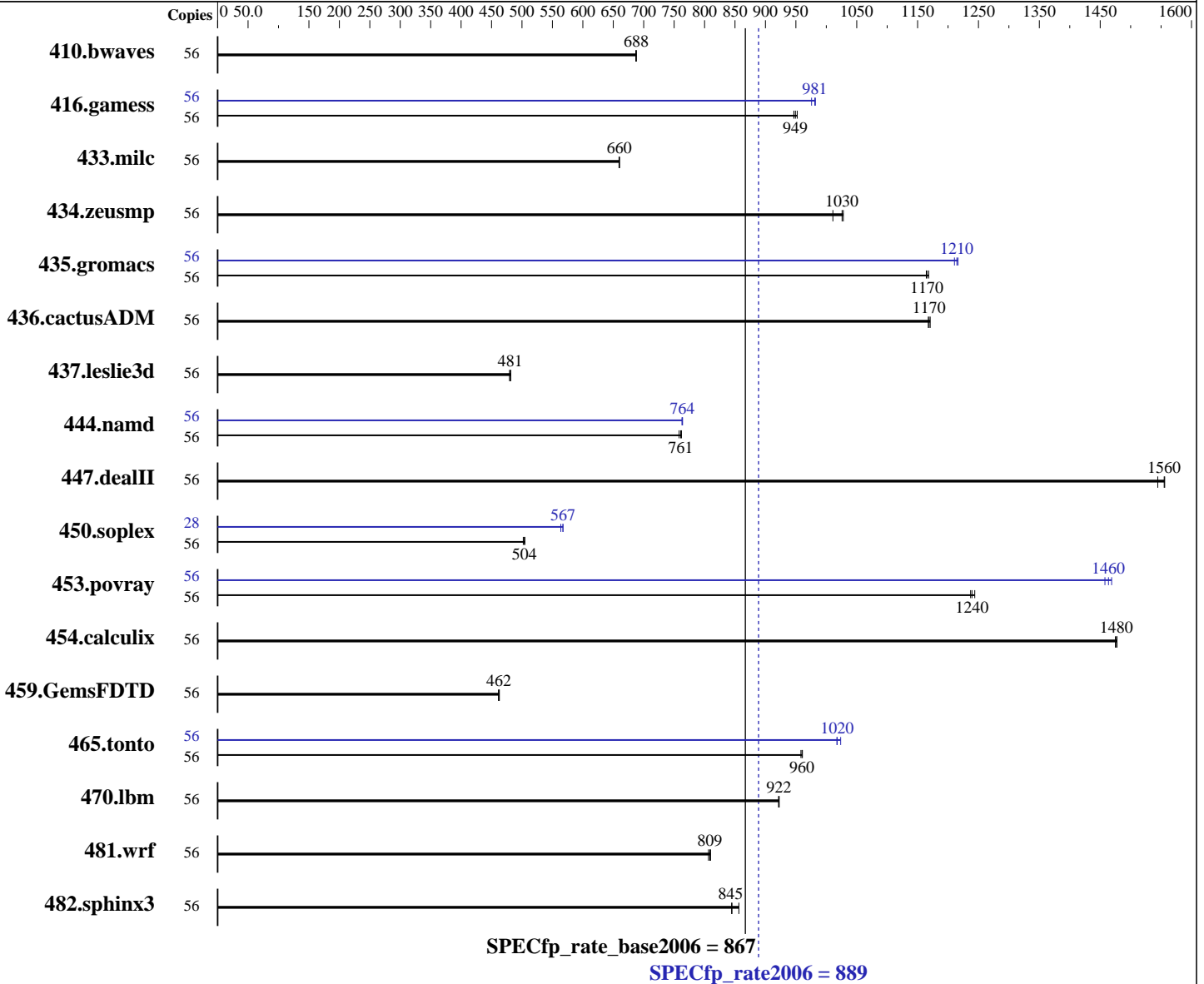
Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015



### Hardware

CPU Name: Intel Xeon E5-2660 v4  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
 CPU MHz: 2000  
 FPU: Integrated  
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

### Software

Operating System: SUSE Linux Enterprise Server 12 SP1, Kernel 3.12.49-11-default  
 Compiler: C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux;  
 Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux  
 Auto Parallel: No  
 File System: ext4  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF, Intel Xeon E5-2660 v4)

SPECfp\_rate2006 = **889**

SPECfp\_rate\_base2006 = **867**

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400P-R)  
Disk Subsystem: 1 x 400 GB, NVMe  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	1106	688	<u>1107</u>	<u>688</u>	1108	687	56	1106	688	<u>1107</u>	<u>688</u>	1108	687
416.gamess	56	1158	947	<u>1155</u>	<u>949</u>	1151	952	56	1116	982	1124	976	<u>1118</u>	<u>981</u>
433.milc	56	780	659	<u>779</u>	<u>660</u>	779	660	56	780	659	<u>779</u>	<u>660</u>	779	660
434.zeusmp	56	<u>497</u>	<u>1030</u>	504	1010	496	1030	56	<u>497</u>	<u>1030</u>	504	1010	496	1030
435.gromacs	56	<u>343</u>	<u>1170</u>	343	1160	342	1170	56	330	1210	<u>329</u>	<u>1210</u>	329	1220
436.cactusADM	56	573	1170	572	1170	<u>573</u>	<u>1170</u>	56	573	1170	572	1170	<u>573</u>	<u>1170</u>
437.leslie3d	56	1093	482	<u>1095</u>	<u>481</u>	1098	479	56	1093	482	<u>1095</u>	<u>481</u>	1098	479
444.namd	56	589	762	<u>590</u>	<u>761</u>	592	758	56	589	763	588	764	<u>588</u>	<u>764</u>
447.dealII	56	<u>412</u>	<u>1560</u>	415	1540	412	1560	56	<u>412</u>	<u>1560</u>	415	1540	412	1560
450.soplex	56	925	505	<u>927</u>	<u>504</u>	930	502	28	414	564	412	567	<u>412</u>	<u>567</u>
453.povray	56	241	1240	<u>240</u>	<u>1240</u>	240	1240	56	<u>204</u>	<u>1460</u>	203	1470	204	1460
454.calculix	56	313	1470	313	1480	<u>313</u>	<u>1480</u>	56	313	1470	313	1480	<u>313</u>	<u>1480</u>
459.GemsFDTD	56	1288	461	<u>1285</u>	<u>462</u>	1284	463	56	1288	461	<u>1285</u>	<u>462</u>	1284	463
465.tonto	56	<u>574</u>	<u>960</u>	575	958	574	961	56	542	1020	538	1020	<u>541</u>	<u>1020</u>
470.lbm	56	835	922	<u>834</u>	<u>922</u>	834	923	56	835	922	<u>834</u>	<u>922</u>	834	923
481.wrf	56	776	806	772	810	<u>774</u>	<u>809</u>	56	776	806	772	810	<u>774</u>	<u>809</u>
482.sphinx3	56	1275	856	<u>1292</u>	<u>845</u>	1293	844	56	1275	856	<u>1292</u>	<u>845</u>	1293	844

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS Settings:  
COD Enable = Enable  
Early Snoop = Disable

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF, Intel Xeon E5-2660 v4)

SPECfp\_rate2006 = 889

SPECfp\_rate\_base2006 = 867

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: May-2016  
Hardware Availability: Mar-2016  
Software Availability: Dec-2015

### Platform Notes (Continued)

```
Home Dir Snoop with IVT- Style OSB = Disable
Enforce POR = Disabled
Sysinfo program /home/SPEC2K6/SPEC2006-V12/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on 30-113 Fri May 20 19:44:19 2016
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2660 v4@ 2.00GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB
```

```
From /proc/meminfo
MemTotal:      264564912 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

```
uname -a:
Linux 30-113 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0)
x86_64 x86_64 x86_64 GNU/Linux
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF, Intel Xeon E5-2660 v4)

SPECfp\_rate2006 = 889

SPECfp\_rate\_base2006 = 867

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: May-2016  
Hardware Availability: Mar-2016  
Software Availability: Dec-2015

### Platform Notes (Continued)

run-level 3 May 20 19:32

SPEC is set to: /home/SPEC2K6/SPEC2006-V12

```
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p4 ext4  355G  7.4G  347G   3% /home
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS American Megatrends Inc. 2.0a 05/17/2016

Memory:

16x Samsung M393A2G40DB1-CRC 16 GB 2 rank 2400 MHz

(End of data from sysinfo program)

### General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/SPEC2K6/SPEC2006-V12/libs/32:/home/SPEC2K6/SPEC2006-V12/libs/64:/home/SPEC2K6/SPEC2006-V12/sh"
```

Binaries compiled on a system with 1x Intel Core i5-4670K CPU + 32GB memory using RedHat EL 7.1

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

### Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF, Intel Xeon E5-2660 v4)

SPECfp\_rate2006 = 889

SPECfp\_rate\_base2006 = 867

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: May-2016  
Hardware Availability: Mar-2016  
Software Availability: Dec-2015

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -DSPEC\_CPU\_LP64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:  
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32  
-ansi-alias -opt-mem-layout-trans=3

## Peak Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks (except as noted below):  
icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF, Intel Xeon E5-2660 v4)

SPECfp\_rate2006 = 889

SPECfp\_rate\_base2006 = 867

CPU2006 license: 001176  
Test sponsor: Supermicro  
Tested by: Supermicro

Test date: May-2016  
Hardware Availability: Mar-2016  
Software Availability: Dec-2015

## Peak Compiler Invocation (Continued)

Fortran benchmarks:  
ifort -m64

Benchmarks using both Fortran and C:  
icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

447.dealII: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF, Intel Xeon E5-2660 v4)

SPECfp\_rate2006 = 889

SPECfp\_rate\_base2006 = 867

CPU2006 license: 001176

Test sponsor: Supermicro

Tested by: Supermicro

Test date: May-2016

Hardware Availability: Mar-2016

Software Availability: Dec-2015

## Peak Optimization Flags (Continued)

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2  
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto  
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.20160628.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Supermicro-Platform-Settings-V1.2-revH.20160628.xml>



# SPEC CFP2006 Result

Copyright 2006-2016 Standard Performance Evaluation Corporation

## Supermicro

SuperServer 2028TP-DNCFR  
(X10DRT-PIBF , Intel Xeon E5-2660 v4)

SPECfp\_rate2006 = 889

SPECfp\_rate\_base2006 = 867

**CPU2006 license:** 001176

**Test sponsor:** Supermicro

**Tested by:** Supermicro

**Test date:** May-2016

**Hardware Availability:** Mar-2016

**Software Availability:** Dec-2015

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Tue Jun 28 17:30:48 2016 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 28 June 2016.