SPEC® CFP2006 Result

Hewlett Packard Enterprise

(Test Sponsor: HPE)

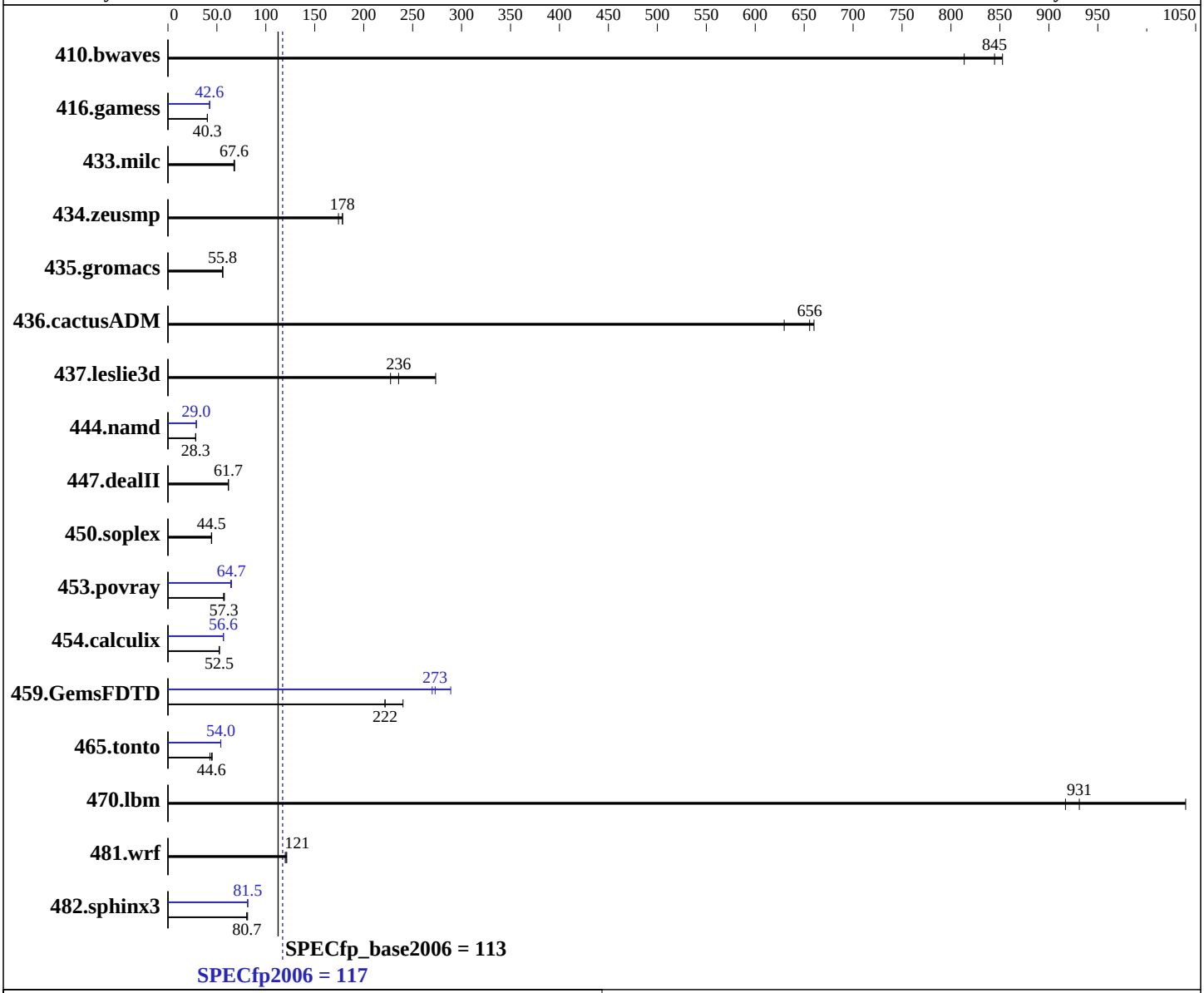
ProLiant BL660c Gen9
(2.60 GHz, Intel Xeon E5-4627 v4)

SPECfp®2006 = 117

SPECfp_base2006 = 113

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Jun-2016
Hardware Availability: Jun-2016
Software Availability: Nov-2015



Hardware

CPU Name:	Intel Xeon E5-4627 v4
CPU Characteristics:	Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz:	2600
FPU:	Integrated
CPU(s) enabled:	40 cores, 4 chips, 10 cores/chip
CPU(s) orderable:	2,4 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	256 KB I+D on chip per core

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 7.2 (Maipo) Kernel 3.10.0-327.el7.x86_64
Compiler:	C/C++: Version 16.0.0.101 of Intel C++ Studio XE for Linux; Fortran: Version 16.0.0.101 of Intel Fortran Studio XE for Linux
Auto Parallel:	Yes
File System:	xfs

Continued on next page

SPEC CFP2006 Result

Hewlett Packard Enterprise
(Test Sponsor: HPE)

ProLiant BL660c Gen9
(2.60 GHz, Intel Xeon E5-4627 v4)

SPECfp2006 = 117

SPECfp_base2006 = 113

CPU2006 license: 3
Test sponsor: HPE
Tested by: HPE

Test date: Jun-2016
Hardware Availability: Jun-2016
Software Availability: Nov-2015

L3 Cache: 25 MB I+D on chip per chip
Other Cache: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-2400T-R)
Disk Subsystem: 1 x 400 GB SAS SSD, RAID 0
Other Hardware: None

System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16.7	814	**16.1**	**845**	15.9	853	16.7	814	**16.1**	**845**	15.9	853
416.gamess	**485**	**40.3**	485	40.4	488	40.2	**460**	**42.6**	460	42.6	460	42.6
433.milc	135	68.1	136	67.4	**136**	**67.6**	135	68.1	136	67.4	**136**	**67.6**
434.zeusmp	**51.1**	**178**	52.2	174	51.0	178	**51.1**	**178**	52.2	174	51.0	178
435.gromacs	127	56.1	128	55.8	**128**	**55.8**	127	56.1	128	55.8	**128**	**55.8**
436.cactusADM	19.0	630	18.1	660	**18.2**	**656**	19.0	630	18.1	660	**18.2**	**656**
437.leslie3d	**39.9**	**236**	41.3	227	34.4	273	**39.9**	**236**	41.3	227	34.4	273
444.namd	283	28.3	**283**	**28.3**	283	28.3	**277**	**29.0**	277	29.0	277	29.0
447.dealII	185	61.8	**185**	**61.7**	185	61.7	185	61.8	**185**	**61.7**	185	61.7
450.soplex	188	44.4	**187**	**44.5**	187	44.7	188	44.4	**187**	**44.5**	187	44.7
453.povray	92.2	57.7	93.6	56.8	**92.9**	**57.3**	81.9	64.9	**82.2**	**64.7**	83.0	64.1
454.calculix	157	52.6	158	52.4	**157**	**52.5**	146	56.6	**146**	**56.6**	146	56.7
459.GemsFDTD	**47.8**	**222**	44.2	240	47.9	222	36.7	289	39.3	270	**38.9**	**273**
465.tonto	229	42.9	218	45.2	**221**	**44.6**	**182**	**54.0**	183	53.9	182	54.0
470.lbm	13.2	1040	**14.8**	**931**	15.0	917	13.2	1040	**14.8**	**931**	15.0	917
481.wrf	92.0	121	93.2	120	**92.6**	**121**	92.0	121	93.2	120	**92.6**	**121**
482.sphinx3	240	81.4	**241**	**80.7**	242	80.5	**239**	**81.5**	239	81.5	240	81.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Platform Notes

```
BIOS Configuration:
  HP Power Profile set to Custom
  HP Power Regulator to HP Static High Performance Mode
  Minimum Processor Idle Power Core C-State set to C1E State
  Minimum Processor Idle Power Package C-State set to No Package State
  QPI Snoop Configuration set to Home Snoop
  Collaborative Power Control set to Disabled
  Thermal Configuration set to Maximum Cooling
```

Continued on next page

Platform Notes (Continued)

```
  Processor Power and Utilization Monitoring set to Disabled
Sysinfo program /home/cpuv1.5/cpu2006/config/sysinfo.rev6914
$Rev: 6914 $ $Date:: 2014-06-25 #$ e3fbb8667b5a285932ceab81e28219e1
running on BL660-Gen9-B Tue Jun 14 09:05:48 2016

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) CPU E5-4627 v4 @ 2.60GHz
      4 "physical id"s (chips)
      40 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 10
      siblings  : 10
      physical 0: cores 0 2 3 4 8 9 10 11 12
      physical 1: cores 0 2 3 4 8 9 10 11 12
      physical 2: cores 0 2 3 4 8 9 10 11 12
      physical 3: cores 0 2 3 4 8 9 10 11 12
   cache size : 25600 KB

From /proc/meminfo
   MemTotal:       528063264 kB
   HugePages_Total:       0
   Hugepagesize:       2048 kB

From /etc/*release* /etc/*version*
   os-release:
      NAME="Red Hat Enterprise Linux Server"
      VERSION="7.2 (Maipo)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="7.2"
      PRETTY_NAME="Red Hat Enterprise Linux Server 7.2 (Maipo)"
      ANSI_COLOR="0;31"
      CPE_NAME="cpe:/o:redhat:enterprise_linux:7.2:GA:server"
   redhat-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release: Red Hat Enterprise Linux Server release 7.2 (Maipo)
   system-release-cpe: cpe:/o:redhat:enterprise_linux:7.2:ga:server

uname -a:
   Linux BL660-Gen9-B 3.10.0-327.el7.x86_64 #1 SMP Thu Oct 29 17:29:29 EDT 2015
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jun 13 09:44

SPEC is set to: /home/cpuv1.5/cpu2006
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda5      xfs   318G   77G  242G  24% /home
```

Continued on next page

Platform Notes (Continued)

```
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS HP I38 05/05/2016
  Memory:
   16x UNKNOWN NOT AVAILABLE
   16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz

(End of data from sysinfo program)
Regarding the sysinfo display about the memory installed, the correct amount of
memory is 512 GB and the dmidecode description should have one line reading as:
16x UNKNOWN NOT AVAILABLE 32 GB 2 rank 2400 MHz
```

General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
OMP_NUM_THREADS = "40"
LD_LIBRARY_PATH = "/home/cpuv1.5/cpu2006/libs/32:/home/cpuv1.5/cpu2006/libs/64:/home/cpuv1.5/cpu2006/sh"

 Binaries compiled on a system with 1x Intel Xeon E5-2660 v4 CPU + 128GB
 memory using RedHat EL 7.2
```

Base Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint

C++ benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -opt-prefetch -ansi-alias

Fortran benchmarks:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-fp-model fast=2
-qopt-prefetch-issue-excl-hint

Benchmarks using both Fortran and C:
-xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch
-ansi-alias -qopt-prefetch-issue-excl-hint
-fp-model fast=2

Peak Compiler Invocation

C benchmarks:
icc -m64

C++ benchmarks:
icpc -m64

Fortran benchmarks:
ifort -m64

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: -xCORE-AVX2 -ipo -O3 -no-prec-div -static -parallel
-opt-prefetch -ansi-alias
-fp-model fast=2 -nofor-main
-qopt-prefetch-issue-excl-hint -funroll-all-loops

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -fno-alias
-auto-ilp32

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

Continued on next page

Peak Optimization Flags (Continued)

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2 -inline-level=0 -opt-prefetch -parallel

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1) -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2) -par-num-threads=1(pass 1) -prof-use(pass 2) -inline-calloc -opt-malloc-options=3 -auto -unroll4

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: -xCORE-AVX2 -ipo -O3 -no-prec-div -auto-ilp32 -ansi-alias

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.html
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/HP-Compiler-Flags-Intel-V1.2-HSW-revF.xml
http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Jul 12 11:03:44 2016 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 July 2016.