



# SPEC® CFP2006 Result

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Dell Inc.

**SPECfp®\_rate2006 = 302**

PowerEdge R530 (Intel Xeon E5-1650 v4, 3.60 GHz)

**SPECfp\_rate\_base2006 = 296**

CPU2006 license: 55

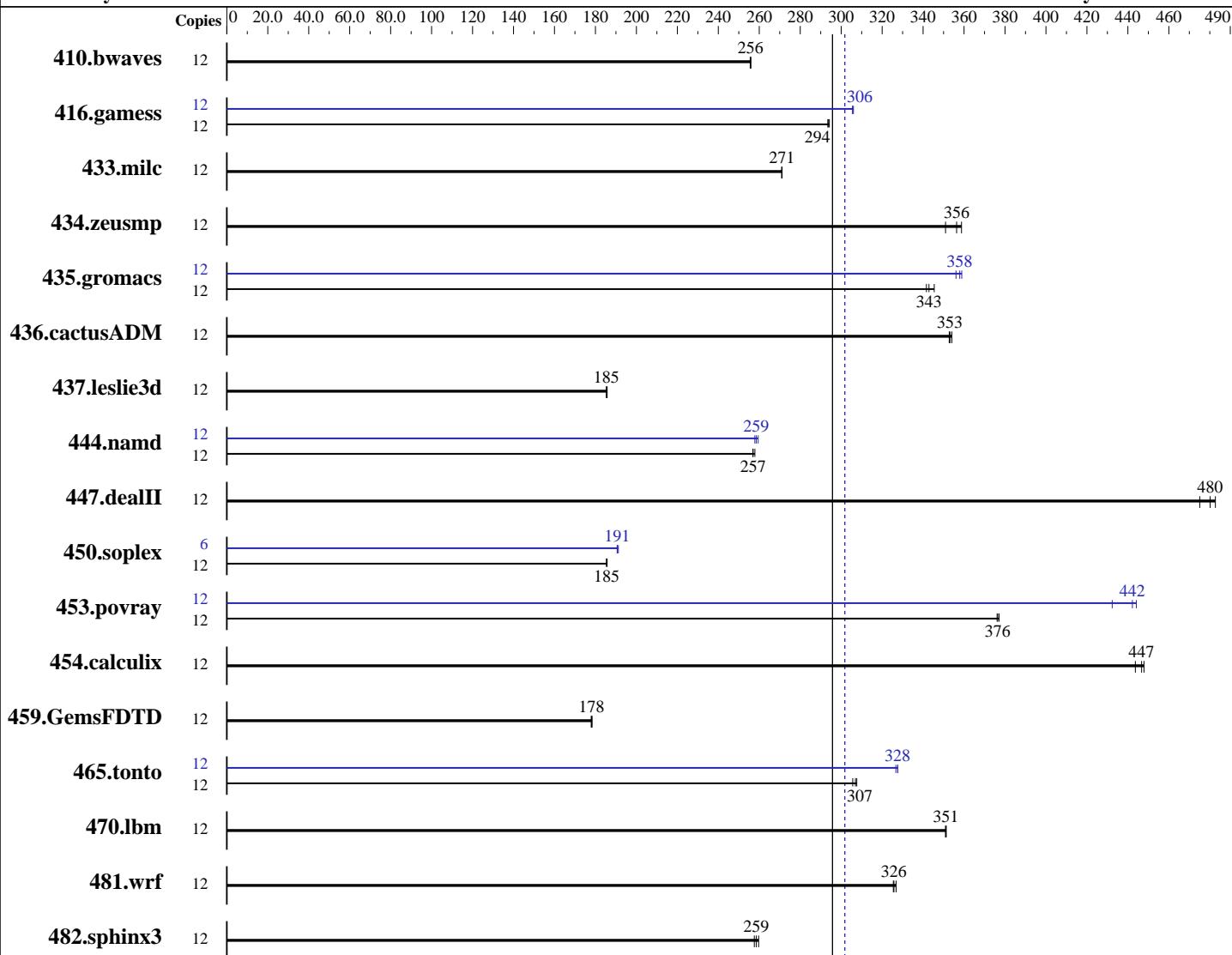
**Test date:** May-2016

Test sponsor: Dell Inc.

**Hardware Availability:** Jun-2016

Tested by: Dell Inc.

**Software Availability:** Dec-2015



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## Hardware

CPU Name: Intel Xeon E5-1650 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 4.00 GHz  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 6 cores, 1 chip, 6 cores/chip, 2 threads/core  
CPU(s) orderable: 1 chip  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 SP1 3.12.49-11-default  
Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;  
Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux  
Auto Parallel: No  
File System: ext4  
System State: Run level 3 (multi-user)

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L3 Cache: 15 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 128 GB (8 x 16 GB 2Rx8 PC4-2400T-R)  
 Disk Subsystem: 1 x 250 GB 7200 RPM SATA HDD  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	12	637	256	638	256	<b>638</b>	<b>256</b>	12	637	256	638	256	<b>638</b>	<b>256</b>
416.gamess	12	<b>800</b>	<b>294</b>	799	294	801	293	12	769	306	768	306	<b>769</b>	<b>306</b>
433.milc	12	407	271	<b>406</b>	<b>271</b>	406	271	12	407	271	<b>406</b>	<b>271</b>	406	271
434.zeusmp	12	311	351	304	359	<b>306</b>	<b>356</b>	12	311	351	304	359	<b>306</b>	<b>356</b>
435.gromacs	12	248	345	251	342	<b>250</b>	<b>343</b>	12	241	356	239	359	<b>239</b>	<b>358</b>
436.cactusADM	12	405	354	407	353	<b>406</b>	<b>353</b>	12	405	354	407	353	<b>406</b>	<b>353</b>
437.leslie3d	12	<b>608</b>	<b>185</b>	607	186	609	185	12	<b>608</b>	<b>185</b>	607	186	609	185
444.namd	12	373	258	375	257	<b>375</b>	<b>257</b>	12	<b>372</b>	<b>259</b>	371	259	373	258
447.dealII	12	284	483	289	475	<b>286</b>	<b>480</b>	12	284	483	289	475	<b>286</b>	<b>480</b>
450.soplex	12	540	185	<b>540</b>	<b>185</b>	539	186	6	262	191	263	191	<b>262</b>	<b>191</b>
453.povray	12	170	376	169	377	<b>170</b>	<b>376</b>	12	148	432	144	444	<b>144</b>	<b>442</b>
454.calculix	12	223	444	<b>222</b>	<b>447</b>	221	448	12	223	444	<b>222</b>	<b>447</b>	221	448
459.GemsFDTD	12	716	178	<b>715</b>	<b>178</b>	714	178	12	716	178	<b>715</b>	<b>178</b>	714	178
465.tonto	12	386	306	384	308	<b>385</b>	<b>307</b>	12	360	328	361	327	<b>360</b>	<b>328</b>
470.lbm	12	<b>470</b>	<b>351</b>	469	351	470	351	12	<b>470</b>	<b>351</b>	469	351	470	351
481.wrf	12	<b>411</b>	<b>326</b>	410	327	412	325	12	<b>411</b>	<b>326</b>	410	327	412	325
482.sphinx3	12	<b>905</b>	<b>259</b>	901	260	908	258	12	<b>905</b>	<b>259</b>	901	260	908	258

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Snoop Mode set to Cluster on Die  
 Virtualization Technology disabled

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## Platform Notes (Continued)

System Profile set to custom

CPU Power Management set to Hardware P States

C States set to Autonomous

C1E disabled

Energy Efficient Turbo disabled

Uncore Frequency set to Dynamic

Energy Efficiency Policy set to Balanced Performance

Memory Patrol Scrub disabled

Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914

\$Rev: 6914 \$ \$Date:: 2014-06-25 #\\$ e3fbb8667b5a285932ceab81e28219e1

running on linux-g0aw Fri May 20 16:54:00 2016

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) CPU E5-1650 v4 @ 3.60GHz
        1 "physical id"s (chips)
        12 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The
    following excerpts from /proc/cpuinfo might not be reliable. Use with
    caution.)
        cpu cores : 6
        siblings   : 12
        physical 0: cores 0 1 2 3 4 5
    cache size : 15360 KB
```

```
From /proc/meminfo
    MemTotal:      132185680 kB
    HugePages_Total:       0
    Hugepagesize:     2048 kB
```

```
/usr/bin/lsb_release -d
  SUSE Linux Enterprise Server 12 SP1
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 1
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP1"
    VERSION_ID="12.1"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
    ID="sles"
    ANSI_COLOR="0;32"
    CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

Continued on next page



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## Platform Notes (Continued)

```
uname -a:  
Linux linux-g0aw 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015  
(8d714a0) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 May 20 08:45
```

```
SPEC is set to: /root/cpu2006-1.2  
Filesystem      Type  Size  Used  Avail Use% Mounted on  
/dev/sda2        ext4  221G  8.6G  212G   4% /  
Additional information from dmidecode:
```

```
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS Dell Inc. 2.0.1 04/11/2016  
Memory:  
7x 00AD063200AD HMA82GR7MFR8N-UH 16 GB 2 rank 2400 MHz  
1x 00CE00B300CE M393A2K43BB1-CRC 16 GB 2 rank 2400 MHz  
4x Not Specified Not Specified
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB  
memory using RedHat EL 7.2 glibc 2.17

Transparent Huge Pages enabled with:

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page



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## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
  447.dealII: -DSPEC_CPU_LP64
  450.soplex: -DSPEC_CPU_LP64
  453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

icc -m64

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## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2016/linux/compiler/lib/ia32\_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)  
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)  
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)  
-prof-use(pass 2) -fno-alias -auto-ilp32

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## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

```
450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -opt-malloc-options=3
```

```
453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
             -prof-use(pass 2) -unroll14 -ansi-alias
```

Fortran benchmarks:

410.bwaves: basepeak = yes

```
416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll12
             -inline-level=0 -scalar-rep-
```

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

```
465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
             -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
             -par-num-threads=1(pass 1) -prof-use(pass 2) -unroll14 -auto
             -inline-calloc -opt-malloc-options=3
```

Benchmarks using both Fortran and C:

```
435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
              -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
              -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
              -prof-use(pass 2) -opt-prefetch -auto-ilp32
```

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Settings-V1.2-revD.20151006.xml>

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