



# SPEC® CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant ML350 Gen9  
(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp®\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

CPU2006 license: 3

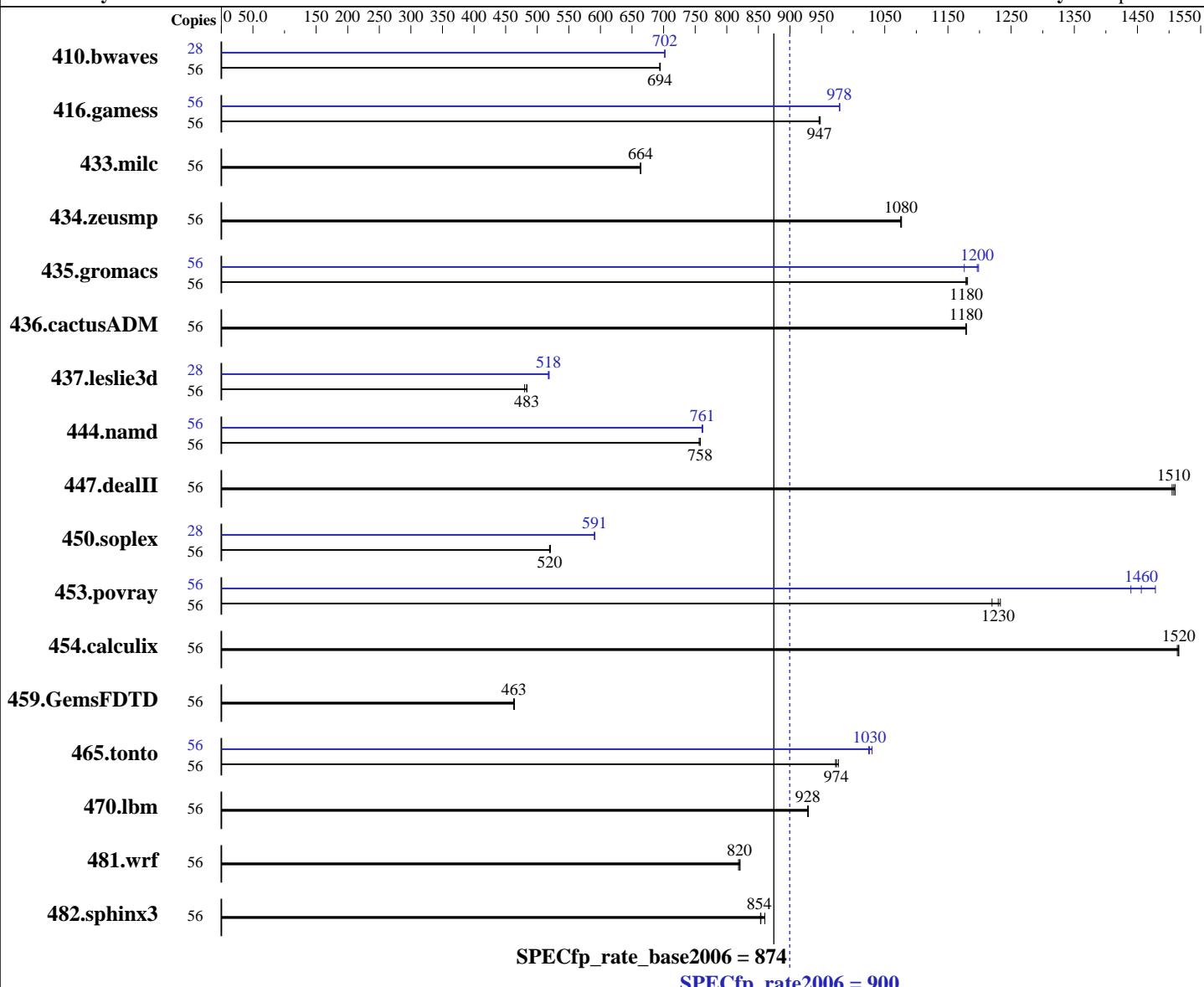
Test sponsor: HPE

Tested by: HPE

Test date: Jan-2017

Hardware Availability: Mar-2016

Software Availability: Sep-2016



## Hardware

CPU Name: Intel Xeon E5-2660 v4  
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core  
CPU(s) orderable: 1, 2 chips  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 256 KB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP1  
Compiler: Kernel 3.12.49-11-default  
C/C++: Version 17.0.0.098 of Intel C/C++ Compiler for Linux;  
Fortran: Version 17.0.0.098 of Intel Fortran Compiler for Linux  
Auto Parallel: No  
File System: xfs  
System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant ML350 Gen9  
(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

**CPU2006 license:** 3

**Test date:** Jan-2017

**Test sponsor:** HPE

**Hardware Availability:** Mar-2016

**Tested by:** HPE

**Software Availability:** Sep-2016

L3 Cache: 35 MB I+D on chip per chip  
Other Cache: None  
Memory: 256 GB (16 x 16 GB 2Rx4 PC4-2400T-R)  
Disk Subsystem: 2 x 450 GB 15 K SAS HDD, RAID 1  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	<b>1097</b>	<b>694</b>	1095	695	1097	694	28	542	702	<b>542</b>	<b>702</b>	542	702
416.gamess	56	1157	948	<b>1158</b>	<b>947</b>	1159	946	56	1120	979	1121	978	<b>1121</b>	<b>978</b>
433.milc	56	<b>775</b>	<b>664</b>	775	663	774	664	56	<b>775</b>	<b>664</b>	775	663	774	664
434.zeusmp	56	<b>474</b>	<b>1080</b>	474	1080	473	1080	56	<b>474</b>	<b>1080</b>	474	1080	473	1080
435.gromacs	56	<b>339</b>	<b>1180</b>	339	1180	339	1180	56	334	1200	340	1180	<b>334</b>	<b>1200</b>
436.cactusADM	56	568	1180	<b>568</b>	<b>1180</b>	568	1180	56	568	1180	<b>568</b>	<b>1180</b>	568	1180
437.leslie3d	56	1097	480	1089	484	<b>1089</b>	<b>483</b>	28	507	519	508	518	<b>508</b>	<b>518</b>
444.namd	56	<b>593</b>	<b>758</b>	592	758	594	756	56	589	762	590	761	<b>590</b>	<b>761</b>
447.dealII	56	424	1510	<b>425</b>	<b>1510</b>	426	1500	56	424	1510	<b>425</b>	<b>1510</b>	426	1500
450.soplex	56	<b>899</b>	<b>520</b>	897	521	899	519	28	395	591	<b>395</b>	<b>591</b>	395	591
453.povray	56	242	1230	244	1220	<b>242</b>	<b>1230</b>	56	<b>205</b>	<b>1460</b>	207	1440	202	1480
454.calculix	56	305	1520	305	1510	<b>305</b>	<b>1520</b>	56	305	1520	305	1510	<b>305</b>	<b>1520</b>
459.GemsFDTD	56	1281	464	1284	463	<b>1282</b>	<b>463</b>	56	1281	464	1284	463	<b>1282</b>	<b>463</b>
465.tonto	56	567	972	<b>566</b>	<b>974</b>	564	977	56	<b>537</b>	<b>1030</b>	535	1030	538	1020
470.lbm	56	828	929	<b>829</b>	<b>928</b>	829	928	56	828	929	<b>829</b>	<b>928</b>	829	928
481.wrf	56	762	821	<b>763</b>	<b>820</b>	764	819	56	762	821	<b>763</b>	<b>820</b>	764	819
482.sphinx3	56	<b>1278</b>	<b>854</b>	1269	860	1279	853	56	<b>1278</b>	<b>854</b>	1269	860	1279	853

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
echo 1 > /proc/sys/vm/drop_caches
runspec command invoked through numactl i.e.:
    numactl --interleave=all runspec <etc>
```



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9

(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jan-2017

**Hardware Availability:** Mar-2016

**Software Availability:** Sep-2016

## Platform Notes

### BIOS Configuration:

```
Power Profile set to Custom
Power Regulator set to Static High Performance Mode
Minimum Processor Idle Power Core C-State set to C6 State
Minimum Processor Idle Power Package C-State set to No Package State
Collaborative Power Control set to Disabled
QPI Snoop Configuration set to Cluster on Die
Thermal Configuration set to Maximum Cooling
Processor Power and Utilization Monitoring set to Disabled
Memory Refresh Rate set to 1x Refresh
Sysinfo program /cpu/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on pl36 Thu Jan 19 22:36:26 2017
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) CPU E5-2660 v4 @ 2.00GHz
        2 "physical id"s (chips)
        56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 14
        siblings : 28
        physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
        physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB
```

From /proc/meminfo

```
MemTotal:      264549708 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 1
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
VERSION="12-SP1"
VERSION_ID="12.1"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP1"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp1"
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9

(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jan-2017

Hardware Availability: Mar-2016

Software Availability: Sep-2016

## Platform Notes (Continued)

```
uname -a:  
Linux pl36 3.12.49-11-default #1 SMP Wed Nov 11 20:52:43 UTC 2015 (8d714a0)  
x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jan 19 09:40
```

```
SPEC is set to: /cpu  
Filesystem      Type  Size  Used  Avail Use% Mounted on  
/dev/sda2        xfs   419G   56G  364G  14% /  
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS HP P92 02/22/2016
```

Memory:

```
16x HP 809081-081 16 GB 2 rank 2400 MHz  
8x UNKNOWN NOT AVAILABLE
```

(End of data from sysinfo program)

Regarding the sysinfo display about the memory installed, the correct amount of memory is 256 GB and the dmidecode description should have one line reading as:  
16x HP 809081-081 16 GB 2 rank 2400 MHz

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu/libs/32:/cpu/libs/64:/cpu/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant ML350 Gen9  
(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jan-2017

**Hardware Availability:** Mar-2016

**Software Availability:** Sep-2016

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks (except as noted below):

```
icpc -m64
```

```
450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant ML350 Gen9  
(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Jan-2017

Hardware Availability: Mar-2016

Software Availability: Sep-2016

## Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
450.soplex: -D_FILE_OFFSET_BITS=64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

```

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
    -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
    -no-prec-div(pass 2) -fno-alias -auto-ilp32
    -qopt-mem-layout-trans=3

```

447.dealII: basepeak = yes

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9

(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jan-2017

**Hardware Availability:** Mar-2016

**Software Availability:** Sep-2016

## Peak Optimization Flags (Continued)

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2) -qopt-malloc-options=3  
                   -qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: basepeak = yes

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
                   -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
                   -no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
                   -qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
                   -par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
                   -qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.html>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64.xml>

<http://www.spec.org/cpu2006/flags/HP-Platform-Flags-Intel-V1.2-HSW-revE.xml>



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant ML350 Gen9

(2.00 GHz, Intel Xeon E5-2660 v4)

**SPECfp\_rate2006 = 900**

**SPECfp\_rate\_base2006 = 874**

**CPU2006 license:** 3

**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Jan-2017

**Hardware Availability:** Mar-2016

**Software Availability:** Sep-2016

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Feb 21 16:14:22 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 21 February 2017.