



# SPEC® CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp<sup>®</sup>\_rate2006 = 1170**

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate\_base2006 = 1140**

CPU2006 license: 55

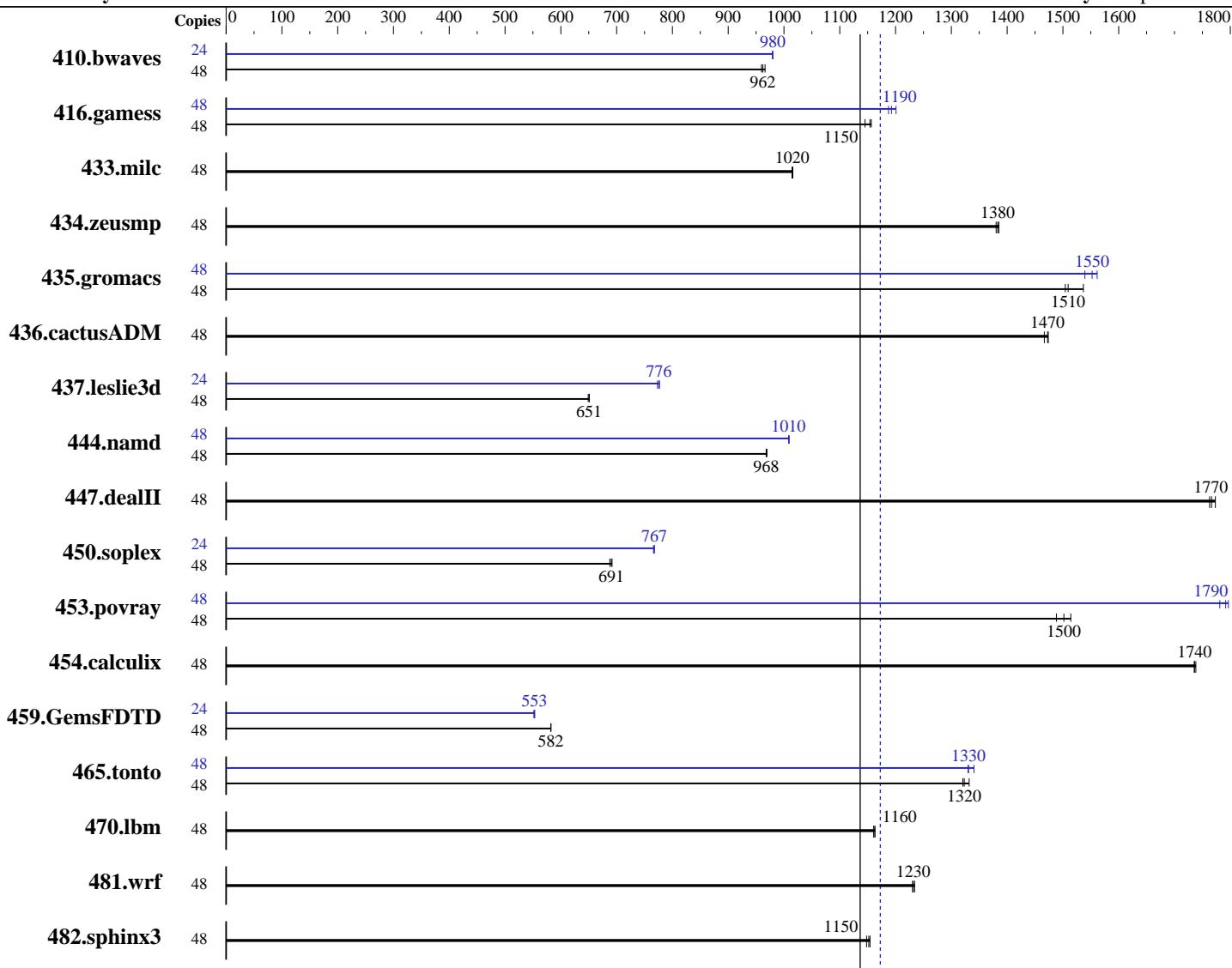
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Aug-2017

Hardware Availability: Sep-2017

Software Availability: Apr-2017



**SPECfp\_rate\_base2006 = 1140**

**SPECfp\_rate2006 = 1170**

## Hardware

CPU Name: Intel Xeon Gold 6136  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 SP2 4.4.16-56-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)

Continued on next page

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1170**

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate\_base2006 = 1140**

CPU2006 license: 55

Test date: Aug-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

L3 Cache: 24.75 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)  
 Disk Subsystem: 1 x 960 GB SATA SSD  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	675	966	680	960	<b>678</b>	<b>962</b>	24	333	980	<b>333</b>	<b>980</b>	333	979
416.gamess	48	813	1160	<b>814</b>	<b>1150</b>	821	1150	48	<b>788</b>	<b>1190</b>	783	1200	792	1190
433.milc	48	434	1010	434	1020	<b>434</b>	<b>1020</b>	48	434	1010	434	1020	<b>434</b>	<b>1020</b>
434.zeusmp	48	316	1380	<b>316</b>	<b>1380</b>	315	1390	48	316	1380	<b>316</b>	<b>1380</b>	315	1390
435.gromacs	48	228	1500	223	1540	<b>227</b>	<b>1510</b>	48	219	1560	<b>221</b>	<b>1550</b>	223	1540
436.cactusADM	48	391	1470	<b>390</b>	<b>1470</b>	389	1470	48	391	1470	<b>390</b>	<b>1470</b>	389	1470
437.leslie3d	48	<b>693</b>	<b>651</b>	693	651	695	649	24	290	777	292	773	<b>291</b>	<b>776</b>
444.namd	48	398	968	397	969	<b>398</b>	<b>968</b>	48	<b>382</b>	<b>1010</b>	381	1010	382	1010
447.dealII	48	<b>311</b>	<b>1770</b>	310	1770	311	1760	48	<b>311</b>	<b>1770</b>	310	1770	311	1760
450.soplex	48	582	688	<b>580</b>	<b>691</b>	579	692	24	<b>261</b>	<b>767</b>	261	768	261	766
453.povray	48	169	1510	<b>170</b>	<b>1500</b>	172	1490	48	142	1800	<b>143</b>	<b>1790</b>	143	1780
454.calculix	48	<b>228</b>	<b>1740</b>	228	1740	228	1740	48	<b>228</b>	<b>1740</b>	228	1740	228	1740
459.GemsFDTD	48	<b>875</b>	<b>582</b>	875	582	875	582	24	460	553	<b>461</b>	<b>553</b>	462	552
465.tonto	48	<b>357</b>	<b>1320</b>	358	1320	355	1330	48	<b>355</b>	<b>1330</b>	355	1330	352	1340
470.lbm	48	568	1160	567	1160	<b>568</b>	<b>1160</b>	48	568	1160	567	1160	<b>568</b>	<b>1160</b>
481.wrf	48	<b>435</b>	<b>1230</b>	436	1230	434	1230	48	<b>435</b>	<b>1230</b>	436	1230	434	1230
482.sphinx3	48	815	1150	811	1150	<b>812</b>	<b>1150</b>	48	815	1150	811	1150	<b>812</b>	<b>1150</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Sub NUMA Cluster enabled  
 Virtualization Technology disabled

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1170**

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate\_base2006 = 1140**

**CPU2006 license:** 55

**Test date:** Aug-2017

**Test sponsor:** Dell Inc.

**Hardware Availability:** Sep-2017

**Tested by:** Dell Inc.

**Software Availability:** Apr-2017

## Platform Notes (Continued)

```
System Profile set to Custom
CPU Performance set to Maximum Performance
C States set to autonomous
C1E disabled
Uncore Frequency set to Dynamic
Energy Efficiency Policy set to Performance
Memory Patrol Scrub disabled
Logical Processor enabled
CPU Interconnect Bus Link Power Management disabled
PCI ASPM L1 Link Power Management disabled
Sysinfo program /root/cpu2006-1.2_icl7u3/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-u8yg Wed Aug 30 14:06:58 2017
```

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6136 CPU @ 3.00GHz
        2 "physical id"s (chips)
        48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
        cpu cores : 12
        siblings : 24
        physical 0: cores 0 1 2 3 8 9 10 11 18 19 24 27
        physical 1: cores 0 1 2 3 4 9 10 16 18 19 25 26
cache size : 25344 KB
```

```
From /proc/meminfo
MemTotal:      196687100 kB
HugePages_Total:       0
Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
        SUSE Linux Enterprise Server 12 (x86_64)
        VERSION = 12
        PATCHLEVEL = 2
        # This file is deprecated and will be removed in a future service pack or
        release.
        # Please check /etc/os-release for details about this release.
os-release:
        NAME="SLES"
        VERSION="12-SP2"
        VERSION_ID="12.2"
        PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
        ID="sles"
        ANSI_COLOR="0;32"
        CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1170**

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate\_base2006 = 1140**

CPU2006 license: 55

Test date: Aug-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

## Platform Notes (Continued)

```
uname -a:  
Linux linux-u8yg 4.4.16-56-default #1 SMP Mon Aug 8 14:24:26 UTC 2016  
(5b281a8) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Aug 30 00:23
```

```
SPEC is set to: /root/cpu2006-1.2_ic17u3  
Filesystem      Type  Size  Used Avail Use% Mounted on  
/dev/sdal      btrfs  921G   17G  898G   2% /  
Additional information from dmidecode:
```

```
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS Dell Inc. 1.0.0 08/10/2017  
Memory:  
12x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz  
4x Not Specified Not Specified
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2_ic17u3/lib/ia32:/root/cpu2006-1.2_ic17u3/lib/intel64:/root/cpu2006-1.2_ic17u3/sh10.2"
```

```
Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run  
runspec command invoked through numactl i.e.:  
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:  
icc -m64

C++ benchmarks:  
icpc -m64

Fortran benchmarks:  
ifort -m64

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1170**

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate\_base2006 = 1140**

CPU2006 license: 55

Test date: Aug-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
  434.zeusmp: -DSPEC_CPU_LP64
  435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
  437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
    447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
  454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

## Peak Compiler Invocation

C benchmarks:

```
icc -m64
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate2006 = 1170**

CPU2006 license: 55

Test date: Aug-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

## Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
416.gamess: -DSPEC\_CPU\_LP64  
433.milc: -DSPEC\_CPU\_LP64  
434.zeusmp: -DSPEC\_CPU\_LP64  
435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
437.leslie3d: -DSPEC\_CPU\_LP64  
444.namd: -DSPEC\_CPU\_LP64  
447.dealII: -DSPEC\_CPU\_LP64  
450.soplex: -D\_FILE\_OFFSET\_BITS=64  
453.povray: -DSPEC\_CPU\_LP64  
454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
459.GemsFDTD: -DSPEC\_CPU\_LP64  
465.tonto: -DSPEC\_CPU\_LP64  
470.lbm: -DSPEC\_CPU\_LP64  
481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1170**

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate\_base2006 = 1140**

CPU2006 license: 55

Test date: Aug-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Apr-2017

## Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

**SPECfp\_rate2006 = 1170**

PowerEdge FC640 (Intel Xeon Gold 6136, 3.00 GHz)

**SPECfp\_rate\_base2006 = 1140**

**CPU2006 license:** 55

**Test date:** Aug-2017

**Test sponsor:** Dell Inc.

**Hardware Availability:** Sep-2017

**Tested by:** Dell Inc.

**Software Availability:** Apr-2017

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Sep 20 11:02:31 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 19 September 2017.