SPEC® CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

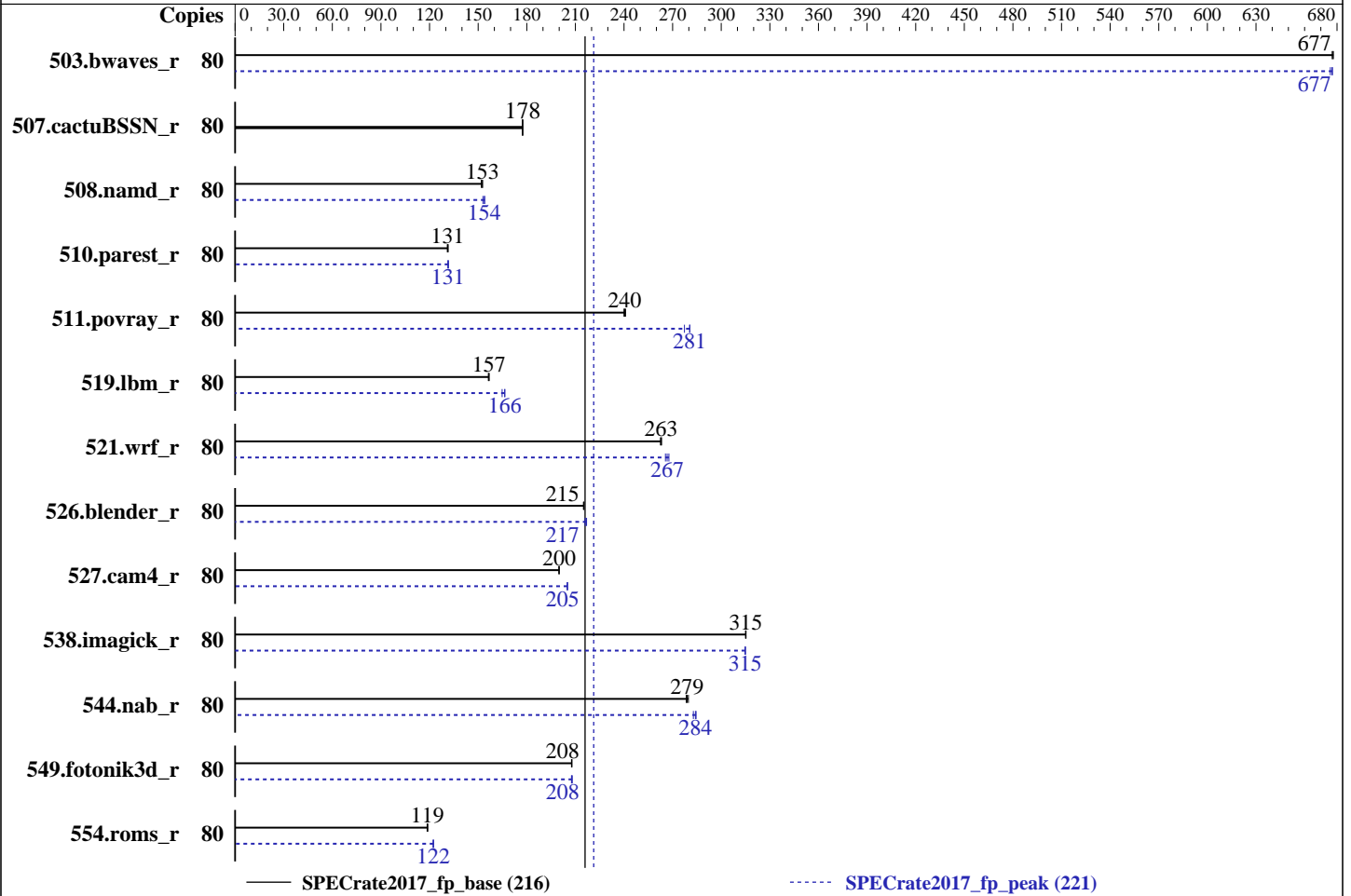**SPECrate2017_fp_peak = 221**

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Dec-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2017

Benchmark	Copies	base	peak
503.bwaves_r	80	677	677
507.cactuBSSN_r	80	178	
508.namd_r	80	153	154
510.parest_r	80	131	131
511.povray_r	80	240	281
519.lbm_r	80	157	166
521.wrf_r	80	263	267
526.blender_r	80	215	217
527.cam4_r	80	200	205
538.imagick_r	80	315	315
544.nab_r	80	279	284
549.fotonik3d_r	80	208	208
554.roms_r	80	119	122

SPECrate2017_fp_base (216)   SPECrate2017_fp_peak (221)

Hardware

CPU Name: Intel Xeon Gold 5115
 Max MHz.: 3200
 Nominal: 2400
Enabled: 40 cores, 4 chips, 2 threads/core
Orderable: 1,2,3,4 chip
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 13.75 MB I+D on chip per chip
 Other: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R, running at 2400)
Storage: 1 x 900 GB SAS, 10000 RPM
Other: None

Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 3.10.0-514.el7.x86_64
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;
 Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux
Parallel: No
Firmware: Version 0.65 released Oct-2017
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: None

SPEC CPU2017 Floating Point Rate Result
Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Dec-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2017

Results Table

Benchmark	Copies	Base Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Peak Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	80	1184	678	1185	677	**1185**	**677**	80	**1185**	**677**	1187	676	1185	677
507.cactuBSSN_r	80	**571**	**178**	571	177	570	178	80	**571**	**178**	571	177	570	178
508.namd_r	80	**498**	**153**	500	152	498	153	80	**494**	**154**	497	153	494	154
510.parest_r	80	1591	132	**1595**	**131**	1598	131	80	**1592**	**131**	1590	132	1593	131
511.povray_r	80	775	241	**777**	**240**	778	240	80	**666**	**281**	666	281	674	277
519.lbm_r	80	540	156	538	157	**538**	**157**	80	**507**	**166**	512	165	507	166
521.wrf_r	80	681	263	**682**	**263**	683	262	80	**672**	**267**	675	266	669	268
526.blender_r	80	**566**	**215**	566	215	567	215	80	562	217	**562**	**217**	564	216
527.cam4_r	80	**700**	**200**	700	200	700	200	80	682	205	**682**	**205**	682	205
538.imagick_r	80	632	315	**631**	**315**	631	315	80	**632**	**315**	632	315	632	315
544.nab_r	80	484	278	481	280	**483**	**279**	80	**474**	**284**	476	283	473	284
549.fotonik3d_r	80	1500	208	**1500**	**208**	1502	208	80	1499	208	**1500**	**208**	1502	208
554.roms_r	80	1073	118	1069	119	**1071**	**119**	80	1038	122	1040	122	**1039**	**122**

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

General Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/spec/lib/ia32:/spec/lib/intel64:/spec/je5.0.1-32:/spec/je5.0.1-64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.4
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3>   /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Dec-2017
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Platform Notes

```
BIOS configuration:
Power Policy Set to Performance
Sysinfo program /spec/bin/sysinfo
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f
running on localhost.localdomain Fri Dec  8 21:00:04 2017

SUT (System Under Test) info as seen by some common utilities.
For more information on this section, see
   https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
      4  "physical id"s (chips)
      80 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
      cpu cores : 10
      siblings  : 20
      physical 0: cores 0 1 2 3 4 8 9 10 11 12
      physical 1: cores 0 1 2 3 4 8 9 10 11 12
      physical 2: cores 0 1 2 3 4 8 9 10 11 12
      physical 3: cores 0 1 2 3 4 8 9 10 11 12

From lscpu:
     Architecture:          x86_64
     CPU op-mode(s):        32-bit, 64-bit
     Byte Order:            Little Endian
     CPU(s):                80
     On-line CPU(s) list:   0-79
     Thread(s) per core:    2
     Core(s) per socket:    10
     Socket(s):             4
     NUMA node(s):          4
     Vendor ID:             GenuineIntel
     CPU family:            6
     Model:                 85
     Model name:            Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz
     Stepping:              4
     CPU MHz:               2400.000
     BogoMIPS:              4806.73
     Virtualization:        VT-x
     L1d cache:             32K
     L1i cache:             32K
     L2 cache:              1024K
     L3 cache:              14080K
     NUMA node0 CPU(s):     0-9,40-49
     NUMA node1 CPU(s):     10-19,50-59
```

(Continued on next page)

Standard Performance Evaluation Corporation (info@spec.org)   https://www.spec.org/

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base =   216

SPECrate2017_fp_peak =   221

CPU2017 License: 3175　　　　　　　　　　　　　　　　　**Test Date:** Dec-2017
Test Sponsor: Huawei　　　　　　　　　　　　　　　　**Hardware Availability:** Jul-2017
Tested by: Huawei　　　　　　　　　　　　　　　　　**Software Availability:** Sep-2017

Platform Notes (Continued)

```
      NUMA node2 CPU(s):      20-29,60-69
      NUMA node3 CPU(s):      30-39,70-79

 /proc/cpuinfo cache data
   cache size : 14080 KB

 From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
 physical chip.
   available: 4 nodes (0-3)
   node 0 cpus: 0 1 2 3 4 5 6 7 8 9 40 41 42 43 44 45 46 47 48 49
   node 0 size: 391577 MB
   node 0 free: 382355 MB
   node 1 cpus: 10 11 12 13 14 15 16 17 18 19 50 51 52 53 54 55 56 57 58 59
   node 1 size: 393216 MB
   node 1 free: 384303 MB
   node 2 cpus: 20 21 22 23 24 25 26 27 28 29 60 61 62 63 64 65 66 67 68 69
   node 2 size: 393216 MB
   node 2 free: 384225 MB
   node 3 cpus: 30 31 32 33 34 35 36 37 38 39 70 71 72 73 74 75 76 77 78 79
   node 3 size: 393216 MB
   node 3 free: 384325 MB
   node distances:
   node   0   1   2   3
     0:  10  21  31  21
     1:  21  10  21  31
     2:  31  21  10  21
     3:  21  31  21  10

 From /proc/meminfo
    MemTotal:       1583435264 kB
    HugePages_Total:         0
    Hugepagesize:         2048 kB

 From /etc/*release* /etc/*version*
    os-release:
       NAME="Red Hat Enterprise Linux Server"
       VERSION="7.3 (Maipo)"
       ID="rhel"
       ID_LIKE="fedora"
       VERSION_ID="7.3"
       PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
       ANSI_COLOR="0;31"
       CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
    redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
    system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
    system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

(Continued on next page)

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

CPU2017 License: 3175

Test Sponsor: Huawei

Tested by: Huawei

Test Date: Dec-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2017

Platform Notes (Continued)

```
uname -a:
   Linux localhost.localdomain 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 8 08:40

SPEC is set to: /spec
   Filesystem     Type  Size  Used Avail Use% Mounted on
   /dev/sda4      xfs   723G  302G  422G  42% /

Additional information from dmidecode follows.  WARNING: Use caution when you interpret
this section. The 'dmidecode' program reads system data which is "intended to allow
hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  BIOS INSYDE Corp. 0.65 10/27/2017
  Memory:
   36x Hynix HMA84GR7AFR4N-VK 32 GB 2 rank 2666, configured at 2400
   12x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666, configured at 2400

(End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
CC  519.lbm_r(base) 538.imagick_r(base, peak) 544.nab_r(base)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
CC  519.lbm_r(peak) 544.nab_r(peak)
------------------------------------------------------------------------------
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
CXXC 508.namd_r(base) 510.parest_r(base)
------------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
------------------------------------------------------------------------------


==============================================================================
CXXC 508.namd_r(peak) 510.parest_r(peak)
```

(Continued on next page)

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Dec-2017
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
 CC  511.povray_r(base) 526.blender_r(base)
--------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
CC   511.povray_r(peak) 526.blender_r(peak)
--------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
 FC  507.cactuBSSN_r(base)
--------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
FC   507.cactuBSSN_r(peak)
--------------------------------------------------------------------------
icpc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
 FC  503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base)
```

(Continued on next page)

SPEC CPU2017 Floating Point Rate Result

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Dec-2017
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Compiler Version Notes (Continued)

```
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
FC   554.roms_r(peak)
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
 CC  521.wrf_r(base) 527.cam4_r(base)
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------


==========================================================================
CC   521.wrf_r(peak) 527.cam4_r(peak)
--------------------------------------------------------------------------
ifort (IFORT) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
icc (ICC) 18.0.0 20170811
Copyright (C) 1985-2017 Intel Corporation.  All rights reserved.
--------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`ifort icc`

Benchmarks using both C and C++:
`icpc icc`

<div align="center">(Continued on next page)</div>

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Dec-2017
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
`icpc icc ifort`

Base Portability Flags

503.bwaves_r: `-DSPEC_LP64`
507.cactuBSSN_r: `-DSPEC_LP64`
508.namd_r: `-DSPEC_LP64`
510.parest_r: `-DSPEC_LP64`
511.povray_r: `-DSPEC_LP64`
519.lbm_r: `-DSPEC_LP64`
521.wrf_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian`
526.blender_r: `-DSPEC_LP64 -DSPEC_LINUX -funsigned-char`
527.cam4_r: `-DSPEC_LP64 -DSPEC_CASE_FLAG`
538.imagick_r: `-DSPEC_LP64`
544.nab_r: `-DSPEC_LP64`
549.fotonik3d_r: `-DSPEC_LP64`
554.roms_r: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
`-qopt-mem-layout-trans=3`

C++ benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
`-qopt-mem-layout-trans=3`

Fortran benchmarks:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

Benchmarks using both Fortran and C:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
`-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte`

Benchmarks using both C and C++:
`-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only`
`-qopt-mem-layout-trans=3`

(Continued on next page)

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base =   216

SPECrate2017_fp_peak =   221

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Dec-2017
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:
```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
```

Base Other Flags

C benchmarks:
```
-m64 -std=c11
```

C++ benchmarks:
```
-m64
```

Fortran benchmarks:
```
-m64
```

Benchmarks using both Fortran and C:
```
-m64 -std=c11
```

Benchmarks using both C and C++:
```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
-m64 -std=c11
```

Peak Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
ifort icc
```

Benchmarks using both C and C++:
```
icpc icc
```

(Continued on next page)

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base =   216

SPECrate2017_fp_peak =   221

CPU2017 License: 3175
Test Sponsor: Huawei
Tested by: Huawei

Test Date: Dec-2017
Hardware Availability: Jul-2017
Software Availability: Sep-2017

Peak Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:
```
icpc icc ifort
```

Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

519.lbm_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

538.imagick_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3

544.nab_r: Same as 519.lbm_r

C++ benchmarks:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3

Fortran benchmarks:

503.bwaves_r: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3
-nostandard-realloc-lhs -align array32byte

549.fotonik3d_r: Same as 503.bwaves_r

554.roms_r: -prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs
-align array32byte

Benchmarks using both Fortran and C:
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3

(Continued on next page)

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base = 216

SPECrate2017_fp_peak = 221

Peak Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):
```
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -nostandard-realloc-lhs -align array32byte
```

Benchmarks using both C and C++:
```
-prof-gen(pass 1) -prof-use(pass 2) -ipo -xCORE-AVX2 -O3
-no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3
```

Benchmarks using Fortran, C, and C++:

```
 507.cactuBSSN_r: basepeak = yes
```

Peak Other Flags

C benchmarks:
```
-m64 -std=c11
```

C++ benchmarks:
```
-m64
```

Fortran benchmarks:
```
-m64
```

Benchmarks using both Fortran and C:
```
-m64 -std=c11
```

Benchmarks using both C and C++:
```
-m64 -std=c11
```

Benchmarks using Fortran, C, and C++:
```
-m64 -std=c11
```

The flags files that were used to format this result can be browsed at
```
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.html
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.html
```

You can also download the XML flags sources by saving the following links:
```
http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.xml
http://www.spec.org/cpu2017/flags/Huawei-Platform-Settings-SKL-V1.7.xml
```

SPEC CPU2017 Floating Point Rate Result

Copyright 2017-2018 Standard Performance Evaluation Corporation

Huawei

Huawei CH242 V5 (Intel Xeon Gold 5115)

SPECrate2017_fp_base =   216

SPECrate2017_fp_peak =   221

CPU2017 License: 3175	**Test Date:** Dec-2017
Test Sponsor: Huawei	**Hardware Availability:** Jul-2017
Tested by: Huawei	**Software Availability:** Sep-2017

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.2 on 2017-12-08 21:00:04-0500.
Report generated on 2018-10-31 13:55:05 by CPU2017 PDF formatter v6067.
Originally published on 2017-12-26.