



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SD650 V3
(3.70 GHz, Intel Xeon Gold 6434)

SPECspeed®2017_fp_base = 169

SPECspeed®2017_fp_peak = Not Run

CPU2017 License: 9017

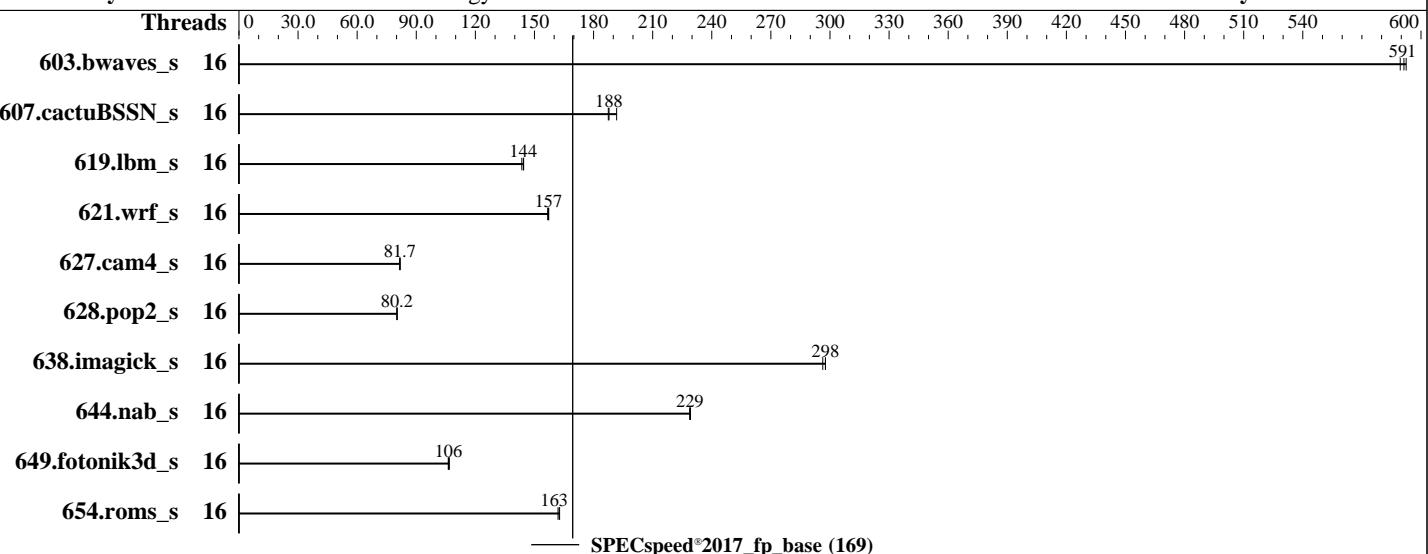
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Feb-2023

Hardware Availability: Feb-2023

Software Availability: Jun-2022



Hardware

CPU Name: Intel Xeon Gold 6434
Max MHz: 4100
Nominal: 3700
Enabled: 16 cores, 2 chips
Orderable: 1,2 chips
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 22.5 MB I+D on chip per chip
Other: None
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-4800B-R)
Storage: 1 x 960 GB SATA SSD
Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4 (x86_64)
Compiler: Kernel 5.14.21-150400.22-default
C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;
Parallel: Yes
Firmware: Lenovo BIOS Version USE109N 1.12 released Jan-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	
603.bwaves_s	16	99.8	591	100	589	99.6	592								
607.cactuBSSN_s	16	88.7	188	88.9	187	87.0	192								
619.lbm_s	16	36.2	145	36.5	144	36.3	144								
621.wrf_s	16	84.1	157	84.2	157	84.4	157								
627.cam4_s	16	108	81.8	108	81.7	109	81.6								
628.pop2_s	16	148	80.2	148	80.2	148	80.3								
638.imagick_s	16	48.5	298	48.5	298	48.7	296								
644.nab_s	16	76.4	229	76.3	229	76.2	229								
649.fotonik3d_s	16	85.7	106	85.8	106	85.3	107								
654.roms_s	16	96.8	163	97.2	162	96.7	163								
SPECspeed®2017_fp_base =		169													
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH =
    "/home/cpu2017-1.1.9-ic2022.1/lib/intel64:/home/cpu2017-1.1.9-ic2022.1/j
    e5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

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General Notes (Continued)

jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS configuration:

Choose Operating Mode set to Custom Mode

Hyper-Threading set to Disabled

CPU P-state Control set to Legacy

Sysinfo program /home/cpu2017-1.1.9-ic2022.1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost Mon Feb 6 01:48:03 2023

SUT (System Under Test) info as seen by some common utilities.

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 10. who -r
 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
 12. Services, from systemctl list-unit-files
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Platform Notes (Continued)

1. uname -a

```
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

2. w

```
01:48:03 up 1 min, 1 user, load average: 0.14, 0.07, 0.02
USER      TTY      FROM          LOGIN@    IDLE    JCPU    PCPU WHAT
root      tty1     -           01:47    18.00s  1.00s  0.00s -bash
```

3. Username

```
From environment variable $USER: root
```

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 2062725
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 2062725
virtual memory           (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags -c
ic2022.1-lin-core-avx512-speed-20220316.cfg --define cores=16 --tune base -o all --define drop_caches
fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2022.1-lin-core-avx512-speed-20220316.cfg --define cores=16 --tune base --output_format all --define
drop_caches --nopower --runmode speed --tune base --size refspeed fpspeed --nopreenv --note-preenv
--logfile $SPEC/tmp/CPU2017.022/templogs/preenv.fpspeed.022.0.log --lognum 022.0 --from_runcpu 2
```

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Platform Notes (Continued)

```
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2022.1
```

```
-----  
6. /proc/cpuinfo  
    model name      : Intel(R) Xeon(R) Gold 6434  
    vendor_id       : GenuineIntel  
    cpu family     : 6  
    model          : 143  
    stepping       : 8  
    microcode      : 0x2b000161  
    bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs  
    cpu cores      : 8  
    siblings        : 8  
    2 physical ids (chips)  
    16 processors (hardware threads)  
    physical id 0: core ids 0-7  
    physical id 1: core ids 0-7  
    physical id 0: apicids 0,2,4,6,8,10,12,14  
    physical id 1: apicids 128,130,132,134,136,138,140,142
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

```
-----  
7. lscpu
```

From lscpu from util-linux 2.37.2:

```
Architecture:                  x86_64  
CPU op-mode(s):               32-bit, 64-bit  
Address sizes:                46 bits physical, 57 bits virtual  
Byte Order:                   Little Endian  
CPU(s):                      16  
On-line CPU(s) list:          0-15  
Vendor ID:                    GenuineIntel  
Model name:                   Intel(R) Xeon(R) Gold 6434  
CPU family:                  6  
Model:                        143  
Thread(s) per core:           1  
Core(s) per socket:           8  
Socket(s):                   2  
Stepping:                     8  
Frequency boost:              enabled  
CPU max MHz:                 3701.0000  
CPU min MHz:                 800.0000  
BogoMIPS:                     7400.00  
Flags:                         fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36  
                                clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
```

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Platform Notes (Continued)

```
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
nonstop_tsc cpuid aperfmpf perf tsc_known_freq pni pclmulqdq dtes64 monitor
    ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xptr pdcm pcid dca sse4_1
    sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rrand
    lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
    invpcid_single intel_ppin cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced
    tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle
    avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
    avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
    xsaveopt xsavenc xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
    cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
    arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
    vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpocndq la57 rdpid
    bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
    tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_lll arch_capabilities
```

Virtualization:

VT-x

L1d cache: 768 KiB (16 instances)

L1i cache: 512 KiB (16 instances)

L2 cache: 32 MiB (16 instances)

L3 cache: 45 MiB (2 instances)

NUMA node(s): 2

NUMA node0 CPU(s): 0-7

NUMA node1 CPU(s): 8-15

Vulnerability Itlb multihit: Not affected

Vulnerability L1tf: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Spec store bypass: Mitigation: Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1: Mitigation: usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2: Mitigation: Enhanced IBRS, IBPB conditional, RSB filling

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	22.5M	45M	15	Unified	3	24576	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 2 nodes (0-1)

node 0 cpus: 0-7

node 0 size: 257674 MB

node 0 free: 256900 MB

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Platform Notes (Continued)

```
node 1 cpus: 8-15
node 1 size: 258030 MB
node 1 free: 257250 MB
node distances:
node   0   1
 0: 10 21
 1: 21 10
```

```
-----9. /proc/meminfo
      MemTotal:      528082256 kB
```

```
-----10. who -r
      run-level 3 Feb 6 01:46
```

```
-----11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
      Default Target  Status
      multi-user     running
```

```
-----12. Services, from systemctl list-unit-files
      STATE          UNIT FILES
      enabled        YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron display-manager getty@ haveged
                      irqbalance issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels
                      rollback rsyslog smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6
                      wickedd-nanny
      enabled-runtime systemd-remount-fs
      disabled       autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                      chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                      firewalld gpm grub2-once haveged-switch-root ipmi ipmievfd issue-add-ssh-keys kexec-load
                      lunmask man-db-create multipathd nfs nfs-blkmap rdisc rpcbind rpmconfigcheck rsyncd
                      serial-getty@ smartd_generate_opts snmpd snmptrapd systemd-boot-check-no-failures
                      systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd tuned
      indirect       wickedd
```

```
-----13. Linux kernel boot-time arguments, from /proc/cmdline
      BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
      root=UUID=5f8bfd2f-a83f-41d5-8917-33e974e2f5bd
      splash=silent
      mitigations=auto
      quiet
      security=apparmor
```

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Platform Notes (Continued)

14. cpupower frequency-info

analyzing CPU 0:

current policy: frequency should be within 800 MHz and 3.70 GHz.

The governor "ondemand" may decide which speed to use
within this range.

boost state support:

Supported: yes

Active: yes

15. tuned-adm active

It seems that tuned daemon is not running, preset profile is not activated.

Preset profile: powersave

16. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60
vm.watermark_boost_factor	15000
vm.watermark_scale_factor	10
vm.zone_reclaim_mode	0

17. /sys/kernel/mm/transparent_hugepage

defrag	always defer defer+madvise [madvise] never
enabled	[always] madvise never
hpage_pmd_size	2097152
shmem_enabled	always within_size advise [never] deny force

18. /sys/kernel/mm/transparent_hugepage/khugepaged

alloc_sleep_millisecs	60000
defrag	1

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Platform Notes (Continued)

max_ptes_none	511
max_ptes_shared	256
max_ptes_swap	64
pages_to_scan	4096
scan_sleep_millisecs	10000

19. OS release

```
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4
```

20. Disk information

SPEC is set to: /home/cpu2017-1.1.9-ic2022.1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sda3	xfs	889G	35G	854G	4%	/

21. /sys/devices/virtual/dmi/id

Vendor:	Lenovo
Product:	ThinkSystem SD650 V3
Product Family:	ThinkSystem
Serial:	9999999999

22. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

```
16x Samsung M321R4GA3BB0-CQKVG 32 GB 2 rank 4800
```

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor:	Lenovo
BIOS Version:	USE109N-1.12
BIOS Date:	01/09/2023
BIOS Revision:	1.12
Firmware Revision:	0.90



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Compiler Version Notes

=====
C | 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
C++, C, Fortran | 607.cactuBSSN_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran | 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====
Fortran, C | 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version
2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2022.1.0 Build 20220316
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:
icx

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -festo
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math
-festo -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -festo
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

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Lenovo Global Technology ThinkSystem SD650 V3 (3.70 GHz, Intel Xeon Gold 6434)	SPECspeed®2017_fp_base = 169 SPECspeed®2017_fp_peak = Not Run
CPU2017 License: 9017 Test Sponsor: Lenovo Global Technology Tested by: Lenovo Global Technology	Test Date: Feb-2023 Hardware Availability: Feb-2023 Software Availability: Jun-2022

Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -fno-mfpmpath-sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at [this location](#).

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-N.html>
http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-N.xml>
http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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