



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

**SPECrate®2017\_int\_base = 871**

**SPECrate®2017\_int\_peak = 900**

CPU2017 License: 3

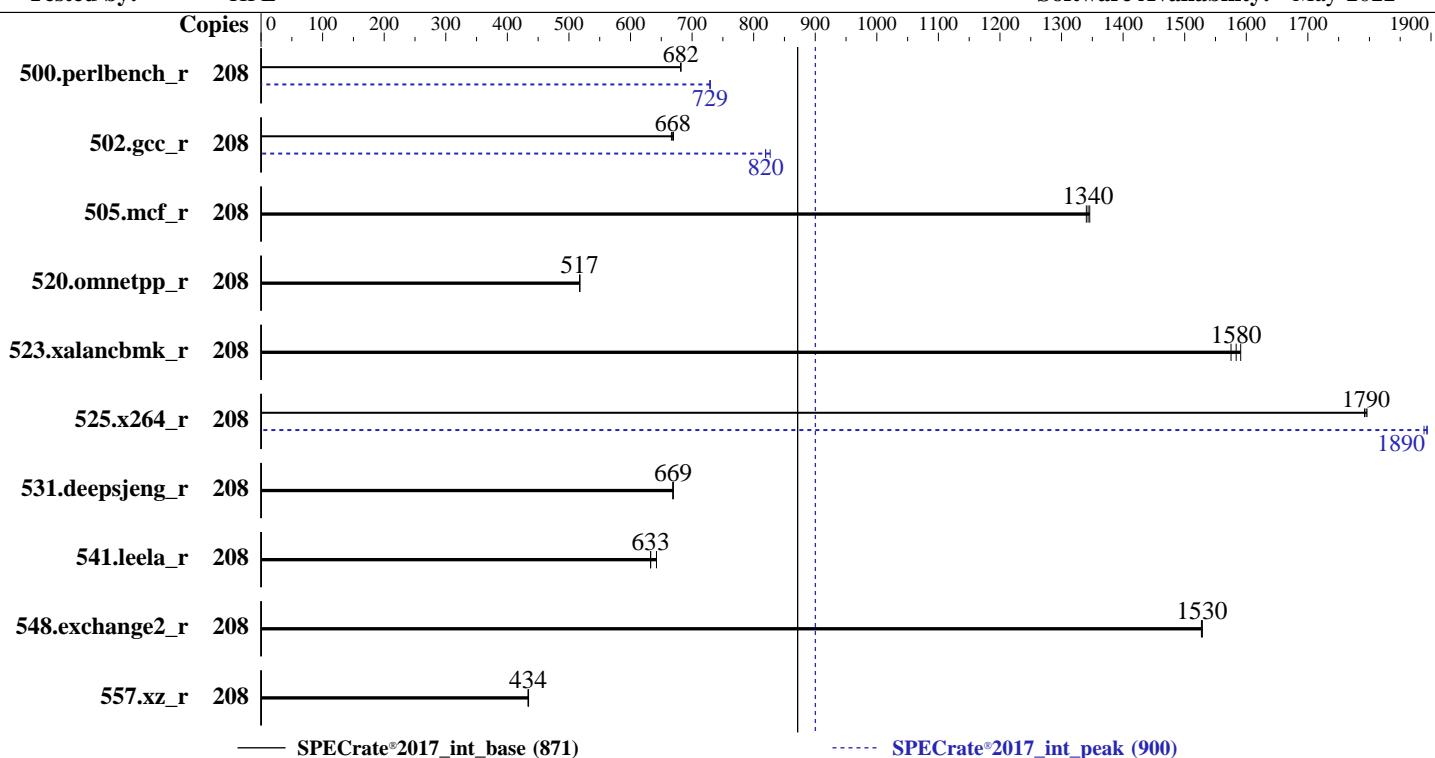
**Test Date:** Feb-2023

**Test Sponsor:** HPE

**Hardware Availability:** Jan-2023

**Tested by:** HPE

**Software Availability:** May-2022



— SPECrate®2017\_int\_base (871)

----- SPECrate®2017\_int\_peak (900)

## Hardware

CPU Name: Intel Xeon Platinum 8470  
 Max MHz: 3800  
 Nominal: 2000  
 Enabled: 96 cores, 2 chips, 2 threads/core  
 Orderable: 1, 2 chip(s)  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 105 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

## OS:

Red Hat Enterprise Linux 9.0 (Plow)

Kernel 5.14.0-70.13.1.el9\_0.x86\_64

C/C++: Version 2022.1 of Intel oneAPI DPC++/C++ Compiler for Linux;

Fortran: Version 2022.1 of Intel Fortran Compiler for Linux;

No

HPE BIOS Version v1.22 01/18/2023 released Jan-2023

## Compiler:

Run level 3 (multi-user)

File System: xfs

System State: 64-bit

Base Pointers: 32/64-bit

Peak Pointers: jemalloc memory allocator V5.0.1

Other: BIOS and OS set to prefer performance at the cost of additional power usage

## Software



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

**SPECrate®2017\_int\_base = 871**

**SPECrate®2017\_int\_peak = 900**

CPU2017 License: 3

Test Date: Feb-2023

Test Sponsor: HPE

Hardware Availability: Jan-2023

Tested by: HPE

Software Availability: May-2022

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	208	486	682	486	681	<b>486</b>	<b>682</b>	208	454	730	<b>454</b>	<b>729</b>	454	729	454	729
502.gcc_r	208	442	666	<b>441</b>	<b>668</b>	440	670	208	<b>359</b>	<b>820</b>	356	827	360	819	360	819
505.mcf_r	208	250	1350	<b>250</b>	<b>1340</b>	251	1340	208	250	1350	<b>250</b>	<b>1340</b>	251	1340	251	1340
520.omnetpp_r	208	528	517	<b>527</b>	<b>517</b>	527	518	208	528	517	<b>527</b>	<b>517</b>	527	518	527	518
523.xalancbmk_r	208	<b>139</b>	<b>1580</b>	138	1590	139	1580	208	<b>139</b>	<b>1580</b>	138	1590	139	1580	139	1580
525.x264_r	208	<b>203</b>	<b>1790</b>	203	1790	203	1800	208	192	1890	193	1890	<b>192</b>	<b>1890</b>	192	1890
531.deepsjeng_r	208	356	669	<b>356</b>	<b>669</b>	356	669	208	356	669	<b>356</b>	<b>669</b>	356	669	356	669
541.leela_r	208	544	633	536	642	<b>544</b>	<b>633</b>	208	544	633	536	642	<b>544</b>	<b>633</b>	544	633
548.exchange2_r	208	357	1530	357	1530	<b>357</b>	<b>1530</b>	208	357	1530	357	1530	357	1530	<b>357</b>	<b>1530</b>
557.xz_r	208	517	434	518	434	<b>518</b>	<b>434</b>	208	517	434	518	434	518	434	<b>518</b>	<b>434</b>

**SPECrate®2017\_int\_base = 871**

**SPECrate®2017\_int\_peak = 900**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

SPEC has ruled that the compiler used for this result was performing a compilation that specifically improves the performance of the 523.xalancbmk\_r / 623.xalancbmk\_s benchmarks using a priori knowledge of the SPEC code and dataset to perform a transformation that has narrow applicability.

In order to encourage optimizations that have wide applicability (see rule 1.4 [https://www.spec.org/cpu2017/Docs/runrules.html#rule\\_1.4](https://www.spec.org/cpu2017/Docs/runrules.html#rule_1.4)), SPEC will no longer publish results using this optimization.

This result is left in the SPEC results database for historical reference.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  
 sync; echo 3> /proc/sys/vm/drop\_caches  
 runcpu command invoked through numactl i.e.:  
 numactl --interleave=all runcpu <etc>  
 IRQ balance service was stopped using "systemctl stop irqbalance.service"  
 tuned-adm profile was set to Accelerator-Performance using "tuned-adm profile accelerator-performance"



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/lib/ia32:/home/cpu2017/je5.0.1-32"  
MALLOC\_CONF = "retain:true"

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM  
memory using Red Hat Enterprise Linux 8.4

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)  
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)  
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)  
is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

The system ROM used for this result contains Intel microcode version 0x2b000161 for  
the Intel Xeon Platinum 8470 processor.

BIOS Configuration

Workload Profile set to General Throughput Compute

Memory Patrol Scrubbing set to Disabled

Last Level Cache (LLC) Dead Line Allocation set to Disabled

Intel UPI Link Enablement set to Single Link

Enhanced Processor Performance Profile set to Aggressive

Thermal Configuration set to Maximum Cooling

Workload Profile set to Custom

Adjacent Sector Prefetch set to Disabled

DCU Stream Prefetcher set to Disabled

Intel UPI Link Power Management set to Enabled

Minimum Processor Idle Power Package C-State set to Package C6 (non-retention) State

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost.localdomain Fri Feb 3 19:58:53 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

1. uname -a  
2. w  
3. Username  
4. ulimit -a  
5. sysinfo process ancestry  
6. /proc/cpuinfo  
7. lscpu  
8. numactl --hardware  
9. /proc/meminfo  
10. who -r  
11. Systemd service manager version: systemd 250 (250-6.el9\_0)  
12. Services, from systemctl list-unit-files  
13. Linux kernel boot-time arguments, from /proc/cmdline

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Platform Notes (Continued)

14. cpupower frequency-info

15. tuned-adm active

16. sysctl

17. /sys/kernel/mm/transparent\_hugepage

18. /sys/kernel/mm/transparent\_hugepage/khugepaged

19. OS release

20. Disk information

21. /sys/devices/virtual/dmi/id

22. dmidecode

23. BIOS

-----  
1. uname -a

Linux localhost.localdomain 5.14.0-70.13.1.el9\_0.x86\_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86\_64  
x86\_64 x86\_64 GNU/Linux

-----  
2. w

19:58:53 up 15 min, 0 users, load average: 0.15, 0.31, 0.70  
USER TTY LOGIN@ IDLE JCPU PCPU WHAT

-----  
3. Username

From environment variable \$USER: root

-----  
4. ulimit -a

real-time non-blocking time (microseconds, -R) unlimited  
core file size (blocks, -c) 0  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (i) 4127055  
max locked memory (kbytes, -l) 64  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited  
cpu time (seconds, -t) unlimited  
max user processes (-u) 4127055  
virtual memory (kbytes, -v) unlimited  
file locks (-x) unlimited

-----  
5. sysinfo process ancestry

/usr/lib/systemd/systemd --switched-root --system --deserialize 28  
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups  
sshd: root [priv]  
sshd: root@notty  
bash -c cd \$SPEC/ && \$SPEC/intrate.sh  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=208 -c  
ic2022.1-lin-core-avx512-rate-20220316.cfg --define smt-on --define cores=104 --define physicalfirst  
--define invoke\_with\_interleave --define drop\_caches --tune base,peak -o all intrate  
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=208 --configfile  
ic2022.1-lin-core-avx512-rate-20220316.cfg --define smt-on --define cores=104 --define physicalfirst  
--define invoke\_with\_interleave --define drop\_caches --tune base,peak --output\_format all --nopower  
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

**SPECrate®2017\_int\_base = 871**

**SPECrate®2017\_int\_peak = 900**

CPU2017 License: 3

**Test Date:** Feb-2023

Test Sponsor: HPE

**Hardware Availability:** Jan-2023

Tested by: HPE

**Software Availability:** May-2022

## Platform Notes (Continued)

```
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017
```

---

```
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Platinum 8470
vendor_id       : GenuineIntel
cpu family     : 6
model          : 143
stepping        : 6
microcode      : 0x2b000161
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 52
siblings        : 104
2 physical ids (chips)
208 processors (hardware threads)
physical id 0: core ids 0-51
physical id 1: core ids 0-51
physical id 0: apicids 0-103
physical id 1: apicids 128-231
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

---

```
7. lscpu
```

From lscpu from util-linux 2.37.4:

```
Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:        46 bits physical, 57 bits virtual
Byte Order:           Little Endian
CPU(s):               208
On-line CPU(s) list: 0-207
Vendor ID:            GenuineIntel
BIOS Vendor ID:      Intel(R) Corporation
Model name:           Intel(R) Xeon(R) Platinum 8470
BIOS Model name:     Intel(R) Xeon(R) Platinum 8470
CPU family:           6
Model:                143
Thread(s) per core:  2
Core(s) per socket:  52
Socket(s):           2
Stepping:             6
BogoMIPS:             4000.00
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                      nonstop_tsc cpuid aperf fmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                      ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtrp pdcm pcid dca sse4_1
                      sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                      lahf_lm abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13
                      invpcid_single cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                      vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
                      erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                      clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsaves
                      xgetbv1 xsaves cqm_llc cqm_occur_llc cqm_mbm_total cqm_mbm_local
                      split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                      avx512vbmi umip pkru ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                      avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid bus_lock_detect
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

**SPECrate®2017\_int\_base = 871**

**SPECrate®2017\_int\_peak = 900**

CPU2017 License: 3

**Test Date:** Feb-2023

Test Sponsor: HPE

**Hardware Availability:** Jan-2023

Tested by: HPE

**Software Availability:** May-2022

## Platform Notes (Continued)

```
cldemote movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
```

Virtualization:

VT-x

L1d cache:

4.9 MiB (104 instances)

L1i cache:

3.3 MiB (104 instances)

L2 cache:

208 MiB (104 instances)

L3 cache:

210 MiB (2 instances)

NUMA node(s):

8

NUMA node0 CPU(s):

0-12,104-116

NUMA node1 CPU(s):

13-25,117-129

NUMA node2 CPU(s):

26-38,130-142

NUMA node3 CPU(s):

39-51,143-155

NUMA node4 CPU(s):

52-64,156-168

NUMA node5 CPU(s):

65-77,169-181

NUMA node6 CPU(s):

78-90,182-194

NUMA node7 CPU(s):

91-103,195-207

Vulnerability Itlb multihit:

Not affected

Vulnerability Llft:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced IBRS, IBPB conditional, RSB filling

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.9M	12	Data	1	64	1	64
L1i	32K	3.3M	8	Instruction	1	64	1	64
L2	2M	208M	16	Unified	2	2048	1	64
L3	105M	210M	15	Unified	3	114688	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-12,104-116

node 0 size: 128731 MB

node 0 free: 127447 MB

node 1 cpus: 13-25,117-129

node 1 size: 129018 MB

node 1 free: 128503 MB

node 2 cpus: 26-38,130-142

node 2 size: 129018 MB

node 2 free: 128527 MB

node 3 cpus: 39-51,143-155

node 3 size: 129018 MB

node 3 free: 128503 MB

node 4 cpus: 52-64,156-168

node 4 size: 129018 MB

node 4 free: 128578 MB

node 5 cpus: 65-77,169-181

node 5 size: 128981 MB

node 5 free: 128586 MB

node 6 cpus: 78-90,182-194

node 6 size: 129018 MB

node 6 free: 128641 MB

node 7 cpus: 91-103,195-207

node 7 size: 128998 MB

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Platform Notes (Continued)

```
node 7 free: 128592 MB
node distances:
node  0   1   2   3   4   5   6   7
 0: 10  20  30  30  30  30  30  30
 1: 20  10  30  30  30  30  30  30
 2: 30  30  10  20  30  30  30  30
 3: 30  30  20  10  30  30  30  30
 4: 30  30  30  30  10  20  30  30
 5: 30  30  30  30  20  10  30  30
 6: 30  30  30  30  30  30  10  20
 7: 30  30  30  30  30  30  20  10

-----
9. /proc/meminfo
MemTotal:      1056566984 kB

-----
10. who -r
run-level 3 Feb 3 19:44

-----
11. Systemd service manager version: systemd 250 (250-6.el9_0)
Default Target      Status
multi-user          running

-----
12. Services, from systemctl list-unit-files
STATE            UNIT FILES
enabled          NetworkManager NetworkManager-dispatcher NetworkManager-wait-online audited chronyd crond
                  dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode
                  nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
                  systemd-network-generator tuned udisks2
enabled-runtime  systemd-remount-fs
disabled         blk-availability chrony-wait console-getty cpupower debug-shell kvm_stat
                  man-db-restart-cache-update nftables rdisc rhsm-facts rpmbuild rebuild serial-getty@
                  sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect         sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap

-----
14. cpupower frequency-info
analyzing CPU 0:
  Unable to determine current policy
  boost state support:
    Supported: yes
    Active: yes

-----
15. tuned-adm active
  Current active profile: accelerator-performance
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Platform Notes (Continued)

```
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       2
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio      10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 40
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   10
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage
defrag      always defer defer+madvise [madvise] never
enabled     [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

```
-----  
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs        60000
defrag                      1
max_ptes_none                511
max_ptes_shared              256
max_ptes_swap                64
pages_to_scan                4096
scan_sleep_millisecs         10000
```

```
-----  
19. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release Red Hat Enterprise Linux release 9.0 (Plow)
system-release Red Hat Enterprise Linux release 9.0 (Plow)
```

```
-----  
20. Disk information
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   819G   32G  787G  4% /home
```

```
-----  
21. /sys/devices/virtual/dmi/id
Vendor:          HPE
Product:        ProLiant DL360 Gen11
Product Family: ProLiant
Serial:         CNX20800PW
```

```
-----  
22. dmidecode
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Platform Notes (Continued)

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M321R8GA0BB0-CQKDG 64 GB 2 rank 4800

-----  
23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE

BIOS Version: 1.22

BIOS Date: 01/18/2023

BIOS Revision: 1.22

Firmware Revision: 1.10

## Compiler Version Notes

=====

C | 502.gcc\_r(peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

C | 502.gcc\_r(peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

=====

Fortran | 548.exchange2\_r(base, peak)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Compiler Version Notes (Continued)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64\_lin  
-lqkmalloc

C++ benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto  
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64\_lin  
-lqkmalloc

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Base Optimization Flags (Continued)

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -fno-omit-frame-pointer  
-mfpmath=sse -funroll-loops -fopt-mem-layout-trans=4  
-fno-standard-realloc-lhs -falign array32byte -fno-align-functions  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64  
502.gcc_r: -D_FILE_OFFSET_BITS=64  
505.mcf_r: -DSPEC_LP64  
520.omnetpp_r: -DSPEC_LP64  
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX  
525.x264_r: -DSPEC_LP64  
531.deepsjeng_r: -DSPEC_LP64  
541.leela_r: -DSPEC_LP64  
548.exchange2_r: -DSPEC_LP64  
557.xz_r: -DSPEC_LP64
```

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs  
-fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

SPECrate®2017\_int\_base = 871

SPECrate®2017\_int\_peak = 900

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2023

Hardware Availability: Jan-2023

Software Availability: May-2022

## Peak Optimization Flags (Continued)

500.perlbench\_r (continued):

```
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-strict-overflow  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

502.gcc\_r: -m32

```
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/ia32_lin  
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)  
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512  
-Ofast -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -L/usr/local/jemalloc32-5.0.1/lib  
-ljemalloc
```

505.mcf\_r: basepeak = yes

```
525.x264_r: -w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-alias  
-L/usr/local/intel/compiler/2022.1.0/linux/compiler/lib/intel64_lin  
-lqkmalloc
```

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev1.1.html>  
[http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.html)

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev1.1.xml>  
[http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64_revA.xml)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen11

(2.00 GHz, Intel Xeon Platinum 8470)

**SPECrate®2017\_int\_base = 871**

**SPECrate®2017\_int\_peak = 900**

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

**Test Date:** Feb-2023

**Hardware Availability:** Jan-2023

**Software Availability:** May-2022

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2023-02-03 09:28:53-0500.

Report generated on 2024-01-29 17:27:44 by CPU2017 PDF formatter v6716.

Originally published on 2023-03-28.