



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

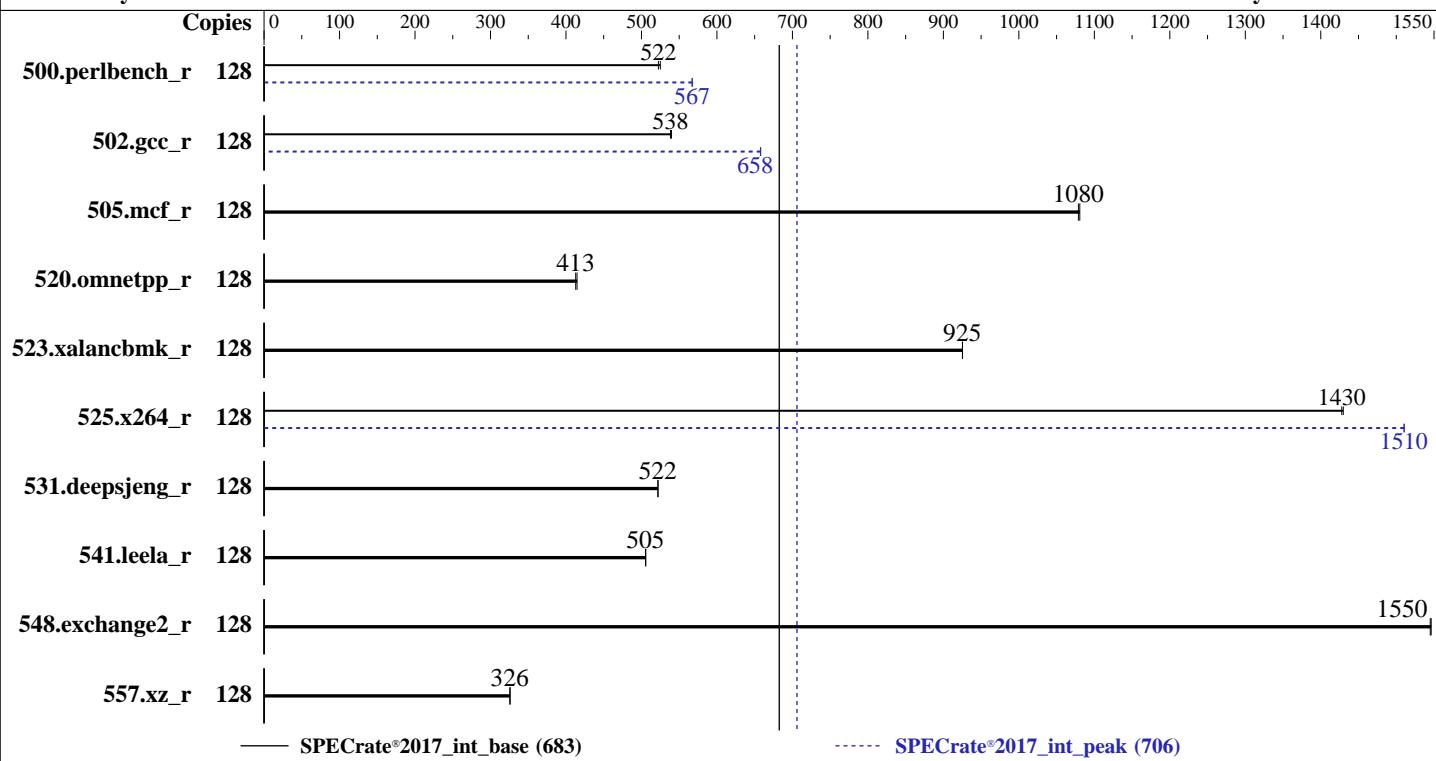
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023



— SPECrate®2017\_int\_base (683)

----- SPECrate®2017\_int\_peak (706)

## Hardware

CPU Name: Intel Xeon Platinum 8562Y+  
Max MHz: 4100  
Nominal: 2800  
Enabled: 64 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 60 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R)  
Storage: 80 GB on tmpfs  
Other: Cooling: DLC

## Software

OS: SUSE Linux Enterprise Server 15 SP5  
5.14.21-150500.53-default  
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: Version 2.0.0 released Nov-2023  
File System: tmpfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: 32/64-bit  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

**SPECrate®2017\_int\_base = 683**

**SPECrate®2017\_int\_peak = 706**

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	128	388	525	<b>390</b>	<b>522</b>			128	<b>359</b>	<b>567</b>	359	567				
502.gcc_r	128	<b>337</b>	<b>538</b>	336	540			128	<b>276</b>	<b>658</b>	275	658				
505.mcf_r	128	191	1080	<b>192</b>	<b>1080</b>			128	191	1080	<b>192</b>	<b>1080</b>				
520.omnetpp_r	128	405	415	<b>407</b>	<b>413</b>			128	405	415	<b>407</b>	<b>413</b>				
523.xalancbmk_r	128	146	925	<b>146</b>	<b>925</b>			128	146	925	<b>146</b>	<b>925</b>				
525.x264_r	128	157	1430	<b>157</b>	<b>1430</b>			128	<b>148</b>	<b>1510</b>	148	1510				
531.deepsjeng_r	128	<b>281</b>	<b>522</b>	281	522			128	<b>281</b>	<b>522</b>	281	522				
541.leela_r	128	<b>419</b>	<b>505</b>	419	506			128	<b>419</b>	<b>505</b>	419	506				
548.exchange2_r	128	<b>217</b>	<b>1550</b>	217	1550			128	<b>217</b>	<b>1550</b>	217	1550				
557.xz_r	128	424	326	<b>425</b>	<b>326</b>			128	424	326	<b>425</b>	<b>326</b>				

**SPECrate®2017\_int\_base = 683**

**SPECrate®2017\_int\_peak = 706**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
    "/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/mnt
    /ramdisk/cpu2017-1.1.9-ic2023.2.3/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
runcpu command invoked through numactl i.e.:
```

```
numactl --interleave=all runcpu <etc>
```

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 80 GB ramdisk created with the cmd: "mount -t tmpfs -o size=80G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

    ADDDC Setting : Disabled  
    DIMM Self Healing on  
    Uncorrectable Memory Error : Disabled

    Virtualization Technology : Disabled  
        DCU Streamer Prefetcher : Disabled  
            Sub NUMA Cluster : 2-way Clustering  
            LLC Prefetch : Disabled  
        Dead Line LLC Alloc : Disabled  
        Optimizer Mode : Enabled

    System Profile : Custom  
    CPU Power Management : Maximum Performance  
        C1E : Disabled  
        C States : Autonomous  
    Memory Patrol Scrub : Disabled  
    Energy Efficiency Policy : Performance  
        PCI ASPM L1 Link  
            Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Tue Jan 2 19:49:34 2024

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent\_hugepage

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

SPECCrate®2017\_int\_base = 683

SPECCrate®2017\_int\_peak = 706

CPU2017 License: 6573

Test Date: Jan-2024

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2024

Tested by: Dell Inc.

Software Availability: Dec-2023

## Platform Notes (Continued)

18. /sys/kernel/mm/transparent\_hugepage/khugepaged

19. OS release

20. Disk information

21. /sys/devices/virtual/dmi/id

22. dmidecode

23. BIOS

1. uname -a

```
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

2. w

```
19:49:34 up 14 min, 1 user, load average: 0.38, 16.70, 18.67
USER      TTY      FROM             LOGIN@     IDLE     JCPU     PCPU WHAT
root      ttys1     -           19:36     1:02    0.88s   0.00s /bin/bash ./dell-run-speccpu.sh rate
--define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define DL-BIOS-adddcD=1 --define
DL-VERS=v4.8.6 --output_format html,pdf,txt --define DL-LQC=1
```

3. Username

From environment variable \$USER: root

4. ulimit -a

```
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size               (blocks, -f) unlimited
pending signals         (-i) 4126054
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files              (-n) 1024
pipe size               (512 bytes, -p) 8
POSIX message queues   (bytes, -q) 819200
real-time priority      (-r) 0
stack size              (kbytes, -s) unlimited
cpu time                (seconds, -t) unlimited
max user processes      (-u) 4126054
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
/bin/bash ./DELL_rate.sh
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-main.sh rate
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
--define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt --define DL-LQC=1
/bin/bash ./dell-run-speccpu.sh rate --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1
--define DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt --define DL-LQC=1
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 -c
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak -o all --define DL-BIOS-SNC=2
--iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```
DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt --define DL-LQC=1 intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=128 --configfile
ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=64 --define physicalfirst
--define invoke_with_interleave --define drop_caches --tune base,peak --output_format all --define
DL-BIOS-SNC=2 --iterations 2 --define DL-BIOSinc=Dell-BIOS_Xeon-5.inc --define DL-BIOS-LogProc=1 --define
DL-BIOS-adddcD=1 --define DL-VERS=v4.8.6 --output_format html,pdf,txt --define DL-LQC=1 --nopower
--runmode rate --tune base:peak --size refrate intrate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.001/templogs/preenv.intrate.001.0.log --lognum 001.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
```

-----

```
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) PLATINUM 8562Y+
vendor_id       : GenuineIntel
cpu family      : 6
model           : 207
stepping         : 2
microcode        : 0x21000200
bugs             : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrss_pbrsb
cpu cores        : 32
siblings          : 64
2 physical ids (chips)
128 processors (hardware threads)
physical id 0: core ids 0-31
physical id 1: core ids 0-31
physical id 0: apicids 0-63
physical id 1: apicids 128-191
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

-----

7. lscpu

```
From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                128
On-line CPU(s) list:   0-127
Vendor ID:             GenuineIntel
Model name:            INTEL(R) XEON(R) PLATINUM 8562Y+
CPU family:            6
Model:                 207
Thread(s) per core:    2
Core(s) per socket:    32
Socket(s):             2
Stepping:              2
BogoMIPS:              5600.00
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                      clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                      lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                      nonstop_tsc cpuid aperf mperf tsc_known_freq pni pclmulqdq dtes64 monitor
                      ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
                      x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
                      abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
                      cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
                      avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                      avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```

xsaveopt xsavec xgetbvl xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hfi
avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
avx512_vnni avx512_bitalg tme avx512_vpocntdq la57 rdpid bus_lock_detect
cldemote movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
L1d cache: 3 MiB (64 instances)
L1i cache: 2 MiB (64 instances)
L2 cache: 128 MiB (64 instances)
L3 cache: 120 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,62,64,68,72,76,80,84,88,92,96,10
0,104,108,112,116,120,126
NUMA node1 CPU(s): 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,60,66,70,74,78,82,86,90,94,98,1
02,106,110,114,118,122,124
NUMA node2 CPU(s): 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,63,65,69,73,77,81,85,89,93,97,10
1,105,109,113,117,121,127
NUMA node3 CPU(s): 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,61,67,71,75,79,83,87,91,95,99,1
03,107,111,115,119,123,125
Vulnerability Itlb multihit: Not affected
Vulnerability Llft: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW
sequence
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3M	12	Data	1	64	1	64
L1i	32K	2M	8	Instruction	1	64	1	64
L2	2M	128M	16	Unified	2	2048	1	64
L3	60M	120M	15	Unified	3	65536	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus:

0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,62,64,68,72,76,80,84,88,92,96,100,104,108,112,116,120,126

node 0 size: 257491 MB

node 0 free: 256919 MB

node 1 cpus:

2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,60,66,70,74,78,82,86,90,94,98,102,106,110,114,118,122,124

node 1 size: 258039 MB

node 1 free: 257529 MB

node 2 cpus:

1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,63,65,69,73,77,81,85,89,93,97,101,105,109,113,117,121,127

node 2 size: 258005 MB

node 2 free: 257467 MB

node 3 cpus:

3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,61,67,71,75,79,83,87,91,95,99,103,107,111,115,119,123,125

node 3 size: 258000 MB

node 3 free: 249240 MB

node distances:

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```
node   0   1   2   3
 0:  10  12  21  21
 1:  12  10  21  21
 2:  21  21  10  12
 3:  21  21  12  10
```

```
-----  
9. /proc/meminfo  
MemTotal:      1056295000 kB
```

```
-----  
10. who -r  
run-level 3 Jan 2 19:36
```

```
-----  
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)  
Default Target     Status  
multi-user         running
```

```
-----  
12. Services, from systemctl list-unit-files  
STATE          UNIT FILES  
enabled        apparmor audtfd cron firewalld getty@ irqbalance issue-generator kbdsettings postfix  
                purge-kernels rollback sshd systemd-pstore tuned wickedd-wicked4 wickedd-dhcp4  
                wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled       boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebttables  
                grub2-once haveged-switch-root issue-add-ssh-keys kexec-load lunmask  
                rpmconfigcheck rsyncd serial-getty@ systemd-boot-check-no-failures  
                systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd  
indirect       wickedd
```

```
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=4e896243-c311-4fee-971d-8de907cffcf2  
splash=silent  
mitigations=auto  
quiet  
security=apparmor
```

```
-----  
14. cpupower frequency-info  
analyzing CPU 0:  
  Unable to determine current policy  
  boost state support:  
    Supported: yes  
    Active: yes
```

```
-----  
15. tuned-adm active  
  Current active profile: throughput-performance
```

```
-----  
16. sysctl  
kernel.numa_balancing      1  
kernel.randomize_va_space   2  
vm.compaction_proactiveness 20  
vm.dirty_background_bytes   0  
vm.dirty_background_ratio   10
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```
vm.dirty_bytes          0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio          20
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold    500
vm.min_unmapped_ratio   1
vm.nr_hugepages          0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness            10
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode     0
```

```
17. /sys/kernel/mm/transparent_hugepage
    defrag      always defer defer+madvise [madvise] never
    enabled     [always] madvise never
    hpage_pmd_size 2097152
    shmem_enabled always within_size advise [never] deny force
```

```
18. /sys/kernel/mm/transparent_hugepage/khugepaged
    alloc_sleep_millisecs 60000
    defrag                 1
    max_ptes_none          511
    max_ptes_shared         256
    max_ptes_swap           64
    pages_to_scan          4096
    scan_sleep_millisecs   10000
```

```
19. OS release
  From /etc/*-release /etc/*-version
  os-release SUSE Linux Enterprise Server 15 SP5
```

```
20. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2023.2.3
Filesystem      Type  Size  Used Avail Use% Mounted on
tmpfs          tmpfs  80G   4.1G  76G   6%  /mnt/ramdisk
```

```
21. /sys/devices/virtual/dmi/id
  Vendor:        Dell Inc.
  Product:       PowerEdge MX760c
  Product Family: PowerEdge
  Serial:        SL6M703
```

```
22. dmidecode
Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  16x 002C0632002C MTC40F2046S1RC56BG1 64 GB 2 rank 5600
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 2.0.0  
BIOS Date: 11/23/2023  
BIOS Revision: 2.0

## Compiler Version Notes

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 502.gcc\_r(peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on IA-32, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C | 500.perlbench\_r(base, peak) 502.gcc\_r(base) 505.mcf\_r(base, peak) 525.x264\_r(base, peak)  
| 557.xz\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++ | 520.omnetpp\_r(base, peak) 523.xalancbmk\_r(base, peak) 531.deepsjeng\_r(base, peak)  
| 541.leela\_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 548.exchange2\_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Base Portability Flags

```
500.perlbench_r: -DSPEC_LP64 -DSPEC_LINUX_X64
502.gcc_r: -DSPEC_LP64
505.mcf_r: -DSPEC_LP64
520.omnetpp_r: -DSPEC_LP64
523.xalancbmk_r: -DSPEC_LP64 -DSPEC_LINUX
525.x264_r: -DSPEC_LP64
531.deepsjeng_r: -DSPEC_LP64
541.leela_r: -DSPEC_LP64
548.exchange2_r: -DSPEC_LP64
557.xz_r: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

## Peak Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

502.gcc\_r: -D\_FILE\_OFFSET\_BITS=64

505.mcf\_r: -DSPEC\_LP64

520.omnetpp\_r: -DSPEC\_LP64

523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX

525.x264\_r: -DSPEC\_LP64

531.deepsjeng\_r: -DSPEC\_LP64

541.leela\_r: -DSPEC\_LP64

548.exchange2\_r: -DSPEC\_LP64

557.xz\_r: -DSPEC\_LP64

## Peak Optimization Flags

C benchmarks:

```
500.perlbench_r: -w -std=c11 -m64 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-fno-strict-overflow
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc

502.gcc_r: -m32
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/ia32_lin
-std=gnu89 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2(pass 1)
-flto -Ofast -xCORE-AVX512 -ffast-math -mfpmath=sse
-funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc32-5.0.1/lib -ljemalloc
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Dell Inc.

PowerEdge MX760c (Intel Xeon Platinum 8562Y+)

SPECrate®2017\_int\_base = 683

SPECrate®2017\_int\_peak = 706

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Peak Optimization Flags (Continued)

505.mcf\_r: basepeak = yes

525.x264\_r: -w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -Ofast  
-ffast-math -futo -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fno-alias  
-L/home/specdev/new\_compilers/ic2023.2.3/compiler/lib/intel64\_lin  
-lqkmalloc

557.xz\_r: basepeak = yes

C++ benchmarks:

520.omnetpp\_r: basepeak = yes

523.xalancbmk\_r: basepeak = yes

531.deepsjeng\_r: basepeak = yes

541.leela\_r: basepeak = yes

Fortran benchmarks:

548.exchange2\_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.6.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-01-02 20:49:34-0500.

Report generated on 2024-01-30 23:23:25 by CPU2017 PDF formatter v6716.

Originally published on 2024-01-30.