



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

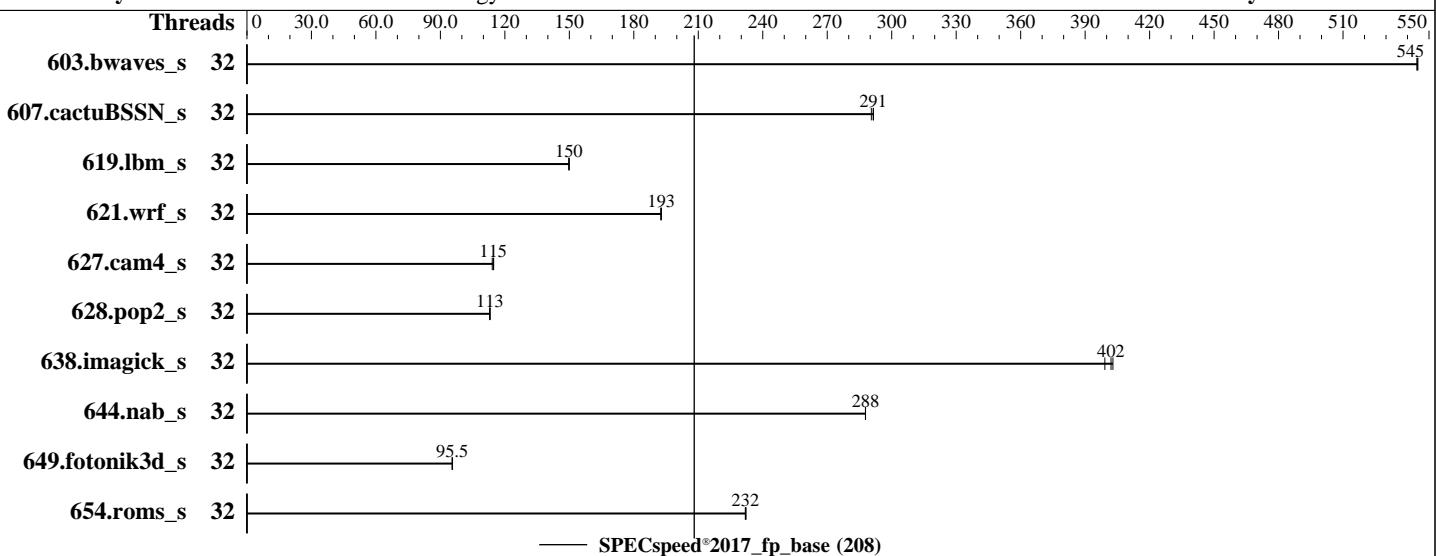
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Gold 6433N  
Max MHz: 3600  
Nominal: 2000  
Enabled: 32 cores, 1 chip  
Orderable: 1 chip  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 60 MB I+D on chip per chip  
Other: None  
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-4800B-R, running at 4400)  
Storage: 1 x 960 GB SATA SSD  
Other: None

### Software

OS: Red Hat Enterprise Linux 9.2 (Plow)  
Compiler: Kernel 5.14.0-284.11.1.el9\_2.x86\_64  
C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
Parallel: Yes  
Firmware: Lenovo BIOS Version ESE121T 3.10 released Dec-2023  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: jemalloc memory allocator V5.0.1  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds	Ratio	Threads	Seconds
603.bwaves_s	32	108	544	<u>108</u>	<u>545</u>	108	545									
607.cactuBSSN_s	32	57.2	292	<u>57.4</u>	<u>291</u>	<u>57.2</u>	<u>291</u>									
619.lbm_s	32	35.0	150	<u>35.0</u>	<u>150</u>	34.9	150									
621.wrf_s	32	68.7	193	<u>68.6</u>	<u>193</u>	68.6	193									
627.cam4_s	32	77.2	115	<u>77.3</u>	<u>115</u>	77.7	114									
628.pop2_s	32	105	113	105	113	<u>105</u>	<u>113</u>									
638.imagick_s	32	36.1	399	35.8	403	<u>35.9</u>	<u>402</u>									
644.nab_s	32	60.7	288	60.7	288	<u>60.7</u>	<u>288</u>									
649.fotonik3d_s	32	95.5	95.5	95.3	95.7	<u>95.4</u>	<u>95.5</u>									
654.roms_s	32	67.8	232	67.9	232	<u>67.9</u>	<u>232</u>									

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/home/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5 sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Platform Notes

### BIOS configuration:

Choose Operating Mode set to Custom Mode  
Adjacent Cache Prefetch set to Disabled  
Hyper-Threading set to Disabled  
DCU IP Prefetcher set to Disabled  
AMP Prefetch set to Enable

```
Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Thu Jan 11 02:38:32 2024
```

SUT (System Under Test) info as seen by some common utilities.

#### Table of contents

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 252 (252-13.el9\_2)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. tuned-adm active
- 16. sysctl
- 17. /sys/kernel/mm/transparent\_hugepage
- 18. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 19. OS release
- 20. Disk information
- 21. /sys/devices/virtual/dmi/id
- 22. dmidecode
- 23. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-284.11.1.el9_2.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 12 10:45:03 EDT
2023 x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
02:38:32 up 2 min, 1 user, load average: 0.02, 0.02, 0.00
USER   TTY    LOGIN@  IDLE   JCPU   PCPU WHAT
root   tty1     02:37   39.00s  1.07s  0.00s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

**SPECspeed®2017\_fp\_base = 208**

**SPECspeed®2017\_fp\_peak = Not Run**

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

## Platform Notes (Continued)

data seg size	(kbytes, -d) unlimited
scheduling priority	(-e) 0
file size	(blocks, -f) unlimited
pending signals	(-i) 1029705
max locked memory	(kbytes, -l) 64
max memory size	(kbytes, -m) unlimited
open files	(-n) 1024
pipe size	(512 bytes, -p) 8
POSIX message queues	(bytes, -q) 819200
real-time priority	(-r) 0
stack size	(kbytes, -s) unlimited
cpu time	(seconds, -t) unlimited
max user processes	(-u) 1029705
virtual memory	(kbytes, -v) unlimited
file locks	(-x) unlimited

---

5. sysinfo process ancestry  
`/usr/lib/systemd/systemd rhgb --switched-root --system --deserialize 31`  
`login -- root`  
`-bash`  
`-bash`  
`runcpu --nobuild --action validate --define default-platform-flags -c`  
`ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=32 --tune base -o all --define drop_caches`  
`fpspeed`  
`runcpu --nobuild --action validate --define default-platform-flags --configfile`  
`ic2023.2.3-lin-core-avx512-speed-20231121.cfg --define cores=32 --tune base --output_format all --define`  
`drop_caches --nopower --runmode speed --tune base --size refspeed fpspeed --nopreenv --note-preenv`  
`--logfile $SPEC/tmp/CPU2017.089/templogs/preenv.fpspeed.089.0.log --lognum 089.0 --from_runcpu 2`  
`specperl $SPEC/bin/sysinfo`  
`$SPEC = /home/cpu/cpu2017-1.1.9-ic2023.2.3`

---

6. /proc/cpuinfo  
`model name : Intel(R) Xeon(R) Gold 6433N`  
`vendor_id : GenuineIntel`  
`cpu family : 6`  
`model : 143`  
`stepping : 8`  
`microcode : 0xb000571`  
`bugs : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrp_pbrsb`  
`cpu cores : 32`  
`siblings : 32`  
`1 physical ids (chips)`  
`32 processors (hardware threads)`  
`physical id 0: core ids 0-31`  
`physical id 0: apicids`  
`0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62`  
`Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for`  
`virtualized systems. Use the above data carefully.`

---

7. lscpu

From lscpu from util-linux 2.37.4:  
`Architecture: x86_64`  
`CPU op-mode(s): 32-bit, 64-bit`  
`Address sizes: 46 bits physical, 57 bits virtual`  
`Byte Order: Little Endian`  
`CPU(s): 32`

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

**SPECspeed®2017\_fp\_base = 208**

**SPECspeed®2017\_fp\_peak = Not Run**

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Jan-2024

**Hardware Availability:** Feb-2024

**Software Availability:** Dec-2023

## Platform Notes (Continued)

On-line CPU(s) list:

0-31

Vendor ID:

GenuineIntel

BIOS Vendor ID:

Intel(R) Corporation

Model name:

Intel(R) Xeon(R) Gold 6433N

BIOS Model name:

Intel(R) Xeon(R) Gold 6433N

CPU family:

6

Model:

143

Thread(s) per core:

1

Core(s) per socket:

32

Socket(s):

1

Stepping:

8

CPU max MHz:

3600.0000

CPU min MHz:

800.0000

BogoMIPS:

4000.00

Flags:

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36  
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp  
lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology  
nonstop\_tsc cpuid aperf mperf tsc\_known\_freq pni pclmulqdq dtes64 ds\_cpl  
vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4\_1 sse4\_2  
x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm  
abm 3dnowprefetch cpuid\_fault epb cat\_13 cat\_12 cdp\_13 invpcid\_single  
intel\_ppin cdip\_12 ssbd mba ibrs ibpb stibp ibrs\_enhanced tpr\_shadow vnmi  
flexpriority ept vpid ept\_ad fsgsbase tsc\_adjust bmil avx2 smp bmi2 erms  
invpcid cqm rdt\_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt  
clwb intel\_pt avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec xgetbv1  
xsaves cq\_m\_l1c cq\_m\_occu\_p\_l1c cq\_m\_mb\_m\_total cq\_m\_mb\_m\_local split\_lock\_detect  
avx\_vnni avx512\_bf16 wbnoinvd dtherm ida arat pln pts hfi avx512vbmi umip  
pku ospke waitpkg avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni  
avx512\_bitalg tme avx512\_vpopcntdq la57 rdpid bus\_lock\_detect cldemote  
movdiri movdir64b enqcmd fsrm md\_clear serialize tsxldtrk pconfig arch\_lbr  
ibt avx512\_fp16 flush\_lld arch\_capabilities

Virtualization:

VT-x

L1d cache:

1.5 MiB (32 instances)

L1i cache:

1 MiB (32 instances)

L2 cache:

64 MiB (32 instances)

L3 cache:

60 MiB (1 instance)

NUMA node(s):

1

NUMA node0 CPU(s):

0-31

Vulnerability Itlb multihit:

Not affected

Vulnerability Lltf:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Mmio stale data:

Not affected

Vulnerability Retbleed:

Not affected

Vulnerability Spec store bypass:

Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1:

Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization

Vulnerability Spectre v2:

Mitigation; Enhanced IBRS, IBPB conditional, RSB filling, PBRSB-eIBRS SW sequence

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.5M	12	Data	1	64	1	64
L1i	32K	1M	8	Instruction	1	64	1	64
L2	2M	64M	16	Unified	2	2048	1	64
L3	60M	60M	15	Unified	3	65536	1	64

-----  
8. numactl --hardware

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Platform Notes (Continued)

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-31
node 0 size: 257468 MB
node 0 free: 256643 MB
node distances:
node 0
 0: 10
```

```
-----  
9. /proc/meminfo
MemTotal:      263647832 kB
```

```
-----  
10. who -r
  run-level 3 Jan 11 02:35
```

```
-----  
11. Systemd service manager version: systemd 252 (252-13.el9_2)
Default Target      Status
multi-user          running
```

```
-----  
12. Services, from systemctl list-unit-files
STATE            UNIT FILES
enabled          NetworkManager NetworkManager-dispatcher NetworkManager-wait-online atd auditd bluetooth
                  chronyd crond dbus-broker firewalld getty@ insights-client-boot irqbalance iscsi
                  iscsi-onboot kdump libstoragemgmt low-memory-monitor lvm2-monitor mcelog mdmon
                  microcode multipathd nis-domainname nvme-fc-boot-connections rhsmcertd rsyslog rtkit-daemon
                  selinux-autorelabel-mark smartd sshd sssd systemd-boot-update systemd-network-generator
                  tuned udisks2 upower
enabled-runtime   systemd-remount-fs
disabled         arp-ethers blk-availability canberra-system-bootup canberra-system-shutdown
                  canberra-system-shutdown-reboot chrony-wait console-getty cpupower debug-shell
                  dnf-system-upgrade iprdump iprinit iprupdate iscsid iscsiuiuo kpatch kvm_stat ledmon
                  man-db-restart-cache-update nftables nvme-fc-boot-connections pesign psacct rdisc rhcd rhsm
                  rhsm-facts rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
                  systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect          sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
                  systemd-sysupdate-reboot
```

```
-----  
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=(hd3,gpt2)/vmlinuz-5.14.0-284.11.1.el9_2.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
rhgb
quiet
```

```
-----  
14. cpupower frequency-info
analyzing CPU 0:
  current policy: frequency should be within 3.60 GHz and 3.60 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.
boost state support:
  Supported: yes
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Platform Notes (Continued)

Active: yes

-----  
15. tuned-adm active  
Current active profile: throughput-performance

-----  
16. sysctl  
kernel.numa\_balancing 0  
kernel.randomize\_va\_space 2  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 40  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 10  
vm.watermark\_boost\_factor 15000  
vm.watermark\_scale\_factor 10  
vm.zone\_reclaim\_mode 0

-----  
17. /sys/kernel/mm/transparent\_hugepage  
defrag always defer defer+madvise [madvise] never  
enabled [always] madvise never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Red Hat Enterprise Linux 9.2 (Plow)  
redhat-release Red Hat Enterprise Linux release 9.2 (Plow)  
system-release Red Hat Enterprise Linux release 9.2 (Plow)

-----  
20. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 819G 54G 765G 7% /home

-----  
21. /sys/devices/virtual/dmi/id

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

Vendor: Lenovo  
Product: ThinkSystem SR650 V3 MB, EGS, DDR5, SH, 2U  
Product Family: ThinkSystem  
Serial: 1234567890

-----  
22. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

1x Samsung M321R4GA3BB0-CQKEG 32 GB 2 rank 4800, configured at 4400  
7x Samsung M321R4GA3BB6-CQKVG 32 GB 2 rank 4800, configured at 4400

-----  
23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo  
BIOS Version: ESE121T-3.10  
BIOS Date: 12/20/2023  
BIOS Revision: 3.10  
Firmware Revision: 3.90

## Compiler Version Notes

=====

C | 619.lbm\_s(base) 638.imagick\_s(base) 644.nab\_s(base)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

C++, C, Fortran | 607.cactubssn\_s(base)

=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

Fortran | 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====

=====

Fortran, C | 621.wrf\_s(base) 627.cam4\_s(base) 628.pop2\_s(base)

=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Compiler Version Notes (Continued)

## Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
627.cam4\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG  
628.pop2\_s: -DSPEC\_LP64 -DSPEC\_CASE\_FLAG -convert big\_endian  
-assume byterecl  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fopenmp  
-DSPEC\_OPENMP -Wno-implicit-int -L/usr/local/jemalloc64-5.0.1/lib  
-ljemalloc

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -DSPEC\_OPENMP -xCORE-AVX512 -Ofast  
-ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -fopenmp -nostandard-realloc-lhs

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR650 V3  
(2.00 GHz, Intel Xeon Gold 6433N)

SPECspeed®2017\_fp\_base = 208

SPECspeed®2017\_fp\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml>  
<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-01-11 02:38:31-0500.

Report generated on 2024-01-30 23:27:17 by CPU2017 PDF formatter v6716.

Originally published on 2024-01-30.