



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

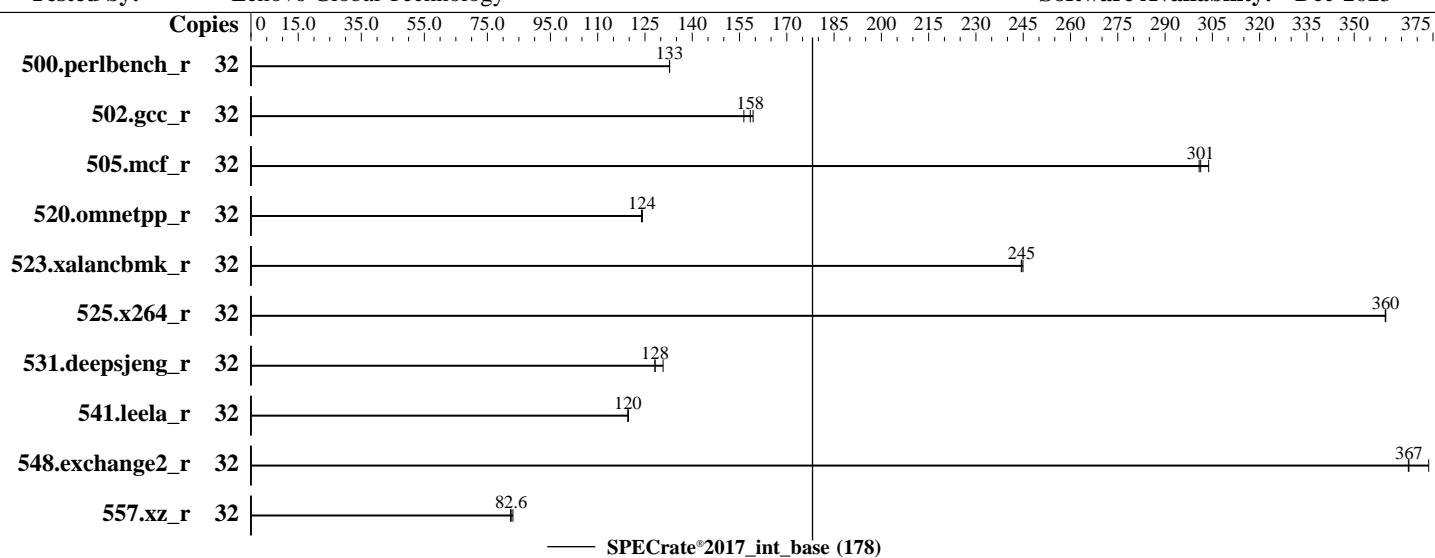
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023



### Hardware

CPU Name: Intel Xeon Gold 5515+  
Max MHz: 4100  
Nominal: 3200  
Enabled: 16 cores, 2 chips, 2 threads/core  
Orderable: 1,2 chips  
Cache L1: 32 KB I + 48 KB D on chip per core  
L2: 2 MB I+D on chip per core  
L3: 22.5 MB I+D on chip per chip  
Other: None  
Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R, running at 4800)  
Storage: 1 x 960 GB SATA SSD  
Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4  
Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;  
Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;  
Parallel: No  
Firmware: Lenovo BIOS Version ESE121V 3.10 released Jan-2024  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: None  
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

**SPECrate®2017\_int\_base = 178**

**SPECrate®2017\_int\_peak = Not Run**

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
500.perlbench_r	32	384	133	<b>384</b>	<b>133</b>	384	133							
502.gcc_r	32	284	159	290	156	<b>286</b>	<b>158</b>							
505.mcf_r	32	170	304	<b>172</b>	<b>301</b>	172	301							
520.omnetpp_r	32	<b>338</b>	<b>124</b>	339	124	338	124							
523.xalancbmk_r	32	<b>138</b>	<b>245</b>	138	244	138	245							
525.x264_r	32	156	360	156	360	<b>156</b>	<b>360</b>							
531.deepsjeng_r	32	<b>286</b>	<b>128</b>	286	128	280	131							
541.leela_r	32	<b>443</b>	<b>120</b>	443	120	443	120							
548.exchange2_r	32	228	367	224	374	<b>228</b>	<b>367</b>							
557.xz_r	32	420	82.3	<b>419</b>	<b>82.6</b>	416	83.1							

**SPECrate®2017\_int\_base = 178**

**SPECrate®2017\_int\_peak = Not Run**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
LD_LIBRARY_PATH =
  "/home/cpu2017-1.1.9-ic2023.2.3/lib/intel64:/home/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/home/cpu2017-1.1.
  9-ic2023.2.3/je5.0.1-32"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

```
sync; echo 3> /proc/sys/vm/drop_caches
```

runcpu command invoked through numactl i.e.:

```
numactl --interleave=all runcpu <etc>
```

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## General Notes (Continued)

is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:

Choose Operating Mode set to Maximum Performance  
SNC set to SNC2

```
Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on test1 Mon Jan 22 00:57:58 2024
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent\_hugepage
17. /sys/kernel/mm/transparent\_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

-----

```
1. uname -a
Linux test1 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86_64
x86_64 x86_64 GNU/Linux
```

-----  
2. w
00:57:58 up 54 min, 1 user, load average: 0.54, 13.68, 23.12
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root ttym1 - 00:04 6.00s 0.98s 0.01s -bash

-----  
3. Username
From environment variable \$USER: root

-----  
4. ulimit -a
core file size (blocks, -c) unlimited

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Platform Notes (Continued)

```
data seg size          (kbytes, -d) unlimited
scheduling priority   (-e) 0
file size             (blocks, -f) unlimited
pending signals       (-i) 4127074
max locked memory    (kbytes, -l) 64
max memory size      (kbytes, -m) unlimited
open files            (-n) 1024
pipe size              (512 bytes, -p) 8
POSIX message queues  (bytes, -q) 819200
real-time priority    (-r) 0
stack size             (kbytes, -s) unlimited
cpu time               (seconds, -t) unlimited
max user processes    (-u) 4127074
virtual memory         (kbytes, -v) unlimited
file locks             (-x) unlimited
```

---

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 -c
  ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base -o all intrate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=32 --configfile
  ic2023.2.3-lin-sapphirerapids-rate-20231121.cfg --define smt-on --define cores=16 --define physicalfirst
  --define invoke_with_interleave --define drop_caches --tune base --output_format all --nopower --runmode
  rate --tune base --size refrate intrate --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.332/templogs/preenv.intrate.332.0.log --lognum 332.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.2.3
```

---

```
6. /proc/cpuinfo
model name      : INTEL(R) XEON(R) GOLD 5515+
vendor_id       : GenuineIntel
cpu family     : 6
model          : 207
stepping        : 2
microcode       : 0x21000200
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 8
siblings        : 16
2 physical ids (chips)
32 processors (hardware threads)
physical id 0: core ids 0-7
physical id 1: core ids 0-7
physical id 0: apicids 0-15
physical id 1: apicids 128-143
```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

---

```
7. lscpu
```

```
From lscpu from util-linux 2.37.2:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         46 bits physical, 57 bits virtual
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Platform Notes (Continued)

```

Byte Order: Little Endian
CPU(s): 32
On-line CPU(s) list: 0-31
Vendor ID: GenuineIntel
Model name: INTEL(R) XEON(R) GOLD 5515+
CPU family: 6
Model: 207
Thread(s) per core: 2
Core(s) per socket: 8
Socket(s): 2
Stepping: 2
BogoMIPS: 6400.00
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
       clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
       lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtTopology
       nonstop_tsc cpuid aperfmpf tsc_known_freq pni pclmulqdq dtes64 ds_cpl
       vmx smx est tm2 ssse3 sdbg fma cx16 xptr pdcm pcid dca sse4_1 sse4_2
       x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
       abm 3dnowprefetch cpuid_fault epb cat_13 cat_12 cdp_13 invpcid_single
       cdp_12 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority
       ept vpvid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid
       rtm cqmq rdt_a avx512f avx512dq rdseed adx smap avx512ifma clflushopt clwb
       intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavexc xgetbv1 xsaves
       cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local avx_vnni avx512_bf16
       wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku ospke waitpkg
       avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
       avx512_vpopcntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
       enqcmd fsrm md_clear serialize tsxlptrk pconfig arch_lbr avx512_fp16
       amx_tile flush_lll arch_capabilities

Virtualization: VT-x
L1d cache: 768 KiB (16 instances)
L1i cache: 512 KiB (16 instances)
L2 cache: 32 MiB (16 instances)
L3 cache: 45 MiB (2 instances)
NUMA node(s): 4
NUMA node0 CPU(s): 0-3,16-19
NUMA node1 CPU(s): 4-7,20-23
NUMA node2 CPU(s): 8-11,24-27
NUMA node3 CPU(s): 12-15,28-31
Vulnerability Itlb multihit: Not affected
Vulnerability Lltf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation: Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation: usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation: Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64
L3	22.5M	45M	15	Unified	3	24576	1	64

-----  
8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```
node 0 cpus: 0-3,16-19
node 0 size: 257709 MB
node 0 free: 256638 MB
node 1 cpus: 4-7,20-23
node 1 size: 258011 MB
node 1 free: 257505 MB
node 2 cpus: 8-11,24-27
node 2 size: 258045 MB
node 2 free: 257462 MB
node 3 cpus: 12-15,28-31
node 3 size: 258025 MB
node 3 free: 257535 MB
node distances:
node    0   1   2   3
 0: 10 12 21 21
 1: 12 10 21 21
 2: 21 21 10 12
 3: 21 21 12 10
```

---

9. /proc/meminfo  
MemTotal: 1056555472 kB

---

10. who -r  
run-level 3 Jan 22 00:04

---

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)  
Default Target Status  
multi-user running

---

12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance iscsi  
issue-generator kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog  
smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled autofs autoyaml-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info  
firewalld gpm grub2-once haveged-switch-root ipmi ipmievfd iscsi-init iscsid iscsiuio  
issue-add-ssh-keys kexec-load lumask man-db-create multipathd nfs nfs-blkmap nmb rdisc  
rpcbind rpmconfigcheck rsyncd serial-getty@ smartd\_generate\_opts smb snmpd snmptrapd  
systemd-boot-check-no-failures systemd-network-generator systemd-sysext  
systemd-time-wait-sync systemd-timesyncd  
generated ntp\_sync  
indirect wickedd

---

13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default  
root=UUID=f976c541-a329-4c54-ba84-4be16556ee18  
splash=silent  
mitigations=auto  
quiet  
security=apparmor

---

14. cpupower frequency-info

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023

## Platform Notes (Continued)

```
analyzing CPU 0:  
  Unable to determine current policy  
  boost state support:  
    Supported: yes  
    Active: yes
```

```
-----  
15. sysctl  
  kernel.numa_balancing          1  
  kernel.randomize_va_space      2  
  vm.compaction_proactiveness   20  
  vm.dirty_background_bytes     0  
  vm.dirty_background_ratio     10  
  vm.dirty_bytes                0  
  vm.dirty_expire_centisecs    3000  
  vm.dirty_ratio                20  
  vm.dirty_writeback_centisecs  500  
  vm.dirtytime_expire_seconds   43200  
  vm.extfrag_threshold          500  
  vm.min_unmapped_ratio         1  
  vm.nr_hugepages               0  
  vm.nr_hugepages_mempolicy     0  
  vm.nr_overcommit_hugepages    0  
  vm.swappiness                 60  
  vm.watermark_boost_factor    15000  
  vm.watermark_scale_factor     10  
  vm.zone_reclaim_mode          0
```

```
-----  
16. /sys/kernel/mm/transparent_hugepage  
  defrag           always defer defer+madvise [madvise] never  
  enabled          [always] madvise never  
  hpage_pmd_size  2097152  
  shmem_enabled    always within_size advise [never] deny force
```

```
-----  
17. /sys/kernel/mm/transparent_hugepage/khugepaged  
  alloc_sleep_millisecs  60000  
  defrag                1  
  max_ptes_none        511  
  max_ptes_shared      256  
  max_ptes_swap        64  
  pages_to_scan        4096  
  scan_sleep_millisecs 10000
```

```
-----  
18. OS release  
  From /etc/*-release /etc/*-version  
  os-release SUSE Linux Enterprise Server 15 SP4
```

```
-----  
19. Disk information  
SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3  
Filesystem  Type  Size  Used Avail Use% Mounted on  
/dev/sdb2    xfs   894G  232G  663G  26% /
```

```
-----  
20. /sys/devices/virtual/dmi/id  
  Vendor:      Lenovo  
  Product:     ThinkSystem SR630 V3
```

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Platform Notes (Continued)

Product Family: ThinkSystem  
Serial: 1234567890

-----  
21. dmidecode

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

16x Samsung M321R8GA0PB0-CWMKH 64 GB 2 rank 5600, configured at 4800

-----  
22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo  
BIOS Version: ESE121V-3.10  
BIOS Date: 01/09/2024  
BIOS Revision: 3.10  
Firmware Revision: 12.6

## Compiler Version Notes

=====  
C | 500.perlbench\_r(base) 502.gcc\_r(base) 505.mcf\_r(base) 525.x264\_r(base) 557.xz\_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====  
C++ | 520.omnetpp\_r(base) 523.xalancbmk\_r(base) 531.deepsjeng\_r(base) 541.leela\_r(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

=====  
Fortran | 548.exchange2\_r(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x  
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

## Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

(Continued on next page)



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifx

## Base Portability Flags

500.perlbench\_r: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
502.gcc\_r: -DSPEC\_LP64  
505.mcf\_r: -DSPEC\_LP64  
520.omnetpp\_r: -DSPEC\_LP64  
523.xalancbmk\_r: -DSPEC\_LP64 -DSPEC\_LINUX  
525.x264\_r: -DSPEC\_LP64  
531.deepsjeng\_r: -DSPEC\_LP64  
541.leela\_r: -DSPEC\_LP64  
548.exchange2\_r: -DSPEC\_LP64  
557.xz\_r: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-L/home/specdev/new_compilers/ic2023.2.3/compiler/lib/intel64_lin
-lqkmalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>  
<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>



# SPEC CPU®2017 Integer Rate Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR630 V3  
(3.20 GHz, Intel Xeon Gold 5515+)

SPECrate®2017\_int\_base = 178

SPECrate®2017\_int\_peak = Not Run

CPU2017 License: 9017

Test Date: Jan-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2024

Tested by: Lenovo Global Technology

Software Availability: Dec-2023

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglesstream-AA.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

SPEC CPU and SPECrate are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.9 on 2024-01-21 11:57:58-0500.

Report generated on 2024-02-14 13:40:44 by CPU2017 PDF formatter v6716.

Originally published on 2024-02-14.