



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECSpeed®2017_int_base = 16.6

SPECSpeed®2017_int_peak = 16.9

CPU2017 License: 9017

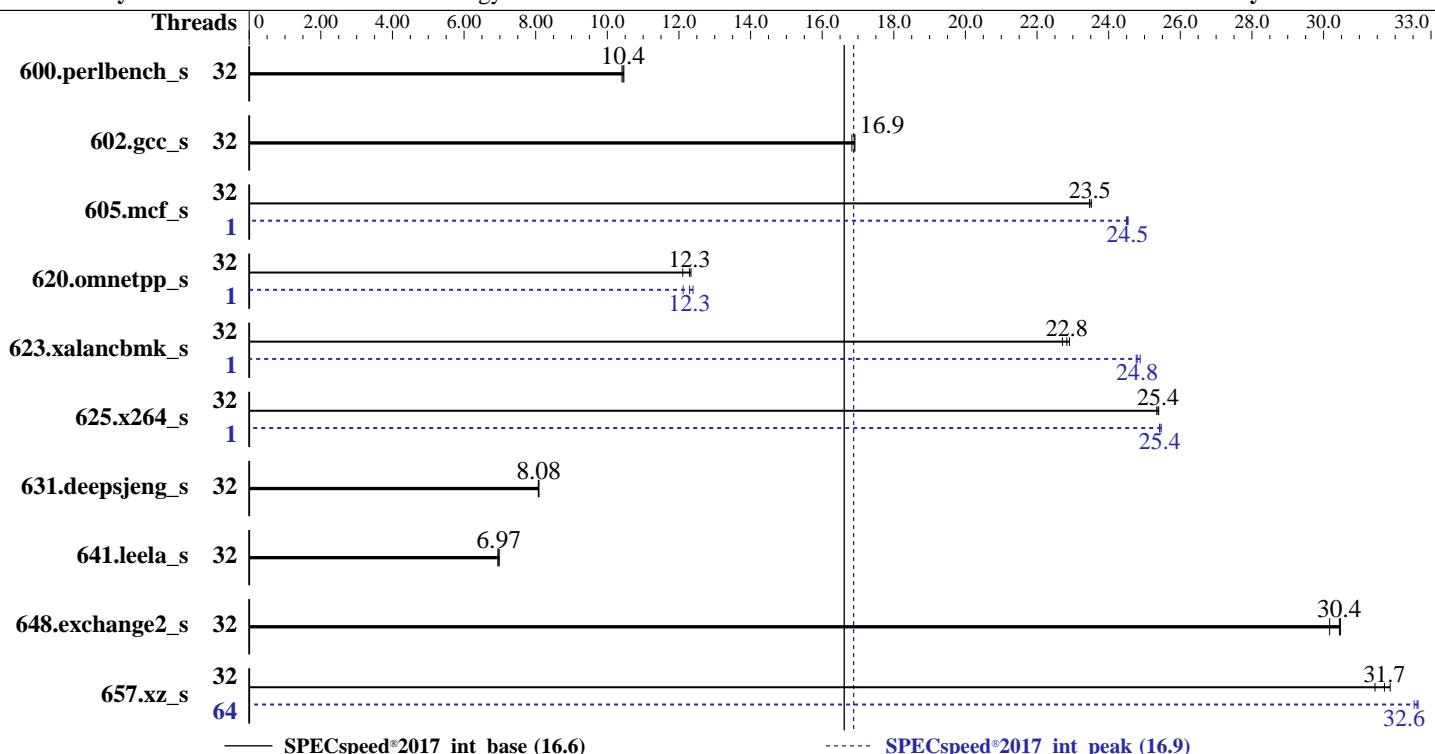
Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023



Hardware

CPU Name: AMD EPYC 9174F
Max MHz: 4400
Nominal: 4100
Enabled: 32 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 256 MB I+D on chip per chip,
32 MB shared / 2 cores
Other: None
Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-4800B-R)
Storage: 1 x 480 GB SATA SSD
Other: CPU Cooling: Air

Software

OS: SUSE Linux Enterprise Server 15 SP5
Compiler: Kernel 5.14.21-150500.53-default
Parallel: C/C++/Fortran: Version 4.0.0 of AOCC
Firmware: Yes
File System: Lenovo BIOS Version KAE117M 4.11 released Feb-2024
System State: xfs
Base Pointers: Run level 3 (multi-user)
Peak Pointers: 64-bit
Other: 64-bit
Power Management: None
BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	171	10.4	<u>170</u>	<u>10.4</u>	170	10.5	32	171	10.4	<u>170</u>	<u>10.4</u>	170	10.5
602.gcc_s	32	<u>236</u>	<u>16.9</u>	237	16.8	235	16.9	32	<u>236</u>	<u>16.9</u>	237	16.8	235	16.9
605.mcf_s	32	201	23.5	201	23.5	<u>201</u>	<u>23.5</u>	1	193	24.5	<u>193</u>	<u>24.5</u>	192	24.5
620.omnetpp_s	32	135	12.1	<u>133</u>	<u>12.3</u>	132	12.3	1	135	12.1	132	12.4	<u>133</u>	<u>12.3</u>
623.xalancbmk_s	32	61.9	22.9	<u>62.1</u>	<u>22.8</u>	62.4	22.7	1	<u>57.2</u>	<u>24.8</u>	57.2	24.8	57.0	24.9
625.x264_s	32	<u>69.5</u>	<u>25.4</u>	69.6	25.3	69.5	25.4	1	69.4	25.4	69.3	25.5	<u>69.4</u>	<u>25.4</u>
631.deepsjeng_s	32	177	8.09	<u>177</u>	<u>8.08</u>	177	8.07	32	177	8.09	<u>177</u>	<u>8.08</u>	177	8.07
641.leela_s	32	244	6.98	246	6.94	<u>245</u>	<u>6.97</u>	32	244	6.98	246	6.94	<u>245</u>	<u>6.97</u>
648.exchange2_s	32	97.4	30.2	<u>96.6</u>	<u>30.4</u>	96.5	30.5	32	97.4	30.2	<u>96.6</u>	<u>30.4</u>	96.5	30.5
657.xz_s	32	197	31.4	<u>195</u>	<u>31.7</u>	194	31.9	64	<u>190</u>	<u>32.6</u>	190	32.5	189	32.6
SPECspeed®2017_int_base = 16.6							SPECspeed®2017_int_peak = 16.9							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-63"  
LD_LIBRARY_PATH = "/home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2/amd_speed_aocc400_znver4_A_lib/lib:  
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"  
MALLOC_CONF = "oversize_threshold:0,retain:true"  
OMP_DYNAMIC = "false"  
OMP_SCHEDULE = "static"  
OMP_STACKSIZE = "128M"  
OMP_THREAD_LIMIT = "64"
```

Environment variables set by runcpu during the 605.mcf_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 620.omnetpp_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 623.xalancbmk_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 625.x264_s peak run:

```
GOMP_CPU_AFFINITY = "15"
```

Environment variables set by runcpu during the 657.xz_s peak run:

```
GOMP_CPU_AFFINITY = "0-63"
```

```
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "8"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance and then set it to Custom Mode

NUMA Nodes per Socket set to NPS4

Periodic Directory Rinse (PDR) Tuning set to Cache-Bound

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Sun Apr 21 01:49:09 2024
```

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Platform Notes (Continued)

```
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS
```

```
1. uname -a
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
01:49:09 up 2 days, 9:20, 2 users, load average: 0.89, 2.60, 2.36
USER      TTY      FROM             LOGIN@     IDLE    JCPU   PCPU WHAT
root      tty1      -           Thu16      2days  0.90s  0.08s /bin/bash ./amd_speed_aocc400_znver4_A1.sh
root      tty2      -           Thu16     16:21m  0.02s  0.02s -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size            (kbytes, -d) unlimited
scheduling priority      (-e) 0
file size                (blocks, -f) unlimited
pending signals           (-i) 6191357
max locked memory        (kbytes, -l) 2097152
max memory size          (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                 (512 bytes, -p) 8
POSIX message queues     (bytes, -q) 819200
real-time priority        (-r) 0
stack size                (kbytes, -s) unlimited
cpu time                  (seconds, -t) unlimited
max user processes        (-u) 6191357
virtual memory             (kbytes, -v) unlimited
file locks                  (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Platform Notes (Continued)

```
-bash
/bin/bash ./run_SR665V3_genoa-X_1.03.sh
/bin/bash ./Run035-compliant-amd-speedint.sh
python3 ./run_amd_speed_aocc400_znver4_A1.py
/bin/bash ./amd_speed_aocc400_znver4_A1.sh
runcpu --config amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 intspeed
runcpu --configfile amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed intspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.664/templogs/preenv.intspeed.664.0.log --lognum 664.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9174F 16-Core Processor
vendor_id       : AuthenticAMD
cpu family     : 25
model          : 17
stepping        : 1
microcode       : 0xa101144
bugs            : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size        : 3584 4K pages
cpu cores       : 16
siblings         : 32
2 physical ids (chips)
64 processors (hardware threads)
physical id 0: core ids 0-1,8-9,16-17,24-25,32-33,40-41,48-49,56-57
physical id 1: core ids 0-1,8-9,16-17,24-25,32-33,40-41,48-49,56-57
physical id 0: apicids 0-3,16-19,32-35,48-51,64-67,80-83,96-99,112-115
physical id 1: apicids 128-131,144-147,160-163,176-179,192-195,208-211,224-227,240-243
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

-----
7. lscpu
From lscpu from util-linux 2.37.4:
Architecture:           x86_64
CPU op-mode(s):         32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:              Little Endian
CPU(s):                 64
On-line CPU(s) list:   0-63
Vendor ID:              AuthenticAMD
Model name:             AMD EPYC 9174F 16-Core Processor
CPU family:             25
Model:                  17
Thread(s) per core:    2
Core(s) per socket:    16
Socket(s):              2
Stepping:               1
Frequency boost:        enabled
CPU max MHz:            4408.2998
CPU min MHz:            1500.0000
BogoMIPS:                8187.36
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                           clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                           constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
                           aperfmpf perf rapl dni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
                           x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Platform Notes (Continued)

```
extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt
tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3
cdp_l3 invpcid_single hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a avx512f
avx512dq rdseed adx smap avx512ifma clflushopt clwb avx512cd sha_ni
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc
cqm_mbm_total cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru
wbnoinvd amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassist pausefilter pfthreshold avic
v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke avx512_vbmi2
gfni vaes vpclmulqdq avx512_vnni avx512_bitalg avx512_vpopcntdq la57 rdpid
overflow_recov succor smca fsrm flush_lld
```

Virtualization:

AMD-V

L1d cache:

1 MiB (32 instances)

L1i cache:

1 MiB (32 instances)

L2 cache:

32 MiB (32 instances)

L3 cache:

512 MiB (16 instances)

NUMA node(s):

8

NUMA node0 CPU(s):

0-3,32-35

NUMA node1 CPU(s):

4-7,36-39

NUMA node2 CPU(s):

8-11,40-43

NUMA node3 CPU(s):

12-15,44-47

NUMA node4 CPU(s):

16-19,48-51

NUMA node5 CPU(s):

20-23,52-55

NUMA node6 CPU(s):

24-27,56-59

NUMA node7 CPU(s):

28-31,60-63

Vulnerability Itlb multihit:

Not affected

Vulnerability Llft:

Not affected

Vulnerability Mds:

Not affected

Vulnerability Meltdown:

Not affected

Vulnerability Mmio stale data:

Not affected

Vulnerability Retbleed:

Not affected

Vulnerability Spec store bypass:

Mitigation: Speculative Store Bypass disabled via prctl and seccomp

Vulnerability Spectre v1:

Mitigation: usercopy/swapgs barriers and __user pointer sanitization

Vulnerability Spectre v2:

Mitigation: Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling, PBRSB-eIBRS Not affected

Vulnerability Srbds:

Not affected

Vulnerability Tsx async abort:

Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	32K	1M	8	Data	1	64	1	64
L1i	32K	1M	8	Instruction	1	64	1	64
L2	1M	32M	8	Unified	2	2048	1	64
L3	32M	512M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 8 nodes (0-7)

node 0 cpus: 0-3,32-35

node 0 size: 193232 MB

node 0 free: 192997 MB

node 1 cpus: 4-7,36-39

node 1 size: 193533 MB

node 1 free: 192999 MB

node 2 cpus: 8-11,40-43

node 2 size: 193499 MB

node 2 free: 193192 MB

node 3 cpus: 12-15,44-47

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Platform Notes (Continued)

```
node 3 size: 193533 MB
node 3 free: 193026 MB
node 4 cpus: 16-19,48-51
node 4 size: 193533 MB
node 4 free: 193296 MB
node 5 cpus: 20-23,52-55
node 5 size: 193533 MB
node 5 free: 193248 MB
node 6 cpus: 24-27,56-59
node 6 size: 193533 MB
node 6 free: 193129 MB
node 7 cpus: 28-31,60-63
node 7 size: 193469 MB
node 7 free: 192657 MB
node distances:
node   0   1   2   3   4   5   6   7
 0: 10  20  20  20  20  20  20  20
 1: 20  10  20  20  20  20  20  20
 2: 20  20  10  20  20  20  20  20
 3: 20  20  20  10  20  20  20  20
 4: 20  20  20  20  10  20  20  20
 5: 20  20  20  20  20  10  20  20
 6: 20  20  20  20  20  20  10  20
 7: 20  20  20  20  20  20  20  10
```

9. /proc/meminfo
MemTotal: 1585019048 kB

10. who -r
run-level 3 Apr 18 16:30

11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
Default Target Status
multi-user running

12. Services, from systemctl list-unit-files
STATE UNIT FILES
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator
 kbdsettings klog lvm2-monitor nsqd postfix purge-kernels rollback rsyslog smartd sshd
 systemd-pstore wickedd-wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime systemd-remount-fs
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
 chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
 firewalld gpm grub2-once haveged haveged-switch-root hwloc-dump-hwdata ipmi ipmievfd
 issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind
 rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts snmpd snmptrapd
 systemd-boot-check-no-failures systemd-network-generator systemd-sysext
 systemd-time-wait-sync systemd-timesyncd
generated ntp_sync
indirect wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default
root=UUID=5c910b1f-682a-4d6d-8760-ecffd555ecb9
splash=silent

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Platform Notes (Continued)

```
mitigations=auto
quiet
security=apparmor

-----
14. cpupower frequency-info
analyzing CPU 0:
    current policy: frequency should be within 1.50 GHz and 4.10 GHz.
                    The governor "performance" may decide which speed to use
                    within this range.

    boost state support:
        Supported: yes
        Active: yes

-----
15. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space       0
vm.compaction_proactiveness    20
vm.dirty_background_bytes       0
vm.dirty_background_ratio       10
vm.dirty_bytes                  0
vm.dirty_expire_centisecs      3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                   1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

-----
16. /sys/kernel/mm/transparent_hugepage
defrag           [always] defer defer+madvise madvise never
enabled          [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag              1
max_ptes_none       511
max_ptes_shared     256
max_ptes_swap       64
pages_to_scan       4096
scan_sleep_millisecs 10000

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5

-----
19. Disk information
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Platform Notes (Continued)

SPEC is set to: /home/cpu2017-1.1.9-amd-aocc400-znver4-A1.2

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/md126p3	xfs	442G	28G	414G	7%	/

20. /sys/devices/virtual/dmi/id

Vendor:	Lenovo
Product:	ThinkSystem SR665 V3 MB,Genoa,Kauai,DDR5,Kauai,2U
Product Family:	ThinkSystem
Serial:	1234567890

21. dmidecode

Additional information from dmidecode 3.4 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

24x SK Hynix HMCG94MEBRA123N 64 GB 2 rank 4800

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor:	Lenovo
BIOS Version:	KAE117M-4.11
BIOS Date:	02/29/2024
BIOS Revision:	4.11
Firmware Revision:	2.30

Compiler Version Notes

=====

C	600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak) 625.x264_s(base, peak)
	657.xz_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

C++	620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak) 631.deepsjeng_s(base, peak)
	641.leela_s(base, peak)

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

=====

Fortran	648.exchange2_s(base, peak)
---------	-----------------------------

=====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Compiler Version Notes (Continued)

Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -futo -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lflang
-lamdalloc

C++ benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -futo

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Base Optimization Flags (Continued)

C++ benchmarks (continued):

```
-mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden -fopenmp=libomp
-lomp -lamdlibm -flang -lamdalloc-ext
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-inline-recursion=4 -Wl,-mllvm -Wl,-lsr-in-nested-loop
-Wl,-mllvm -Wl,-enable-iv-split -O3 -march=znver4 -fveclib=AMDLIBM
-ffast-math -fopenmp -flto -mllvm -optimize-strided-mem-cost
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -fopenmp=libomp
-lomp -lamdlibm -flang -lamdalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

C++ benchmarks:

```
-Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Peak Compiler Invocation

C benchmarks:

```
clang
```

C++ benchmarks:

```
clang++
```

Fortran benchmarks:

```
flang
```



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECspeed®2017_int_base = 16.6

SPECspeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

600.perlbench_s: basepeak = yes

602.gcc_s: basepeak = yes

605.mcf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-allow-multiple-definition -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto
-fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdaloc -lflang

625.x264_s: Same as 605.mcf_s

657.xz_s: Same as 605.mcf_s

C++ benchmarks:

620.omnetpp_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdaloc-ext -lflang

623.xalancbmk_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-do-block-reorder=aggressive -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -finline-aggressive -mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden
-fopenmp=libomp -lomp -lamdlibm -lamdaloc-ext -lflang

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665 V3
(4.10 GHz, AMD EPYC 9174F)

SPECSpeed®2017_int_base = 16.6

SPECSpeed®2017_int_peak = 16.9

CPU2017 License: 9017

Test Date: Apr-2024

Test Sponsor: Lenovo Global Technology

Hardware Availability: Feb-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

Peak Optimization Flags (Continued)

631.deepsjeng_s: basepeak = yes

641.leela_s: basepeak = yes

Fortran benchmarks:

648.exchange2_s: basepeak = yes

Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-U.html>
<http://www.spec.org/cpu2017/flags/aocc400-flags.2023-09-13.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-U.xml>
<http://www.spec.org/cpu2017/flags/aocc400-flags.2023-09-13.xml>

SPEC CPU and SPECSpeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-04-20 13:49:09-0400.

Report generated on 2024-05-07 22:19:16 by CPU2017 PDF formatter v6716.

Originally published on 2024-05-07.