



# OMPM2001 Result

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## Colfax Intel S5000PAL Server Board

SPECompMpeak2001 = --  
SPECompMbase2001 = 11808

SPEC license #HPG0017 | Tested by: Advanced Micro Devices | Test site: -- | Test date: Apr-2007 | Hardware AvailMar-2007 | Software AvailMar-2007

Benchmark	Reference Time	Base Runtime	Base Ratio	Peak Runtime	Peak Ratio
310.wupwise_m	6000	441	13611		
312.swim_m	6000	834	7193		
314.mgrid_m	7300	1104	6613		
316.applu_m	4000	448	8934		
318.galgel_m	5100	410	12425		
320.earthquake_m	2600	252	10330		
324.apsi_m	3400	188	18050		
326.gafort_m	8700	649	13400		
328.fma3d_m	4600	446	10308		
330.art_m	6400	243	26294		
332.ammp_m	7000	548	12783		

### Hardware

CPU: Quad-core Intel(R) Xeon(R) Processor X5355  
 CPU MHz: 2660  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32KB(I)+32KB(D)/core on chip  
 Secondary Cache: 8192KB/chip (I+D) on chip, 4 MB shared / 2 cores  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 8x1GB PC2-5300 FBDIMM (CL5)  
 Disk Subsystem: Western Digital WD2500 250GB 7200 rpm SATA  
 Other Hardware:

### Software

OpenMP Threads: 8  
 Parallel: OpenMP  
 Operating System: SLES9 SP3 64bit (2.6.5-7.244-smp)  
 Compiler: Intel(R) C Compiler for Intel(R) EM64T-based applications, Version 9.1 Build 20070109  
 Intel(R) Fortran Compiler for Intel(R) EM64T-based applications, Version 9.1 Build 20070109  
 GNU C Compiler (4.1.0)  
 File System: Linux/ext3  
 System State: Multi-User

## Notes/Tuning Information

```
Baseline optimization flags:
COPTIMIZE = -O2 -xT -ipo -openmp -ansi_alias
FOPTIMIZE = -O2 -xT -ipo -openmp
export KMP_AFFINITY=compact,0
export KMP_LIBRARY=turnaround
export KMP_STACKSIZE=31M
export OMP_DYNAMIC=FALSE
ONESTEP=yes
ulimit -s 64000
"powersave -f" used to set speed to maximum
BIOS version S5000.86B.06.00.0074
BIOS set to "Optimized Defaults" (F9)
Portability Flags:
  318.galgel_m: -FI -132
Extra Flags:
  330.art_m: -DINTS_PER_CACHELINE=32 -DDBLS_PER_CACHELINE=16
all: -gcc-name=/usr/bin/gcc
```